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the small systems journal

HAND HELD COMPUTER

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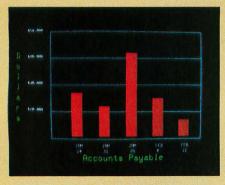
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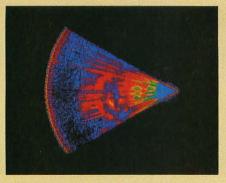


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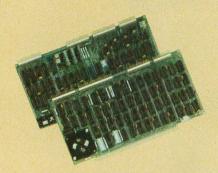
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The SDI has still more features that you should be informed about. So contact your Cromemco representative now and see all that the SDI will do for you.



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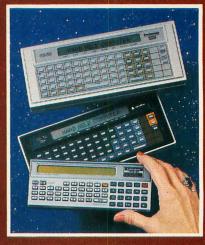
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In This Issue

This month's cover photograph by Ed Crabtree highlights three examples of a new phenomenon in the personal computer field: the HHC (hand-held computer). Shown are (from top to bottom): the Panasonic HHC; the Quasar HHC; and the Radio Shack HHC. All three units are discussed in this issue. Other articles this month describe two other miniature computers: the Sinclair ZX80 and the Hewlett-Packard HP-41C.

Elsewhere in this issue, Steve Ciarcia describes electromagnetic interference; we describe some of the exciting capabilities of Atari graphics; and we review an intriguing new Japanese computer: the NEC 8001; plus a new regular section of hardware and software reviews.

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Editorial

The Hand-Held Computer

Chris Morgan, Editor-in-Chief

There's a new trend in personal computing today—the HHC (hand-held computer). For years computer aficionados have dreamed of a computer small enough to fit in one's pocket, yet powerful enough to do the sorts of jobs that full-size microcomputers do today.

Amazingly enough, the dream is coming true. There are now no less than four models (the Radio Shack/Sharp, the Panasonic/Quasar, the Hewlett-Packard HP-41C, and the Sinclair ZX80) that fall roughly into the ultra-small computer category. One might quibble with calling the HP-41C a "computer" rather than a programmable calculator, but it has all the necessary elements to qualify: memory, processor, I/O (input/output), and a full line of peripherals. Each of these computers is discussed in this issue.

Among the new crop of HHCs, the Panasonic/Quasar (reviewed on page 34) is perhaps the most impressive in terms of engineering innovations; it sports some features that many full-size personal computers don't have, such as the ability to run for long periods from battery power alone—an impressive achievement when you realize that the unit uses, not a CMOS (complementary metal-oxide semiconductor) processor, but a standard 6502! It also has such niceties as user-definable keys, a built-in real-time clock, uninterruptible storage of user programs, and the ability to produce color images on a color television (with the addition of an optional interface unit).

The Radio Shack HHC has its own attractions, including its (relatively) low price of \$250 and its surprisingly complete BASIC interpreter. The first time I saw the Radio Shack unit was at the West Coast Computer Faire last spring, where it was being shown in its original form from Sharp. I was intrigued, but I quickly concluded it was just a passing fad. Not until I used the computer at length did I begin to realize its potential. Here was a machine capable of running complex BASIC programs—and it was truly portable! (I have to admit that a lot of the fun connected with these units is taking them out of one's pocket and showing them to noncomputer people.)

What about the practical considerations of typing programs on such a tiny keyboard? Well, at first it felt awkward, but I quickly adjusted to it. (The Panasonic/Quasar is a bit better in this regard, because the keys are spaced more widely apart.)

Speaking of attractive prices, the Sinclair ZX80, for \$200 or so, has its own appeal. Strictly speaking, it's not a hand-held computer because it uses a separate AC adapter. Still, it's tiny and can be easily transported. It has become an overnight sensation in England. As our review on page 94 points out, the ZX80 has some bad characteristics, such as screen blankout during execution of programs. Even so, a student or other beginner in computer programming could learn a lot with this machine in conjunction with its introductory BASIC book (included in the purchase price), which seems to be very good.

Why all the sudden interest in miniaturization? In part, it's the logical culmination of the never-ending battle to put more and more capability into less and less space. Combine that with the recent Japanese trend toward miniature hi-fi components, and you begin to see the driving forces involved.



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Editorial_

The Japanese are going to continue to assert themselves in the personal-computer market with both large and small personal computers. Seiko is rumored to be working on a hand-held computer to be released later this year—and that will be just the beginning, our sources tell us. Interestingly, Commodore had until recently been planning to market a hand-held computer, but abandoned the plan to concentrate on the new VIC 20 color computer. (We saw this \$299 (!) unit recently, and will be reporting on it soon. The color quality is remarkable for the price.) Look for additional entries into the hand-held-computer market from US companies later this year.

Miniature Intelligent Terminals

One of the most important trends now going on behind the scenes is the pocket-size intelligent terminal being developed by Bob Doyle and Jeff Rochliss. The unit, called the Microterminal, will be battery operated and the size of a pocket calculator. It will contain an intelligent terminal with single-line liquid-crystal display, a modem, a repertory dialer, and a printer. With this unit (which will probably retail for under \$300), the user can plug into any modular phone jack and access data bases all around the country, pay bills, get news, send and receive messages, and so on. The implications of this technology are enormous. We'll have a full report on this unit in an upcoming issue of BYTE.

Our New Look

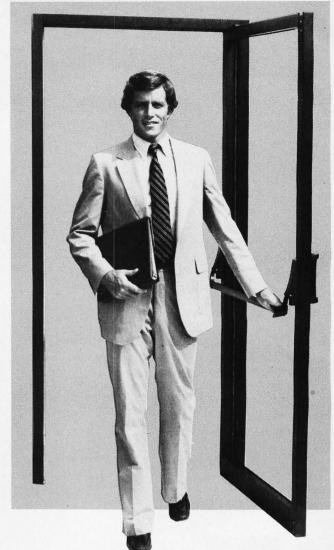
You may have already noticed some of the layout and design changes in this issue of BYTE. It's all part of our continuing effort to make the magazine easier to read and more useful to our readers. The major change is the addition of a new section in the magazine devoted to hardware and software reviews. This is in response to our reader surveys that show your increasing interest in the many new products flooding the market. This new section will give you a variety of unbiased, detailed reviews each month.

We have redesigned the table-of-contents, or "In The Queue," page to make room for the additional new material. We have *not* decreased the number of articles. They will continue to be the mainstay of BYTE, as will the many popular features in the "Nucleus" section. We have

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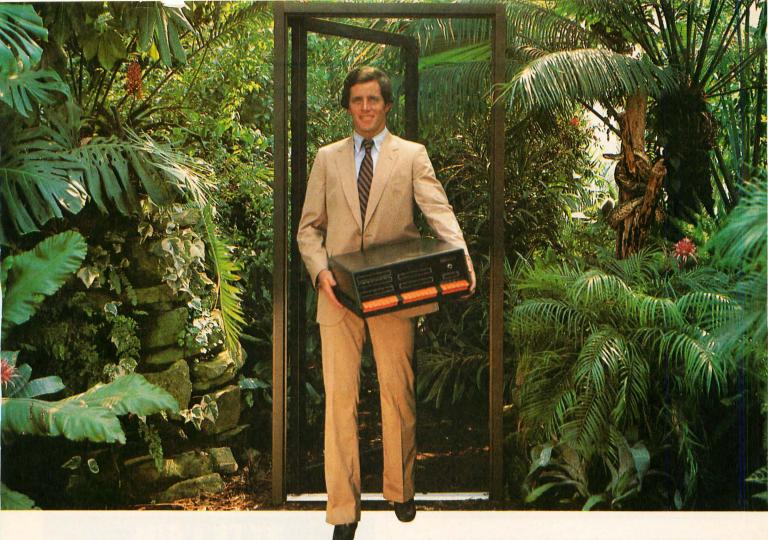
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eliminated the "Background" and "Foreground" designations because we have encountered many good articles

that don't fit either category. We invite your comments, pro or con.

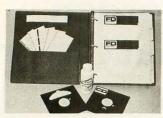
The November Cover

Much mail has come in requesting further information on our November cover. It's actually a "still," one of many extraordinary images from "The Works," a 90-minute fully computer-generated feature film. This science-fiction film is currently in production at the Computer Graphics Laboratory of the New York Institute of Technology in Old Westbury, Long Island, New York. The laboratory staff consists of a large number of exceptionally talented artists and engineers with extensive backgrounds in film-making, computer science, mathematics, and digital audio.

The digital-animation systems are state-of-the-art, using many Digital Equipment Corporation computers that have been interfaced to frame buffers. The contents of the frame buffers are recorded onto 35 mm movie film with high precision. The film will be in production for the next two years. Judging from what I have seen, it should be sensational. We thank the New York Institute of Technology for allowing us to see their work in progress. We hope to report on their graphics activities sometime soon in BYTE.



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own bank news and daily information on savings and deposit rates. Customers of the bank are issued a security pack and certificates that can be redeemed at Radio Shack outlets. In addition to the financial services, customers can use the TRS-80 Color Computer for home entertainment, education, security, message services, electronic filing, and as an electronic mail service. For details, contact Tom Sudman, c/o the United American Bank, in Knoxville, Tennesee, (615) 971-2121; David Beckerman, c/o the Tandy Corporation, Ft Worth, Texas, (817) 390-3273; or Richard Baker, c/o CompuServe, Columbus, Ohio, (614) 457-8600. ■

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If you have an Apple* and you want to interface it with parallel and serial devices, we have a board for you that will do both. It's the AIO™

Serial Interface.

The RS-232 standard assures maximum compatibility with a variety of serial devices. For example, with the AIO you can connect your Apple* to a video terminal to get 80 characters per line instead of 40, a modem to use time-sharing services, or a printer for hard copy. The serial interface is software programmable, features three handshaking lines, and includes a rotary switch to select from 7 standard baud rates. On-board firmware provides a powerful driver routine so you won't need to write any software to utilize the interface.



This interface can be used to connect your Apple* to a variety of parallel printers. The programmable I/O ports have enough lines to handle two printers simultaneously with handshaking control. The users manual includes a software listing for controlling parallel printers or, if you prefer, a parallel driver routine is available in firmware as an option. And printing is only one application for this general purpose parallel interface.



The AIO is the only board on the market that can interface the Apple to both serial and parallel devices. It can even do both at the same time. That's the kind of innovative design and solid value that's been going into SSM products since the beginning of personal computing. The AIO comes complete with serial PROM's, serial and parallel cables, and complete documentation including software listings.

See the AIO at your local computer store or contact



Maybe we can save you a call.

Many people have called with the same questions about the AIO. We'll answer those and a few more here.

Q: Does the AIO have hardware handshaking? A: Yes. The serial port accommodates 3 types—RTS CTS, and DCD. The parallel port handles ACK, ACK, BSY, STB, and STB.

Q: What equipment can be used with the AIO?

A: A partial list of devices that have actually been tested with the AIO includes: IDS 440 Paper Tiger, Centronics 779, Qume Sprint 5, NEC Spinwriter, Comprint, Heathkit H14, IDS 125, IDS 225, Hazeltine 1500, Lear Siegler ADM-3, DTC 300, AJ 841.

Q: Does the AIO work with Pascal?

A: Yes. The current AIO serial firmware works great with Pascal. If you want to run the parallel port, or both the serial and parallel ports with Pascal, order our "Pascal Patcher Disk".

Q: What kind of firmware option is available for the parallel interface?

A: Two PROM's that the user installs on the AIO card in place of the Serial Firmware PROM's provide: Variable margins, Variable page length, Variable indentations, and Auto-line-feed on carriage

Q: How do I interface my new printer to my Apple . using my AIO card?

A: Interconnection diagrams for many popular printers and other devices are contained in the AIO Manual. If your printer is not mentioned, please contact SSM's Technical Support Dept. and they will help you with the proper connections.

Q: I want to use my Apple as a dumb terminal with a modem on a timesharing service like The Source. Can I do that with the AIO?

A: Yes. A "Dumb Terminal Routine" is listed in the AIO Manual. It provides for full and half duplex, and also checks for presence of a carrier.

Q: What length cables are provided? A: For the serial port, a 12 inch ribbon cable with a DB-25 socket on the user end is supplied. For the parallel port, a 72 inch ribbon cable with an unterminated user end is provided. Other cables are available on special volume orders.

The AIO is just one of several boards for the Apple that SSM will be introducing over the next year. We are also receptive to developing products to meet special OEM requirements. So please contact us if you have a need and there is nothing available to meet it.



SSM Microcomputer Products 2190 Paragon Drive San Jose, California 95131 (408) 946-7400

Letters

Send + More = Code

I certainly enjoyed Peter Frey's article "Machine Problem Solving, Part 2" (see the October 1980 BYTE, page 266), which concerned directed search using cryptarithmetic. Unfortunately the program does not do quite all that it is advertised to do, probably due to omissions in the press copy.

For example, on page 268 Mr Frey stated, "It is also necessary to prepare the machine with the knowledge that blank spaces which precede letters in the first two rows should be treated as zeros." Program lines 270 and 280, however, can never be executed because of the branch instruction in line 210, which bypasses lines 270 and 280 completely. As a result, problems such as 'SPEND+MORE=MONEY" cannot be solved, and an error message is generated. Changing the branch instructions at line 210 to cause a jump to line 270, instead of line 300, eliminates this problem, as long as the short word is not more than one letter less than the other word.

A second malfunction occurs in problems of the "SEND+MORE=MONEY" type: when the sum word contains one more letter than the addends and also is a unique letter (such as in "SEND +MORE=HONEY"). The program recognizes the patterns and alters the array correctly, but the value for that letter is not displayed on the screen. A short statement immediately after a successful pattern search, such as:

415 PRINT @ 762+6*NL, 1

seems to correct this error.

K W Butcher Canton ME 04221

Mr Butcher's comments are correct. We appreciate the feedback....CM

Software for the Altos

I read with great interest Mark Dahmke's article in the November 1980 BYTE concerning the Altos machine. (See "The Altos ACS 8000 Single-Board Computer," page 158.) I agree with Mr Dahmke's assessment of the Altos as a well-designed and reliable machine. I was especially interested, however, in his comments on the available software for the Altos.

I represent Avtek Inc, the software house that wrote APULIB and the bisynchronous and asynchronous communications packages for the Altos machine mentioned in the article. The software picture for the Altos is not really as grim as the article makes it appear. Avtek has written many other software packages for the Altos. Among them:

• OPRA—A enhancement to the CP/M operating system. It increases diskstorage capacity by 40%, disk-I/O (input/output) speed by a factor of 2, it supports a type-ahead buffer, and it provides for easy mixed-mode operation.

• Communications Packages—In addition to the full IBM 2780/3780 bisvnchronous and asynchronous packages I already mentioned, there is a synchronous communications package for Altos-to-Altos use. Incidentally, the price of the bisync package has been lowered to \$495.

• GRAFLIB-A two- and threedimensional graphics-subroutine library for use with the Altos and a modified Lear-Siegler ADM-3A terminal (512 by 256 resolution), a Diablo 1650 printer, and a multicolor plotter.

• Graphics and Scientific System-A complete system for the Altos and the modified ADM-3A that contains Avtek's own screen-oriented editor, a scientificpaper typesetting package, and many stand-alone and subroutine packages for graphics and for the solution of specialized scientific and mathematical problems. This system also supports the Diablo 1650 printer, for graphics and manuscripts, etc, and multicolor plotters.

In addition to those packages, Avtek has plans for several others, including a financial modeling package. I think that the software that Avtek supplies makes the Altos a very versatile and useful machine. In fact, it turns the Altos into a system.

John C Theys President Advanced Computational Technology Inc 30 Side Cut Rd West Redding CT 06896

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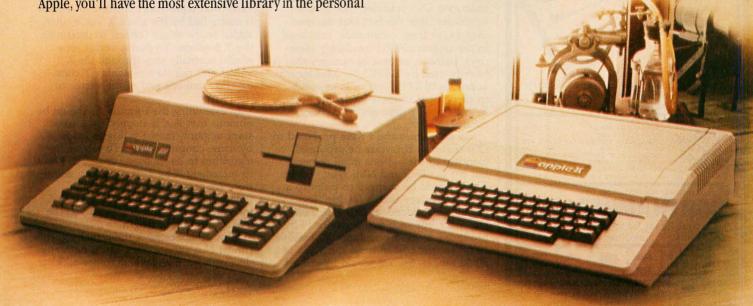
There's even a series of utility programs called the DOS Tool Kit that not only lets you design high-resolution graphic displays, but lets you work wonders with creative animation.

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history pass you by. Visit your nearest Apple dealer or call 800-538-9696. In California, 800-662-9238. Or write: Apple Computer, 10260 Bandley Drive, Cupertino, CA 95014.







Letters.

68000 At Last?

In looking over a recent issue of BYTE, I came across a section titled "BYTELINES" that contained references to the MC68000. (See "68000, Where Art Thou?" September 1980 BYTE, page 164.) The message that I got from reading the commentary was that the MC68000 is still in the experimental stage. This is untrue! All unreserved op codes have been defined, and the instruction set has been frozen since January 1980. The second point is that we have been shipping the 68000 in large quantities for some time now. We have no problem committing to delivery on large-production quantities.

Since those comments were based on customer inputs, I can understand some confusion. I hope that this letter will help to resolve it.

Steve Sparks Manager Marketing and Applications Motorola Inc 3501 Ed Bluestein Blvd Austin TX 78721

Sol Libes Replies:

The column in question was written some time ago. At that time, two OEMs (original equipment manufacturers) that wanted to use the 68000 reported to me that they were still not able to go into production on planned products because Motorola still had not completed the 68000's design and would not fill production orders. In other words, the facts as I reported them were true at the time. I understand that Motorola is now shipping production quantities.

A System Note

One problem with OSI (Ohio Scientific) systems (most notably the C-2) has been the inability to utilize the 6502 IRQ and NMI commands from a BASIC program, via USR routines. The problem originates from the fact that the reset vectors for these commands, contained in the system's ROM (read-only memory), point to an area of memory that is heavily used by BASIC (ie: hexadecimal addresses 01XX). Thus, it is impossible to field either of these interrupts because BASIC rapidly destroys any service routine.

My colleagues and I have proposed to OSI that new firmware be produced, identical to the old one in all respects but for the IRQ and NMI reset vectors. These would be changed to point to a part of memory that is "stable" (eg: hexadecimal addresses D0XX or E0XX). However, for such a new device to be produced, it must be financially feasible to do so (the cost to be in the \$0.25 to \$0.50 range). So, we would like to ask

all interested OSI users to drop a quick note to Ohio Scientific expressing interest:

Ohio Scientific Computers Attn: Customer Relations 1333 S Chillicothe Rd Aurora OH 44202

If enough replies are received, all of us may well see a new monitor device.

Thanks so much!

Shaun D Black University of Michigan Department of Biological Chemistry 5440 Medical Sciences I Ann Arbor MI 48109

Intercepting Raster

I very much enjoyed John Beetem's article entitled "Vector Graphics for Raster Displays." (See the October 1980 BYTE, page 286.) To say the least, I found it a unique method. However, I must take exception to one statement that was made regarding techniques for plotting vectors.

In referring to the slope-intercept and trigonometric methods of calculation, Mr Beetem states, "None of these is very good for a small computer, because many slow multiplications and divisions are needed." This is simply not true, at least not in the case of the slope-intercept method. (Note: In the following discussion, for simplicity, it will be assumed that the X length is greater than the Y length. If this is not the case, the X and Y values should be swapped; the program under discussion handles the data in approximately this way.)

The formula used in the common implementation of the slope-intercept method is Y = MX + B, where M = (Y2 - Y1)/(X2 - X1) and $B = Y2 - (X2 \times M)$. In other words, the value that represents the slope of the line is multiplied by the given X value, then added to the origin (offset) to determine the Y position. To plot a vector, one would normally step through the X values and calculate matching Y coordinates from one end of the vector to the other.

In examining the formula, it should be obvious that if X is stepped by a constant amount, then Y will also increase by some constant value. To reduce the algorithm to its simplest form, it is best to increment X by 1 (because, by definition, we cannot plot any fractional points). One can, therefore, find the Y increment value simply by dividing the Y length by the X length.

How complicated is the actual algorithm? Not very. Unitek Ltd is currently developing a high-level graphics package



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Letters.

for a commercial graphics product, and the vector routine uses this method. The division itself encompasses only fifteen instructions (30 bytes), and need be done only once, which is before the actual write loop is entered. The loop proper contains only an X increment instruction, a double-precision add (two instructions) for the Y increment, the actual write-routine call, and a simple test for end-of-vector. Since Mr Beetem is using an 8080 and Unitek's system is 6800-based, a speed comparison would be worthless. Suffice to say that the routine actually calculates the vector faster than the hardware can plot the points.

To show the simplicity of the algorithm, here is a minimal representation.

- 1. Find the lengths of the X and Y components of the vector.
- 2. Divide the Y length by the X length.
- 3. Set location to X, Y origin.
- 4. Set the *X* increment to 1.
- 5. Set the Y increment to the result of the division.
- 6. Set the Y fraction register to hexadecimal 80 (1/2 for round-up).
- 7. Plot the location.
- 8. If location is end-of-vector, stop.
- 9. Increment X.
- 10. Add the Y increment to the Y fraction register.
- 11. If an overflow occurs, increment Y. 12. Go to 7.

As can be seen, the algorithm is rather simple, and uses no complex mathematics in the loop.

It turns out that this method solves a

particularly knotty problem that crops up in other variations (especially in a parametric line representation). When vectors approach angles that are multiples of 45° (ie: the X length nears the Y length), varying overflow rates in the two variables cause undesired excursions away from the actual vector. This creates a rough section about the points where steps would normally occur. Incrementing one of the variables by 1 eliminates any possibility of variable overflow and results in a very smooth vector.

I found Mr Beetem's logic interesting and informative; had I considered this method of drawing vectors when we at Unitek were designing our graphics package, I probably would have discarded it without careful examination, believing it too slow and complex. Mr Beetem has proven this not to be so. Perhaps the same thing happened when Mr Beetem was writing his routine. He too may have considered the slope-intercept method briefly, but discarded it, without closer examination, as being too clumsy. (Alas, it always seems that the algorithm one discards later turns out to be the variation with the greatest potential....) In this case, it happened for the best; otherwise, we would not have Mr Beetem's method to consider. I do not in any way intend to detract from his approach; merely to indicate that the slope-intercept is also a viable method for microcomputers.

Richard H Rae, CET Unitek Ltd POB 671 Emporia VA 23847

Fewer Resistors = Same Resistance

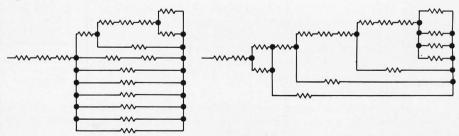
In the August 1980 BYTE, W Lloyd Milligan shows a network of twenty-six 1-ohm resistors (see "Letters," page 20) that he believes is the smallest network whose value is very close to π (pi). However, by using the same continuedfraction principle with only six parallelconnected resistors, a solution with a total of only eighteen resistors is shown in figure 1. Alas, I have been unable to

find any network that starts with three in series with fewer resistors; starting with two in series, there is another solution with eighteen. All of these differ from π by about one part in four million. They all have the value 355/113.

Can anyone find a solution with seventeen or fewer?

John Fitzallen Moore 714 E Birch Rd Lake Bluff IL 60044

Figure 1



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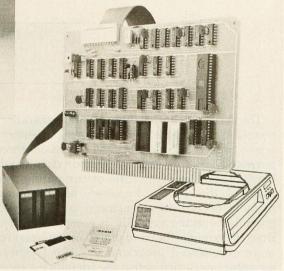
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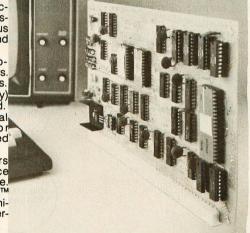
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An Introduction to Atari Graphics

Chris Crawford and Lane Winner Atari Inc 1272 Borregas Ave Sunnyvale CA 94086

The Atari 400 and 800 are second-generation personal computers. In addition to the normal memory and processor integrated circuits, they contain three specialpurpose LSI (large-scale integrated) circuits which make them capable of many feats of computing legerdemain. Most of this power, however, lies brooding beneath many layers of "human engineering." The beginning programmer working in BASIC is paternalistically protected from the complexities and power of the beast within. The more experienced programmer seeking cybernetic high adventure must first defeat the friendliness engineered into the machine to unleash its throbbing brute power. Without help, this can be most difficult. We will act as native guides for one region of this complex machine: the display list. We will show you how to generate flashy displays by creating you own display list and redefining the character set.

Display-List Fundamentals

Most personal computers use a straightforward memory-mapped display in which the screen format is fixed and each screen pixel's (picture element's) contents are provided by a specific location in memory. This is a simple scheme demanding little of either the programmer or the computer. The Atari 400/800 uses a more complex scheme involving a display list and display data. A display list is a sequence of commands that defines the vertical format of the video display; the display data is the information to be displayed.

The Atari 400/800 display list is actually a small pro-

gram; it is processed by a special LSI circuit called ANTIC. ANTIC is a dedicated microprocessor whose sole function is to control the video display. ANTIC uses a process called DMA (direct memory access) to gain access to the display list and display data. The display list and display data are stored by the high-speed (1.8 MHz) 6502 microprocessor. When the BASIC programmer types GRAPHICS n, the operating system writes a complete display list into memory and clears the display data. The information flow for this process is diagrammed in figure 1. Clearly, the adventurous programmer who bypasses BASIC and writes his or her own display list will have more direct control over the screen.

Associated with the display list are the concepts of a graphics mode and a graphics-mode line. The Atari 400/800 supports fourteen fundamental graphics modes, only nine of which are directly accessible from BASIC. The first six modes (three of which are accessible from BASIC) are character modes which display characters in different combinations of size and color. The remaining eight graphics modes display squares of color in different resolution and color combinations. A graphics-mode line is a group of horizontal-scan lines which are treated as a unit for display purposes. (A horizontal-scan line is a single sweep of the electron beam across the television screen. There are 192 horizontal-scan lines in the visible area of the screen.) A graphics-mode line will contain between one and sixteen horizontal-scan lines, depending on the graphics mode involved. A graphics-mode line stretches horizontally all the way across the screen (you

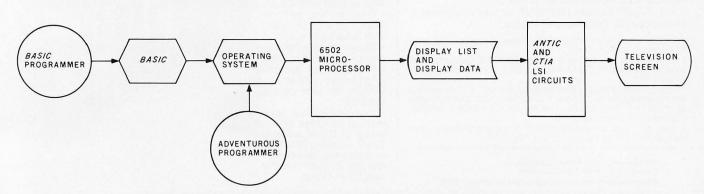


Figure 1: Information flow for Atari 400/800 display. The adventurous programmer who bypasses BASIC gains more control over the display list and display data, and thus is able to customize the displayed image to a greater extent.

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 cannot change graphics modes halfway across the screen). The video display is thus organized as a vertical sequence of mode lines of varying height and contents. There are many thousands of possible sequences of mode lines on the screen; BASIC restricts the programmer to seventeen such sequences. Each such sequence is referred to in the BASIC manual as a graphics mode.

Display-List Details

The display list and the display data normally reside at the top of available memory-address space. Since the amount of available memory is not fixed, the operating system must keep track of the address of the display list. The address of the beginning of the list is stored in decimal addresses 560 and 561. The first 3 bytes in the display list skip twenty-four blank scan lines, which is necessary to defeat the vertical overscan of many television sets. The next byte is called the LMS (load memory scan) byte. It defines the first mode line of the display and also instructs ANTIC that the following 2 bytes give the address at which display data can be found. Since we rarely need to tamper with these first 4 bytes, we will start with the fifth byte, whose address we will assign to a BASIC variable called START. The value of START can be calculated by:

START = PEEK(560) + 256 * PEEK(561) + 4

The bytes at this location and the succeeding location give the starting address of the display data. Beginning at location START+2 is a sequence of mode bytes which specify the mode lines for the display. The codes for these mode bytes are found in table 1. The programmer has the freedom to create any sequence of mode bytes for the display list. The programmer also has the responsibility to insure that the chosen sequence includes exactly 192 horizontal-scan lines. At the end of the mode-byte sequence, the programmer must place an ANTIC JUMP byte (decimal 65) followed by the low- and high-order address bytes of the beginning of the display list—four bytes lower in memory than the location we refer to as START.

The starting address of the display data, which we will assign to a BASIC variable called MEMST, can be calculated from:

MEMST = PEEK(START) + 256 * PEEK(START + 1)

The display data is simply strung together in sequence; this can cause a headache when mixing modes. Since different mode lines require different numbers of displaydata bytes, the programmer wishing to change a displaydata byte must calculate its position in display-data memory by adding up the space requirements of each previous mode line. The BASIC POSITION and PLOT commands work reliably only with the homogeneous display lists used by BASIC, so the programmer who mixes modes must expend greater effort to use such a specialized display.

A Real Display List

We shall now illustrate these principles with a sample program and its resultant display, display list, and display data. The program is a straightforward affair which plots the BYTE logo in graphics mode 7+16. The pro-

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Mode	Remark	Left 4 Bits	Right 4 Bits	Color Dots Per Pixel	Scan Lines Per Mode Line	Number of Colors	BASIC Mode	Bytes Per Line
character	1	0	2	1/2	8	11/2	0	40
character	1	0	3	1/2	10	11/2	_	40
character	1	0	4	1	8	4	_	40
character	1	0	5	1	16	4	T. T	40
character	1	0	6	1	8	5	1	20
character	1	0	7	1	16	5	2	20
character	1	0	8	4	8	4	3	10
character	1	0	9	2	4	2	4	10
graphic	1	0	A	2	4	4	5	20
graphic	1	0	В	1	2	2	6	20
graphic	1	0	C	1	1	2		20
graphic		0	D	1	Ž	4	7	40
graphic	1	0	E	1	1	4	_	40
graphic	1	0	F	1/2	1	11/2	8	40
special	2	0-7	0	Blank				
special	3	4	1	JUMP	-	-	·	_

Table 1: Interpretation of the graphics-mode-byte codes. Remarks are as follows:

1. The left nybble of the very first mode byte of the display list must be changed from 0 to 4.

2. The blank mode is used to output a selected number of blank background lines.

3. The JUMP instruction causes the ANTIC graphics processor to recognize the end of the display list and return to the beginning of the list, waiting for vertical blanking to occur so it can proceed with another frame. Where 1½ colors are indicated, the hue of the foreground color cannot be controlled.

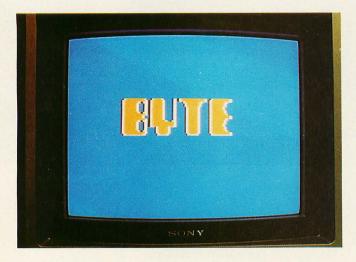


Photo 1: The BYTE logo as displayed by the Atari 400/800 running the program of listing 1. See table 2 for details.

gram is presented in listing 1 (page 24), and the display it produces is shown in photo 1. Figure 2a and table 2a show the display list for this display. Since this is a standard BASIC graphics-mode display list, it is neat and tidy.

Tampering With the Display List

With the formal goal of improving the display and the heuristic goal of demonstrating display-list manipulations from BASIC, we shall now tamper with this display list. The first step in this process is to prepare our proposed display list on paper. The desired screen format is shown in figure 2b.

We must consult table 3 to determine which of the display modes will require the greatest amount of memory space. In our case, we are using modes 0, 1, 2, and 7; mode 7 is clearly the most memory-intensive mode. We shall therefore start with mode 7 and modify the mode-7 display list. It is always easier to pare down an oversized display list than to build up an undersized one.

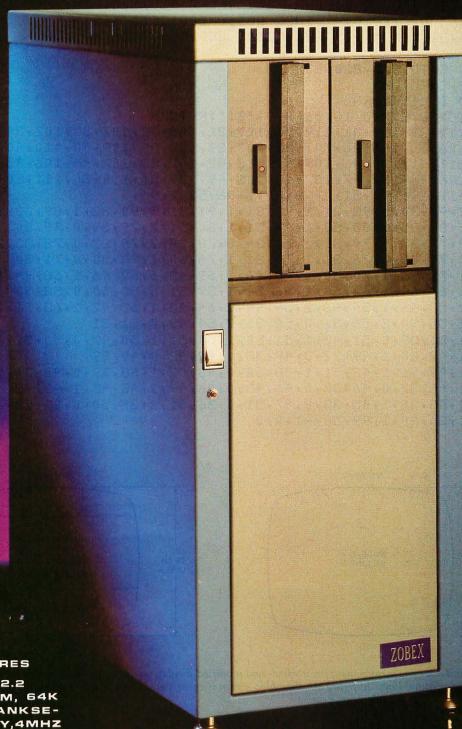
Next, we must verify that our proposed display list does indeed produce 192 horizontal-scan lines. Consult table 1 to find the number of scan lines per mode line. Our calculation produces the following results:

Mode	Number of Mode Lines	Scan Lines Per Mode Line	Total Scan Lines			
0	1	8	8			
1	4	8	32			
2	4	16	64			
7	44	2	88			
			_			
			192 Tota	ıl		

We now determine the mode bytes for each of the mode lines by looking them up in table 1. It is handy to convert these to decimal for later use. Our results are:

Mode	Hexadecimal Mode Byte	Decimal Mode Byte			
0	02	2			
1	06	6			
2	07	7			
7	0D	13			

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Listing 1: Atari 400/800 program to plot the BYTE logo, shown in photo 1. See table 2 on page 26 for details.

```
100 GRAPHICS 7:COLOR 1:POKE 765,1:POKE 710,128:POKE 712,128
110 A=0:READ B,C:IF B>-1 THEN GOSUB 800:GOTO 110
120 READ A,B,C:IF A>-1 THEN GOSUB 800:GOTO 120
130 END
800 ON A+1 GOTO 810,820,830
BIO PLOT B, C: RETURN
820 DRAWTO B,C:RETURN
BBC POSITION B,C:XIO 18,#6,0,0,"S:":RETURN
900 DATA 111,30,111,31,110,31,109,31,108,32,107,33,107,34
905 DATA 106,35,106,36,107,37,107,38,108,39,109,40,110,40,111,40
910 DATA 111,41,110,41,109,41,108,42,107,43,107,44,106,45
915 DATA 106,46,107,47,107,48,108,49,109,50,110,50,111,50,111,51
920 DATA -1,-1,1,97,51,2,96,50,1,96,50,2,96,31
925 DATA 2,97,30,0,93,31,1,92,31,1,91,31,1,90,32,1,89,33,1,89,34
930 DATA 1,88,35,1,88,50,1,87,51,1,80,51,2,79,50,0,79,50
935 DATA 2,79,35,0,79,35,2,78,34,0,78,34,2,78,33,0,78,33,2,77,32
940
    DATA 0,77,32,2,76,31,1,74,31,1,74,30,1,93,30,0,71,30
945 DATA 1,71,46,1,70,30,1,70,46,1,69,43,1,69,46,1,68,44,1,68,46
950 DATA 1,67,44,1,67,50,1,66,51,1,59,51,2,58,50,0,58,50
955 DATA 2,58,46,1,54,46,2,54,44,1,64,43,1,63,42,1,63,31,1,62,30
960 DATA 1,55,30,2,54,31,0,54,31,2,54,43,0,51,31
965 DATA 1,51,39,0,51,42,1,51,50,1,50,51,1,50,42,0,50,39,1,50,30
970 DATA 1,49,30,1,49,32,0,49,33,1,49,43,0,49,49
975 DATA 1,49,51,1,48,51,1,48,50,0,48,42,1,48,39,0,48,31,1,48,30
980 DATA 1,46,32,0,46,38,0,46,43,0,46,49,1,45,48,1,45,48
985 DATA 0,45,38,1,45,33,0,47,51,1,36,51,2,35,50,0,35,50,2,35,50,2
990 DATA 1,36,30,1,49,30,-1,0,0
```

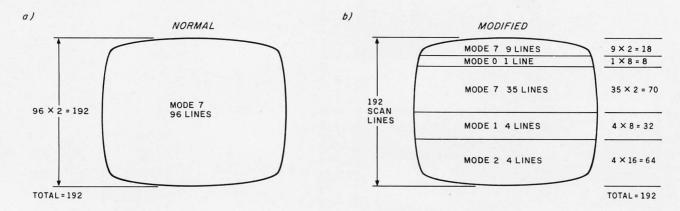


Figure 2: Horizontal-scan line arrangement for normal- and modified-display screens. The video screen in figure 2a is composed completely of mode-7 horizontal lines. In figure 2b, the video screen is constructed from multiple-mode sections that allow a mix of images to be displayed. Refer to table 2 for details.

The results of this paperwork are presented in table 2b. Now, at last, we are ready to write some code. Please

Now, at last, we are ready to write some code. Please refer to listing 2 on pages 28 and 30 in conjunction with this narrative. We begin by checking to see that there is enough memory available to reposition the display list (line 0). If there isn't enough, the program aborts. We then move the top of available memory down by 4 K bytes and execute a GRAPHICS call (line 20) to write a

new display list and display data in memory. This procedure reserves 4 K bytes of memory for our own use later on. We then define our display strings (lines 30 and 40) and execute another GRAPHICS call to initialize our display list—which we shall subsequently modify. The series of POKEs in lines 50 and 55 define the colors we will be using and turn off the character display while we redefine our characters.

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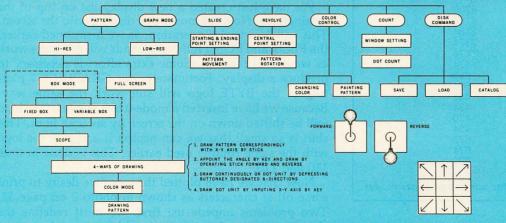
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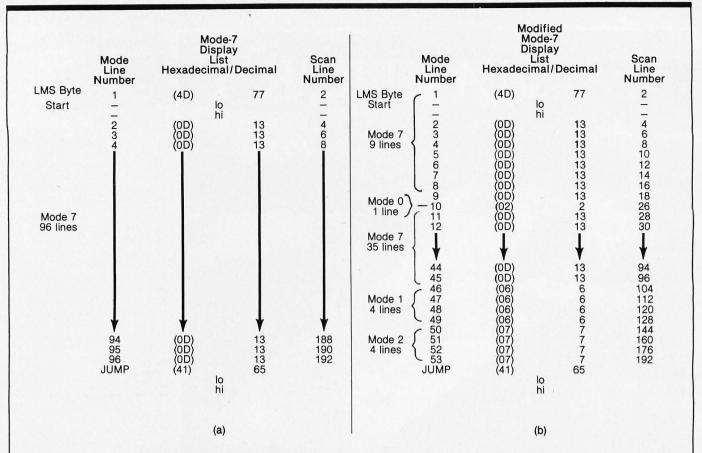


Table 2: Normal- and modified-display lists correspond to the lines displayed in figure 2. The program corresponding to table 2a is given in listing 1, and the actual display is pictured in photo 1. Listing 2 corresponds to table 2b.

The state of the s		THE RESERVE OF THE PERSON NAMED IN COLUMN 2 IS NOT THE OWNER.
Mode	8 + 16	8138 Bytes 8112
	8 <u>7</u> + 16	4200
	7	4190
	6 + 16 6 + 16 5 + 16 5 + 16 4 + 16 4 + 16 3 + 16 2 + 16	2184
	6	2174
	5 + 16	1176
	5	1174
	4 + 16	696
	4	694
	3 + 16	432
	3	434
	2 + 16	420
	2	424
	1 + 16	672
	1	674
	0	992

Table 3: Memory requirements for various graphics modes.

We then calculate the variable START in line 60. In lines 70 thru 90, we POKE the new and different mode bytes into the display list to create our new display list. The offsets from START (the numbers added to START) are simply the mode-line numbers for the new mode lines. Thus, the offset in line 70 is 10 because the mode byte we are POKEing is for the tenth mode line from the top of the screen. (Remember, a mode line is not the same as a scan line.) In line 95, we POKE the ANTIC JUMP byte and the jump-address bytes at the end of our new

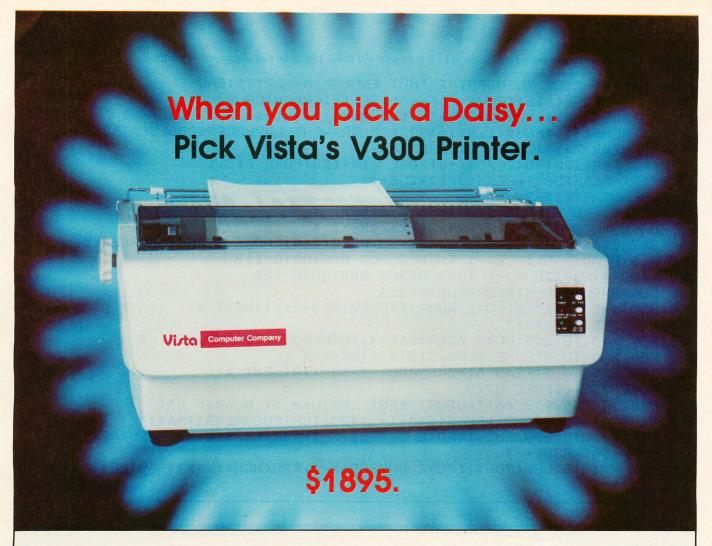
display list. The value of the jump-address bytes points to the beginning of the display list and can be found in locations 560 and 561.

We have just created a new display list on top of the original one. Now we must put a display onto the screen. This will be a tricky operation; as we mentioned earlier, the PLOT and POSITION commands will not quite work as we expect them to. Some extra effort is necessary to produce a display. Fortunately, our GRAPHICS 7 plotting of the BYTE logo will still work the same way. Because we have inserted a mode-0 line above it, the logo will be shifted down on the screen by six scan lines. This shift is so small that we can neglect it and plot the logo with the same routine used earlier. This is done in lines 110 and 120.

Now that we have plotted the logo, we desire to print some other characters as shown in photo 2 on page 32. Two problems impede us: first, we must redefine the character set to mix uppercase and lowercase characters; second, we must calculate where these characters go.

The first problem arises from the natural limitations of an 8-bit processor. If four colors are supported (as in GRAPHICS 1 and 2), only 64 distinct characters can be displayed in each color. This is because 2 bits are required to specify the color, leaving only 6 bits to specify the character. This restricts our available set; the Atari character set in ROM (read-only memory) supplies uppercase and punctuation or lowercase and graphics symbols, but

Text continued on page 32



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Listing 2: Atari 400/800 program to plot the BYTE logo and the other characters as displayed in photo 2.

O IF FRE(O)<5825 THEN PRINT "NOT ENOUGH MEMORY!": END 20 RAMTOP=PEEK(106):POKE 106,RAMTOP-16:GRAPHICS 0 30 DIM SML\$(32):SML\$(1,32)=" the small systems 40 DIM MGH\$(41):MGH\$(1,41)="@@@A@McGRAW@HILL@@@@@@@@PUBLICATION" 50 GRAPHICS 7+16:COLOR 2:POKE 765,2 55 POKE 708,128:POKE 709,40:POKE 710,128:POKE 712,128 60 START=PEEK(560)+PEEK(561)*256+4 70 POKE START+10,2 BO FOR X=0 TO 3:POKE START+46+X,6:NEXT X 90 FOR X=0 TO 3:POKE START+50+X,7:NEXT X 95 POKE START+54,65:POKE START+55,PEEK(560):POKE START+56,PEEK(561) 110 A=0:READ B,C:IF B>-1 THEN GOSUB 800:GOTO 110 120 READ A, B, C: IF A>-1 THEN GOSUB 800: GOTO 120 200 CHBAS=RAMTOP-4:ADDR=CHBAS*256 210 FOR X=0 TO 1023:FOKE ADDR+X,PEEK(57344+X):NEXT X 220 POKE 756, CHBAS+2 230 FOR X=0 TO 255:POKE ADDR+512+X,PEEK(ADDR+256+X):NEXT X 240 FOR X=0 TO 7:POKE ADDR+512+X+0:NEXT X 250 FOR X=0 TO 7:READ A:POKE ADDR+99*8+X*A:NEXT X 290 POKE 755,0:POKE 87,0 306 POSITION 4,9:? #6; AUGUST 1980 Volume 5, Number 8"; 310 MEMST=PEEK(START)+PEEK(START+1)*256:CHRPOS=MEMST+46*40 320 FOR X=1 TO LEN(SML\$):FORE CHRFOS+X-1,ASC(SML\$(X,X))+128:NEXT X 330 CHRPOS=CHRPOS+60 340 FOR X=1 TO LEN(MGH\$):POKE CHRPOS+X-1,ASC(MGH\$(X,X))-64:NEXT X

Listing 2 continued on page 30

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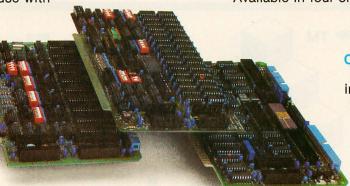


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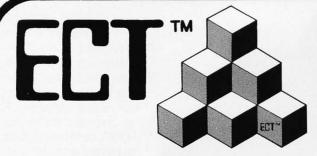
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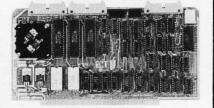
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```
Listing 2 continued:
350 FOKE 708,200
360 GOTO 360
800 ON A+1 GOTO 810,820,830
B10 PLOT B,C:RETURN
B20 DRAWTO B,C:RETURN
830 POSITION 8,C:XIO 18,#8,0,0,"S:":RETURN
900 DATA 111,20,111,21,110,21,109,21,108,22,107,23,107,24
905 DATA 106,25,106,26,107,27,107,28,108,29,109,30,110,30,111,30
910 DATA 111,31,110,31,109,31,108,32,107,33,107,34,106,35
915 DATA 106,36,107,37,107,38,108,39,109,40,110,40,111,40,111,41
920 DATA -1,-1,1,97,41,2,96,40,1,96,40,2,96,21
925 DATA 2,97,20,0,93,21,1,92,21,1,91,21,1,90,22,1,89,23,1,89,24
         1,88,25,1,88,40,1,87,41,1,80,41,2,79,40,0,79,40
930 DATA
935 DATA 2,79,25,0,79,25,2,78,24,0,78,24,2,78,23,0,78,23,0,77,22
940 DATA 0,77,22:2,76,21,1,74,21,1,74,20,1,93,20,0,71,20
945 DATA 1,71,36,1,70,20,1,70,36,1,69,33,1,69,36,1,68,34,1,68,36
950 DATA 1,67,34,1,67,40,1,66,41,1,59,41,2,58,40,0,58,40
955 DATA 2,58,36,1,54,36,2,54,34,1,64,33,1,63,32,1,63,21,1,62,20
960 DATA 1,55,20,2,54,21,0,54,21,2,54,33,0,51,21
965 DATA 1,51,29,0,51,32,1,51,40,1,50,41,1,50,32,0,50,29,1,50,20
970 DATA 1,49,20,1,49,22,0,49,28,1,49,33,0,49,39
975 DATA 1,49,41,1,48,41,1,48,40,0,48,32,1,48,29,0,48,21,1,48,20
980 DATA 1,46,22,0,46,28,0,46,33,0,46,39,1,45,36,1,45,36
985 DATA 0,45,28,1,45,23,0,47,41,1,36,41,2,35,40,0,35,40,2,35,21
990 DATA 1,36,20,1,49,20,-1,0,0
999 DATA 0,60,96,96,96,30,0,0
```



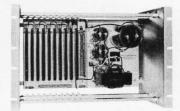
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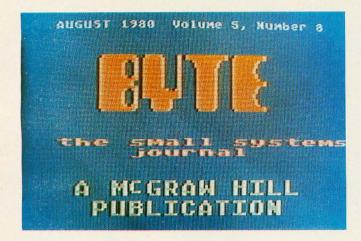


Photo 2: The BYTE logo as displayed by the Atari 400/800 running the program in listing 2.

8 BY 8 PIXEL SQUARE			В	INA	R	4		н	EXADI	ECIMAL	DECI	MAL
	0	0	0	0	0	0	0	0	0	0		0
	0	0	1	1	1	1	0	0	3	C	6	0
	0	1	1	0	0	0	0	0	6	0	9	6
	0	1	1	0	0	0	0	0	6	0	9	6
	0	1	1	0	0	0	0	0	6	0	9	6
	0	0	1	1	1	1	0	0	3	C	6	0
	0	0	0	0	0	0	0	0	0	0		0
	0	0	0	0	0	0	0	0	0	0		0

Figure 3: The assignment of values to create an elevated lower-case "c" character.

Text continued from page 26:

not uppercase and lowercase together—at least not in GRAPHICS 1 or 2. Since we want uppercase and lowercase together, we will have to redefine the character set.

To do this, we must have some memory reserved for the new character set. Line 20 did this by fooling the operating system into believing that the top of memory (called RAMTOP) lies sixteen pages lower than it actually does. This has reserved 4 K bytes for our use. The character set needs only 1 K bytes, but the display data cannot cross a 4 K boundary (without entailing difficulty), hence we must move the display list and display data down by an entire 4 K. The address of the beginning of our new character set is calculated in line 200 and is called ADDR.

In line 210, we move the original character set (starting at address 57344 in ROM) into user memory. In line 220, we tell the operating system where the new character set is. In line 230, we move the uppercase characters into the positions previously occupied by punctuation. Our new 64-member character set has uppercase and lowercase, but very little punctuation. In line 240, we define a new space character, as the original space character was part of the old punctuation group. We shall use the place previously occupied by the @ character for our space character.

We next take this technique of defining our own characters one step further. We had earlier decided to elevate the lowercase "c" in "McGraw-Hill." To do this, we must redefine what a lowercase "c" looks like. This is done in

The Atari 400/800 display list is actually a small program.

line 250, with data coming from line 999. Obviously, this procedure can be greatly extended. The diligent programmer can define any character set that can be expressed in an 8- by 8-pixel grid and POKE it into user memory directly (see figure 3). Greek, Cyrillic, or special technical character sets can be created in this way.

We now have our display list and character set in order. We need only display our text. This is done starting at line 290. The first POKE suppresses the cursor for a neater display; the second POKE fools the operating system into believing that it is working in mode 0. This prepares the way for a straightforward POSITION and PRINT of the first text line. The only trick is that the line is positioned vertically according to the number of mode lines from the top of the screen.

The next two text lines pose a particularly knotty problem. We desire to print GRAPHICS 1 and 2 characters on mode lines 46 thru 52. Neither graphics mode allows so many lines; when we try to position the cursor onto line 46 the computer will generate a "cursor out of range" error. Our only recourse is to POKE the character bytes directly into the display memory. We do this starting at line 310. First, we calculate the starting address of the display memory (MEMST). Then we calculate the address where our characters are to be stored (CHRPOS). Our calculation relies on the fact that the characters are on the 46th line and all previous lines used 40 bytes each. In more complicated situations, we would have to add up the byte requirements of all previous lines. This can get messy when a display mixes mode-1 or mode-2 lines at 20 bytes per line with other modes that use 40 bytes per line. Fortunately, our case is simple. Once CHRPOS has been calculated, we POKE the character values into the display data using a simple loop (line 320). Adding 60 to CHRPOS (line 330) skips three of our 20-byte mode-1 or mode-2 lines. We then POKE the character values for our third text line using the same technique we used in line 320, except that a different character-value offset (-64 instead of +128) gives us green characters instead of red ones. Line 350 turns the characters back on.

Conclusion

The two major tricks we have demonstrated in this article (modifying the display list and redefining the character set) will greatly extend the graphics and display power of your BASIC programs. The Atari 400/800 running BASIC alone has stunning graphics capabilities. With these tricks, the machine brings previously unheard-of capabilities into the hands of the personal computer owner. Yet, we are still just trundling down the runway. There are even grander functions built into this machine—movable graphics objects for animation, vertical and horizontal fine scrolling, and display-list interrupts, to name a few. With these tricks in hand, we can soar beyond the limits of yesterday's color display and animation.

The Panasonic and Quasar Hand-Held Computers

Beginning a New Generation of Consumer Computers

Gregg Williams, Editor Rick Meyer, Friends Amis c/o BYTE 70 Main St Peterborough NH 03458

Arthur C Clarke talked about them in his futuristic novel *Imperial Earth*. Jerry Pournelle and Larry Niven talked about them in *The Mote in God's Eye*. The subject is hand-held computers that can run programs, remind you of upcoming appointments, and serve as portable intermediaries between you and large, immobile, mainframe computers. Are they still science fiction? No, the hand-held computer is here—and for less than the price of some color televisions.

The HHC (hand-held computer) is a device about the size of a standard paperback book with two inches added to its longest dimension (see photo 1). Its weight is under a pound, yet it has the capabilities (when extended with portable peripherals) to do anything that existing personal computers do. The device, developed jointly by the Japanese corporation Matsushita (pronounced mat-SOOSH-ta) and Friends Amis of San Francisco, is being marketed in America by Panasonic and Quasar. Photographs in this article show both

It is impossible to lose the work you are doing by pressing the OFF key.

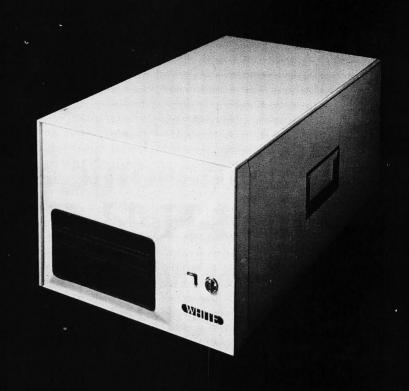
the Quasar and the Panasonic versions.

Description of the HHC System

The Quasar/Panasonic HHC is an integrated package of hardware and software that has the ability to do anything that other personal computers do. The HHC unit has the following characteristics:

- Dimensions: 22.7 by 3.0 by 9.5 cm (8¹5%, by 1%, by 3% inches);
- •Weight: 397 grams (14 oz.);
- •6502 microprocessor running at 1 MHz;
- Sixty-five-key keyboard with two-key rollover;

- ●159 by 8 dot low-persistence LCD (liquid-crystal display);
- Uninterrupted storage of all user programs and other data through use of a unique "power-down" circuit;
- Redefinition of all keys during execution of an application program;
- •Redefinition of all characters displayed on the LCD display and printer during execution of an application program;
- •2 K bytes of programmable memory, expandable to 4 K bytes internally or any practical limit (up to a theoretical limit of 4 megabytes) externally, by adding programmable memory peripherals;
- •16 K bytes of internal ROM (readonly memory) with sockets for four program capsules containing up to 64 K bytes of application programs or data (additional ROM, up to a theoretical limit of 4 megabytes, can be added externally through ROM peripherals);
- An internal real-time clock with a resolution of ½56 second;
- A built-in nickel-cadmium battery



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Photo 2: The HHC and its peripherals. The HHC computer is in the center of the photograph. The peripherals are (clockwise, from upper left): a programmable-memory extender, the color television interface, the I/O driver (a distributor of bus signals from the HHC to other peripherals), an acoustic-coupler modem, a portable printer, a cassette interface, and a ROM expander.

All functions are selected via a set of nested menus.

enough current to retain the contents of the HHC's display image and CMOS (complementary metal-oxide semiconductor) memory and to preserve the real-time clock and keyboard functions. A side benefit of this feature is that it is impossible to lose the work you are doing by pressing the OFF key; when you press the ON key, the computer resumes whatever it was doing before it was turned off.

A specially designed 44-pin bus connector allows you to connect and disconnect the HHC and its peripherals while all the components are powered up. Because of this feature, the HHC and its peripherals can join their respective data, address, and control buses without destroying data in either unit. As an additional safety feature, the piezoelectric beeper inside the HHC sounds if the HHC finds any loose connectors.

The ability to connect and discon-

nect modules while the power is on is very important because it allows the unit to be used in a variety of combinations without worrying that data will be destroyed by doing so. The HHC and its peripherals can be considered as interconnecting modules, and you can effectively forget that they contain volatile data. For example, when future program-development capsules become available, you will be able to write a program while traveling, then debug it more easily by hooking the HHC into the color TV adapter and printer. Data can also be entered into an HHC memory peripheral that may then be detached from the HHC and given to another HHC owner. He or she can plug it into another HHC and access the data that was stored.

Friends Amis has invented a particularly elegant solution to the packaging of programs in ROM (read-only memory). This solution also allows denser storage of information than was previously possible. The HHC uses 24-pin ROMs that are packaged in a plastic carrier around which the pins of the ROM are bent (see photo 3). This combination is

called an Amis Memory System Capsule (patent pending). (When a capsule is inserted into the back of the HHC, the flat base of each pin makes contact with the socket. This insures a good electrical contact without the usual fragility of integrated circuit pins.) Since a minimal amount of hardware is used to package the ROMs, more can fit inside the small body of the HHC.

These capsules have already been used in the Craig, Panasonic, and Quasar language translators (also developed by Friends Amis), and in the Friends Amis point of information display computer. Capsules can contain data to be manipulated (eg: words in a French language capsule), application software (eg: a capsule of game programs), programming languages (eg: a BASIC capsule), or any other data that the computer can act upon. Capsules can hold 2 K, 4 K, 8 K, or 16 K bytes of information. The 16 K-byte ROM allows an unprecedented amount of data to be stored in a small space. The large amount of information that can be stored in the HHC is increased by its internal use of a threaded language and by the application of a set of data compression techniques.

Human-Engineered Features

As a direct result of the manufacturers' desire to design a computer specifically for the mass market, the Quasar/Panasonic HHC was developed with a heavy emphasis on human engineering. This design philosophy is reflected in the operation and features of the HHC.

The keyboard has always been a crucial interface between the user and the computer, and the popularity of several existing microcomputers has been largely influenced by the usability of their keyboard. This fact, coupled with the small size of the HHC, makes it necessary for the HHC keyboard to be as usable as possible. We feel that the designers have achieved this objective.

[Despite my initial disbelief that a keyboard this small could be of any practical use, I was soon convinced that the HHC keyboard is easy to use and that, given some familiarity with it, I could use the keyboard without being distracted from the task at hand...GW]

Photo 1 indicates that the keys on

pack that supplies all power to the unit:

- Internal shielding against RF (radiofrequency) interference in compliance with the new regulations from the Federal Communications Commission:
- An internal set of application programs that includes a four-function calculator, a free-form file system and editor, as well as several other functions.

In addition, the capabilities of the HHC are greatly extended by an integrated system of intelligent peripherals that include:

- A bus expander through which other modules are connected to the HHC:
- A portable thermal printer that prints 16 characters per line;
- A ROM extender that allows you to attach an additional four program or data capsules:
- A programmable-memory extender that allows you to add additional memory to the HHC;
- •A 110/300 bps modem and telecomputing program through which the HHC can act as a remote terminal to other computers and to large information utilities and data bases;
- A cassette interface module that transfers data to a microcassette recorder at 1200 bps;
- A color television interface that allows a display of 16 lines of 32 characters each or up to 48 by 64 pixel (picture element) graphics in eight colors and black.

When connected to the HHC, all of the above peripherals can fit in a custom case the size of an average attaché case, or they can be interconnected to make a flat, rigid, easily portable combination. With the exception of the color television interface, the HHC and the peripherals can operate without connections to any outside power source, thus making the system truly portable and hand-held. Photo 2 shows the HHC and several of its peripherals.

Innovations in the HHC

The Panasonic/Quasar HHC embodies several technical breakthroughs. Without these developments, a computer as small and as powerful as the HHC could not have been built.

One of the most important innova-

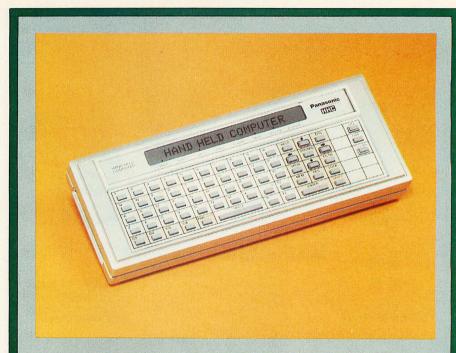




Photo 1: The Panasonic and Quasar HHCs (hand-held computers). Both units shown are prototype models and will have the same keyboard layout in the finished versions.

tions in the HHC is the proprietary "power-down" circuit that allows the HHC to use the popular 6502 microprocessor in a hand-held device. In the past, manufacturers have designed hand-held products around microprocessors like the 1802. Such devices use a very small amount of current and can be powered by batteries, but they force the designer to use a slow microprocessor with a weak instruction set.

Designers have been prevented from using the more popular microprocessors because of their high current drain: a conventional 6502-based circuit (using the same batteries as the HHC) would discharge them in about two hours. But, with this powerdown circuit and additional hardware innovations, the amount of current needed to power the HHC in both its fully functioning and "off" (powereddown) modes is drastically reduced.

A related feature of the HHC is that when the OFF button has been pressed, the computer is still on. It is in a dormant state that uses only

the HHC are arranged in the standard typewriter format. In addition, a key can be pressed without pressing any adjacent keys, so it is possible to touch-type on the HHC, regardless of individual finger width. This fact allows the HHC to be used in text applications—an area not practically accessible by any other device of its size.

Another powerful feature of the HHC is its ability within an application program to redefine any key position to any function. With the addition of a keyboard overlay, this can provide a keyboard that is completely suited to a given application. It was the intention of the HHC designers that no application, regardless of complexity, would require memorization of command language or special key sequence (like control-P for print) to perform a function available to the computer but not allotted a key. With redefinable keys and keyboard overlays, this will never happen.

Three special keys, labeled f1, f2, and f3, can be assigned to be any sequence of keystrokes, including most function keys. When one of these keys is typed, its current definition is input as if the sequence of keys had been typed by the user. The definitions are processed as interrupts and are independent of the program in use. Thus, they can be used with any present or future programs, even those written in BASIC or SNAP (the two computer languages currently planned for the HHC). For example, one key can be assigned to a sequence of calculations and/or constant values for use with the built-in calculator. Another key can be used to enter repetitive text in the memory bank text editor or to create special functions such as search-and-replace. Another definition can be used to make a commonly used sequence of menu selections to reach a frequently used program.

A unique feature of the HHC is the HELP key. When this key is pressed, you are prompted by the LCD display to press any key to find its definition. When a key is pressed, the function is given in a complete sentence of up to 80 characters. For example, pressing the HELP key followed by the STP/SPD key causes the message "STOP / ENTER 1-9 FOR SPEED" to be displayed.

Four HHC keys are used to indicate

LEFT, RIGHT, UP, and DOWN. In most programs, these keys are used for cursor control and horizontal and vertical scrolling. Since the HHC's built-in display shows only one short (26-character) line at a time, it is important to be able to "steer" the display through a larger page or list of material. The display is often used as a window into a larger virtual space (as is done in the popular VisiCalc program), and the four direction keys, which are auto-repeat

keys, move the window in any direction. Another key, STP/SPD (stop/speed), allows you to freeze and continue any program, like a run/stop switch, and to adjust the rate of information display.

The HHC also has INSERT and DELETE keys that allow text material to be changed. The HHC normally displays a solid rectangular cursor, but when you enter the insertion mode, the cursor changes to a blinking checkerboard cursor. Similarly,

WORD	FIRST NUMBER*	LETTERS BORROWED FROM LAST WORD	FIRST LETTER NOT COPIED	SECOND NUMBER * (COUNT FORWARD)	NEXT LETTER OF NEW WORD	REMAINING LETTERS OF NEW WORD*
SLOW						
SLUMP	2	SL	0 +	6 =	U	MP
SLY	2	SL	U +	4 =	Y	
SMALL	1	S	L +	1 =	M	ALL
SMART	3	SMA	L +	6 =	R	Т

Figure 1: Compression of an alphabetized list. The tables of alphabetized lists within the HHC are kept as small as possible by using numbers to keep track of the number of letters shared from the previous word and the number of letters between the first different letter in the new word and its counterpart in the previous word. Note that the shaded letters on a line make up the word being encoded, but only the two numbers and the letters in the last column (all marked with an asterisk in their table headers) are actually stored in the encoded table. The dashes indicate an empty entry (as in the line for the word SLY). The first line is all dashes because it does not have a previous line to refer to; in practice, all the letters of the first entry must be normally encoded.

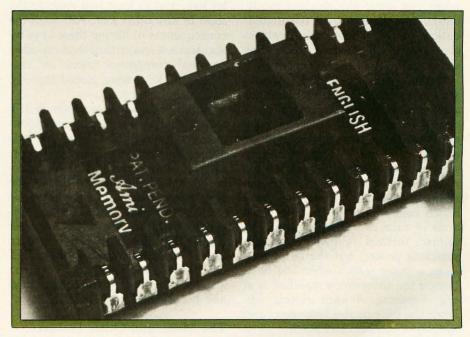


Photo 3: Close-up of an HHC program capsule. The program capsule is actually a standard 24-pin integrated circuit with its pins curled around a plastic harness. Its length is 3.65 cm (1%₁₆ inches).

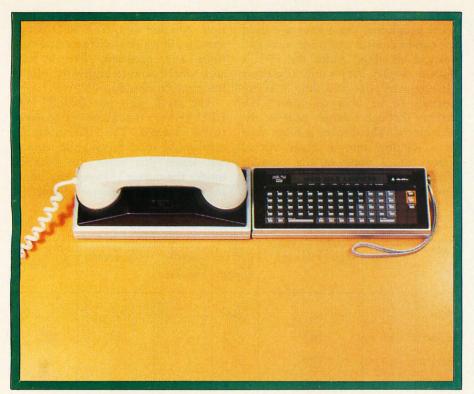


Photo 4: The Quasar HHC connected directly to its acoustic coupler. The combination, which is also available in the Panasonic HHC system, is a self-contained portable computer terminal.

The computer executes a FORTH-like language called SNAP.

when you enter the deletion mode, the cursor changes to a rectangular outline cursor. These useful features give you visual feedback regarding the mode that the computer is in.

Other keyboard-related features are the search and locate commands available within the memory bank electronic file system. These features are available in two modes-context and initial search. A context search searches for a match to the given character string anywhere in the file, while an initial search searches for a match beginning with the first character of each record in the file. The former method allows maximum searching power, but the latter provides a faster search when the position of the string to be matched is at the beginning of each record (eg: when the file contains last names and telephone numbers and you are given the last name).

Other strong keyboard features of the HHC are the size and placement of certain keys. The SPACE and ENTER keys are in their traditional positions, and both are wider than the other keys for ease of use. Also notice from photo 1 that the CLEAR, ON, and OFF keys are located five rows to the right of the rightmost letter key, and at least two rows to the right of any other key. Although the consequences of hitting these keys by accident are less critical than on other personal computers (more on that later), the keys were placed there to minimize the danger.

Finally, the behavior of the SHIFT and LOCK keys should be mentioned. In applications where the program differentiates between uppercase and lowercase letters, an uppercase letter is obtained by hitting the SHIFT key, followed by the key to be shifted. The HHC is locked into uppercase by hitting the LOCK key after the SHIFT key. You can return to lowercase by hitting either the SHIFT or LOCK keys. The LOCK key can also lock the four cursor-control keys and the INSERT and DELETE keys.

The Menu and Other Features

To allow for use of the Panasonic/ Quasar HHC with minimal prior knowledge of the machine, all func-

tions are selected via a set of nested menus. The first menu that appears when the computer is turned on is called the primary menu. It displays the available internal and capsule program choices (eg: clock/secretary, program capsule, etc) with a 1-digit number assigned to each. A choice is selected by pressing the corresponding digit key. If the selected application allows choices of its own, its menu is displayed in the same way. This process is repeated until an executable program is reached. Pressing the CLEAR key causes the HHC to display the second menu (the one immediately after the primary menu). Pressing the CLEAR key twice causes the HHC to return to the primary menu.

The HHC computer contains a piezoelectric beeper that can produce either a click (to provide audible feedback to an event, usually a keypress) or a tone within a four-octave range.

Squeezing More into Less

There has been recent publicity on threaded languages—most visibly FORTH. (See the special language issue on FORTH, August 1980 BYTE.) Threaded languages offer program compactness and speed of execution halfway between those of machine language and a high-level language like BASIC, while offering the programming ease and language transportability of high-level languages.

The Quasar/Panasonic HHC is actually a hardware machine that executes a FORTH-like language called SNAP, in addition to 6502 machine code. The HHC uses SNAP for every function that it performs, from the display of characters on the LCD readout to the handling of interrupts from the peripherals. When timing is critical in a specific routine, such as interrupt handling for high-speed peripherals, SNAP allows any portion of itself to be coded in assembly language for maximal speed.

SNAP, like other threaded languages, is defined in terms of a given set of operators (which are analogous to the operation codes of a given microprocessor). SNAP programs are simply lists of these operators, so these programs (including applications programs embedded in program capsule ROMs) may be used without change on any machine that executes the SNAP language, provided no ma-

chine code is used. This protects the sizable programming effort put into the HHC against hardware innovations in future versions of the HHC, while maintaining a body of programs that execute quickly and use

little memory.

Another way in which the execution time of programs is decreased is through the use of interrupts for the HHC keyboard and all peripherals. In contrast to other computers which use polling (ie: they periodically check the device to see if it needs computer time), the HHC peripherals and keyboard generate interrupts when they require attention from the 6502 microprocessor. In this way several peripherals can be serviced at once. The HHC slows down only when it is interrupted to do specific work and is therefore faster than computers that waste time polling inactive devices. The HHC peripherals that require serial data all use separate UART (universal asynchronous receiver-transmitter) integrated circuits for this purpose.

Given the 64 K-byte maximum addressing ability of the 6502 microprocessor, the HHC must somehow pack more memory into less space. It does so, using the familiar technique of bank-switching. Three banks of memory, hexadecimal 2000 to 3FFF, 4000 to 7FFF, and 8000 to BFFF, are bank-switched. This means that several blocks of up to 16 K bytes of memory could be assigned to one of the above address areas, with electronic circuitry enabling only one such block to be

active at a time.

The program capsules that insert into the back of the HHC all map into the same 16 K-byte address area: hexadecimal 4000 to 7FFF. Only one capsule is active at a time and is selected from the HHC primary menu. This area is also used for user data and programs.

The 16 K-byte area from hexadecimal locations 8000 to BFFF is used for external programmable memory banks. Since this bank is in a different address area from ROM banks, many ROM-based programs can reference data in programmable memory without bank-switching.

The 8 K-byte address area (from hexadecimal locations 2000 to 3FFF) is used by the specialized firmware

is used by the specialized firmware that is contained in each HHC peripheral. When a given peripheral is being used, the firmware that con-

Letter E T I O N S •	Huffman Code 000 001 010 0110 0111 1000 •		(a)
Code	Bit(s) to Be Matched	Comments	
<u>0</u> 100111 <u>01</u> 00111 <u>010</u> 0111 (I) <u>0</u> 111 (I) <u>01</u> 11 (I) <u>011</u> 1 (I) (I)(N)	0 01 010 0 01 011 0111 done	no matchno matchmatches Ino matchno matchno matchmatches Nmessage is IN	(b)

Table 1: An example of Huffman coding. Table 1a shows an example Huffman code for several letters. Table 1b shows how the code 0100111 is decoded into the letters I and N. Bits are taken from the left side of the remaining binary string until the sequence of bits matches one of the table entries. Notice in table 1a that the code for no letter is a beginning substring of the code for another letter. (This, for example, accounts for the fact that no letter is given to the bit string 011—it would conflict with 0110, the code for the letter O.) Every Huffman code (of which there are an infinite number) is constructed so that no two letters can be confused with each other. If the letters are assigned codes in the order of their decreasing frequency for the text to be decoded, a Huffman code permits the maximum data compression possible.

Number of Elements in Table (= 2")	Number of Bits in New Permutation Algorithm $(F(N) = 2^N + 2F(N - 1))$	Number of Bits in Ordinary Look-up Table (= N2 ^M)
2	1	2
4	6 = 4 + 2(1)	8
8	20 = 8 + 2(6)	24
16	56 = 16 + 2(20)	64
32		160
64	352 = 64 + 2(144)	384
	in Table (= 2*) 2 4 8 16 32	in Table (= 2^N) In New Permutation Algorithm (F(N) = $2^N + 2F(N - 1)$) 2 4 6 = 4 + 2(1) 8 20 = 8 + 2(6) 16 56 = 16 + 2(20) 32 144 = 32 + 2(56)

Table 2: Efficiency of the permutation algorithm given in figures 2 through 4 and the text box. As can be seen from the last two columns, this algorithm uses fewer bits to define a given permutation. The ordinary look up table uses a table 2^N entries long by N bits long to look up the value (from 0 to 2^N-1) that a given element (in the same range) is permuted to.

trols its communication with the HHC is selected and used. This area also contains the memory-mapped contents of the video display when the HHC is connected to the color TV interface.

In both 16 K-byte bank-switched areas it is possible to reference a program or a program/data combination that is more than 16 K bytes long. The program (or program and data)

is divided into 16 K-byte blocks, all of which map into the same area. Under program control the software can then jump between 16 K-byte blocks by writing the appropriate value to a location in the HHC that determines which block is currently selected.

Text Compression in the HHC
The increase in data storage caused

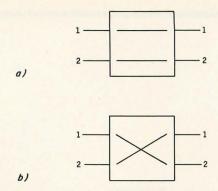


Figure 2: Two possible outcomes for the permutations of a two-element list. See the Mapping Algorithm text box for further details.

by the use of SNAP and 16 K-byte program capsules is significant. But the increase caused by the use of data compression techniques is even more significant, almost doubling the amount of information that can be stored in an HHC data capsule. A variable word-length code and increased data compaction through context are the two techniques used.

In traditional data storage, one character of information is stored in a byte (or 8 bits or binary digits) of computer memory. Letters, numbers, and punctuation are stored in the ASCII (American Standard Code for Information Interchange) format, which uses 7 bits per character. Using a method developed by Friends Amis that modifies what is called a Huffman code, variable bit-length codes can be devised for the characters to be encoded such that frequently used characters will be given shorter codes (called codons), thus decreasing the average number of bits used per character. Table 1 shows an example of a standard Huffman code (there are an infinite number of such codes).

Because of this variable-length coding, the computer's memory is seen as a long string of bits. Bits are read from left to right (figuratively speaking) until the bits read match the codon for any character in the set. (Codons are generated by rules that guarantee that a beginning string of bits can match the codon of only one letter in the set.) Codons are also devised so that the most frequently used letters have the shorter representations and are also near the top of the look-up stack. Because the number of look-up entries read before a match occurs is kept to a minimum (on the average, slightly more than eight entries), the decoding process

does not slow the machine down.

A further measure of compression is made by modifying the look-up procedure to be sensitive to the context of the previous letter. For example, even though the most frequently used letters in normal English text are (in decreasing frequency) E, T, I, O, N, and so on, if the previous letter looked up was Q, then the letter U is most probably the next letter and so should be close to the beginning of the look-up table. Within the HHC, the letter-decoding routine uses the previously decoded letter to index one of several look-up tables. In this way, encoded characters can be represented in even fewer bits than would otherwise be possible using straight frequency-determined co-

Two more techniques are used within the HHC to decrease the number of bits used to represent character information to a final density of just over 4 bits per character. Although these techniques

were developed to deal with alphabetized lists of words (for the Friends Amis language translator), it is possible to use them to compress nonalphabetized text in some situations.

The first technique replaces the beginning of each word (except the first word in a list) with two numbers. The first number tells how many letters to borrow from the previous word. The second number tells how many letters away the first nonmatching letter is from its counterpart in the previous word. For example, if the words are SMALL and SMART, the following is stored for the word SMART: 3 (telling the computer to borrow SMA from the word SMALL); 6 (telling the computer to count forward six letters from the L in SMALL to arrive at the R in SMART); the encoded letter T (ending the encoding of the word SMART). (See figure 1 for other examples.) Because the two numbers (contained in 3 and 4 bits, respectively) take up fewer bits than the letters

The Mapping Algorithm

It is sometimes profitable to maintain a list of words in alphabetic order but to be able to retrieve them in some other prespecified order. The problem then becomes one of finding the most compact way of specifying a permutation of N elements from (1, 2, 3, ., N) to some other ordering.

The algorithm used within the Panasonic/Quasar HHC requires that the list be a power of 2 (ie: have 2, 4, 8, 16, 32, 64,... elements). The algorithm can be considered as a recursive set of pair switchings. The permutations of a list of two elements can be represented by 1 bit of information—say, a 0 to represent that the elements are not switched, eg: (1, 2) becomes (1, 2); and a 1 to represent that the elements are switched, eg: (1, 2) becomes (2, 1). This is represented pictorially in figure 2, where a box represents 1 bit of information.

The diagram in figure 3a is used with a list of four elements. The upper-lefthand box is always filled in with an equal sign (=). The input arrangement, usually (1, 2, 3, 4), is substituted for IN1 thru IN4, and the desired permutation is sub-

stituted for OUT1 thru OUT4. The boxes in the first and third columns are filled in with either equal signs (=) or cross signs (×), leaving the boxes in the second column for last.

Consider the example of permuting the list (1, 2, 3, 4) to become (4, 1, 3, 2). Given the interconnections between boxes and the constraints given above, the only path that can be taken from 1 to 1 goes through the top middle box (in a manner not yet specified) and to the righthand side through a cross in the upper-right box, as shown in figure 3b. In figure 3c, the element 4 is traced from box A to box B. Similarly, element 3 is traced from box B to box C, and element 2 is traced from box B to box S, where we started.

Given the conditions shown in figure 3c, it is a simple task to fill in the middle columns, thus completing the diagram. The finished diagram is shown in figure 3d. Through use of this diagram, the list (1, 2, 3, 4) can be permuted to the list (4, 1, 3, 2) using 6 bits of information (1 bit for each of the six boxes).

Study of an eight-element list example illustrates the recursive

they replace, this method can represent the same text in fewer bits.

The last technique saves space in that it allows alphabetized lists to be used in a different order. (For example, in language lists a given set of words is mapped from the sequential order in its alphabetized list to a semantic order in a list of words of equivalent meaning available in each language list; this is done so that the computer can translate a given word to its equivalent in another language.) With this technique, a list of 2 elements can be permuted into any other arrangement of the same elements by a relatively small number of bits of information (see table 2). Refer to the Mapping Algorithm text box for the details of this algorithm.

The Real-Time Clock

One of the most important internal features of the Panasonic/Quasar HHC is its real-time clock and event sequencer. The real-time clock exists in memory as a 40-bit number stored

method that is used to generate the final structure for longer lists. Figure 4 shows a mapping of the list (1, 2, 3, 4, 5, 6, 7, 8) to (6, 3, 8, 1, 7, 5, 4, 2). As before, box S is marked with an equal sign. Boxes in the first and last columns are then filled in; this can even be done with no knowledge of the contents of boxes X and Y. The boxes A through G are filled in alphabetically. Note that when these boxes are filled, the boxes X and Y become "black boxes" that map four-element lists into another ordering. These boxes are then solved as shown in figure 3, and the permutation of eight elements is now solved. The final solution has twenty boxes: eight as shown in figure 4, plus six boxes each for boxes X and Y.

Larger lists are solved in an analogous way, with a list of 2^N elements first filling the 2^N boxes in the first and last columns, followed by the solution of the two middle boxes, each of which permutes a list of 2^{N-1} elements. Table 2 shows the number of boxes (or bits) necessary to solve larger permutations.

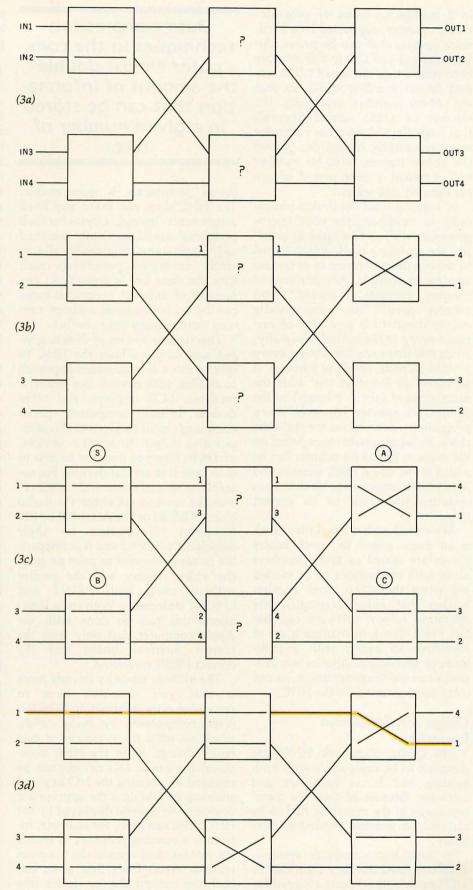


Figure 3: Solving a four-element permutation problem as a network of binary decisions. Figure 3a shows the initial configuration used in the solution of any four-element permutation. Figures 3b, 3c, and 3d show steps in the solution of this problem. See the Mapping Algorithm text box for further details.

in 5 contiguous bytes of programmable memory, supported by a hardware counter that can be preset. An increment of one unit in this number represents a time change of 1/256 second (about 4 milliseconds), so that the 40-bit number represents the number of 1/256 second intervals that have elapsed since the computer was permanently turned on. (Given the above figures, a 40-bit number will represent a time period of approximately 139 years.)

In keeping with the design philosophy of burdening the 6502 microprocessor with as few tasks as possible, the real-time clock was designed to require the generating of as few interrupts as possible. Another area of memory contains a signed 23-bit counter circuit that automatically counts down to 0 at a rate of one count every 1/256 second. Normally, when this timer reaches 0 (once every 2²³/256 seconds, or about 9 hours), it generates an interrupt that adds the same amount (about 9 hours) to the 40-bit clock number. However, if any program needs to access the real-time clock, the appropriate count based on the value in the 23-bit counter can be added to the 40-bit clock number and the 23-bit counter can be cleared, thus updating the clock to its correct value.

Associated with the real-time clock is an event queue in which future events are stored as 40-bit numbers along with instructions to be carried out when the 40-bit clock number reaches that value. Internally, the operating system software can use this event queue to manage a set of asynchronous events with a minimum of processing. Application programs can use the event queue, as can users programming on the HHC.

Design for Component Interaction

The Quasar/Panasonic HHC was designed to be compatible with both existing and future hardware and software. Because of this, the memory usage of the computer had to be planned to provide maximum flexibility.

In most microcomputer systems, there are fixed memory locations or I/O (input/output) ports assigned for specific hardware peripherals. The limitation of this approach is that the entire memory mapping must be foreseen; otherwise the ability to include

Data compression techniques in the computer almost double the amount of information that can be stored in a given number of bits.

future peripherals is questionable. The HHC does not make any fixed assignments. Instead, 4 bytes for each peripheral are dynamically assigned as I/O and status locations for all currently connected peripherals each time the clear key is pressed, so any number of different peripheral types can be accommodated without running into memory map conflicts.

This flexible system of directing input and output allows the HHC to offer a more commonsense approach to dealing with devices like printers. modems, LCD displays, and other devices. In most computers, special commands must be given to direct input and output to specific devices, and even then you may not be able to distribute it to several devices. For example, a special command, LPRINT, must be used to get either the Radio Shack TRS-80 or the Atari 400 or 800 to print information on their associated printers, and it is impossible to get a program to print on both the video display and the printer without using both PRINT and LPRINT statements. With some limitations this can be done with the Apple computer, but only with the correct interface board and the correct PR#N command.

The attitude taken by Friends Amis that you shouldn't have to remember extra information (which is often complicated by being conditional on what the computer is currently doing). With the HHC computer, the use of I/O devices can be changed by pressing the I/O key and enabling or disabling the appropriate devices from a menu displayed by the HHC. You can even, for example, interrupt a running program to enable the printer, and resume the program without error; from that point on, both the current display device (the LCD display, color TV, or other device) and the printer display whatever the program tells them to. This method allows HHC programs to be independent of the I/O devices, and it allows the use of future peripherals with current software.

Application Software

The Panasonic/Quasar HHC includes several application programs that are contained in the same built-in read-only memory devices as the operating system. These programs implement a calculator, a clock/secretary, and an electronic file system and editor. Each of these programs is called from the primary menu of the HHC.

The calculator program, when selected, transforms the HHC into a standard four-function calculator that adds, subtracts, multiplies, and divides. The calculator can store one number and has keys to add to, subtract from, clear, and recall memory. It also has a percent key.

The clock/secretary uses the realtime clock that knows the time of day, the day of the week, and the date (day, month, and year). A clock option within the clock/secretary allows the time and date to be displayed and continuously updated on the LCD display window. Otherwise, the clock/secretary can be used to keep track of future events. You can specify a time for the clock/secretary to activate itself, and include an optional reminder message. When that time arrives, the HHC sounds a musical tune regardless of its current task; you can then perform an "acknowledge" operation and see the message associated with the event. The number of events and messages that the clock/secretary can hold is limited by the amount of programmable memory in the HHC.

The "memory bank" is the nickname of an electronic file system and editor within the HHC. You can enter lines (or records) of up to 80 characters of ASCII information, group them to make files, and modify and list these files. Any file can be edited with a powerful cursor-controlled editor that allows insertion and deletion of characters or lines at the current cursor position. With the SEARCH key, you can also retrieve records from a file based on a character string to be matched.

Memory bank files can have any number of records, with each record holding up to 80 characters. The size and number of files that can be stored depends on the amount of programmable memory in the HHC. The current model of the HHC has somewhat less than 1500 bytes of memory for this purpose, but the amount of memory in the HHC can be expanded with a battery-powered 4 K-byte memory extender peripheral. Future models will accept more programmable memory in the form of capsules that fit into the same sockets as the read-only memory capsules.

The Extended HHC

The Quasar/Panasonic HHC. when combined with its line of peripherals, has the ability to perform any function that existing personal computers do, while retaining the characteristics and advantages of a hand-held unit. The following sections describe two of the most interesting peripherals—the color television interface and the modem.

The color television interface is the only peripheral that requires connection to an AC power line. But since the interface is also connected to a color TV, this is hardly a limitation. Once the interface is connected, output can be routed to the TV through

the use of the I/O key.

Through the color TV, the HHC will display 16 lines of 32 characters each. Characters can be displayed in several combinations (orange or green characters on black, or black characters on either an orange or a green background). Several kinds of characters can be displayed: uppercase and lowercase ASCII letters; numbers and punctuation; graphics patterns; and katakana characters (a set of phonetic characters used by the Japanese). All characters are created in a 7 by 9 dot matrix.

The color TV interface offers two modes of color graphics: 32 by 64 pixels, or 48 by 64 pixels. The interface allows for black and eight colors (red, blue, green, yellow, orange, magenta, cyan, and buff).

The color TV interface contains a built-in RF (radio-frequency) modulator, as well as 1.5 K bytes of dynamic memory organized as two software-selectable screen images. The connection from the interface to the HHC is an interrupt-driven parallel connection.

The modem, which connects to the HHC through an interrupt-driven parallel interface, is acoustically coupled to a standard telephone handset (see photo 4). Its options—

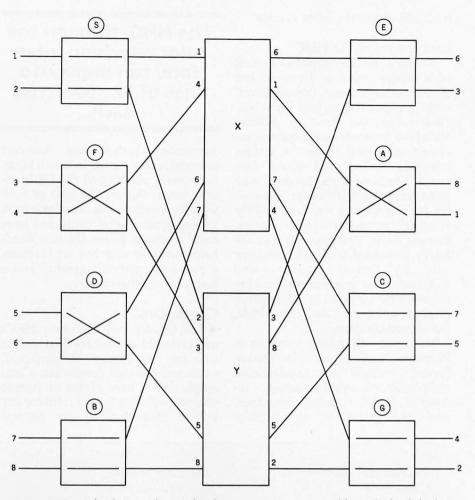


Figure 4: Partial solution of an eight-element permutation problem. Each of the boxes in the first and last columns is filled in first. The solution of this problem is then finished by the solution of two four-element permutations as given by the numbers on both sides of the boxes marked X and Y.

110 or 300 bps (bits per second) data transfer rate, full- or half-duplex transmission, answer or originate mode, number of start and stop bits, and parity-are all selected by software. In a daring departure from conventional modems, the HHC modem has no visible switches to set any of its options. This forces the software to control all the options and leaves nothing for you to worry with (or set incorrectly)

The HHC modem, like other HHC peripherals, is responsible for supplying standard input and output routines. (By using a uniform software interface for all peripherals, the HHC can be expected to work with peripherals that have not yet been designed.) Since the modem can be used in several ways, it is supplied with a socket in which to place a program capsule for a given application. The first capsule to be produced for the HHC modem is called 'Telecom-

puting" and it will allow the HHC to be used as an intelligent remote terminal that is connected, through the modem, to a timesharing computer or data base. The program can be used with the small battery-operated modem directly connected to the HHC, in a hand-held configuration, or the printer and TV can be used.

The telecomputing software can use an automatic X-ON/X-OFF handshaking with a host computer so that you can regulate the rate of display to your reading speed. This protocol is supported by most popular networks such as Micronet, The Source, and Tymnet. When a printer is not connected, you can review many lines of previous interaction as they appear in the LCD display, creating, in effect, a virtual printout. Incoming lines longer than the 26-character LCD display are divided only at blanks. This "word-wrap" feature, combined with the review mode,

readability with the 1-line display.

Background of the HHC

The HHC was developed as a result of a unique union of Japanese and American technology. Friends Amis, with headquarters in San Francisco, contributed the best of Silicon Valley-a software-based systems architecture, circuit design, a unique operating system and SNAP language. The company's founders, who came from Atari Inc, were responsible for introducing the now widely accepted consumer video games. Friends Amis' first product was the highly successful language translator sold by Craig, Quasar, and Panasonic; this product was quickly followed by its point of information display computer and the HHC (hand-held computer).

Matsushita, the parent company of Panasonic and Quasar, in Osaka, Japan, brought its unparalleled techniques of miniaturization, industrial design, quality assurance, and the ultimate in highly

The HHC, through the color television interface, can display 16 lines of 32 characters each.

automated, high-volume, low-cost manufacturing—areas in which Japan has clearly outstripped the US in recent years. Putting the best of both worlds together has resulted in a special product that could not have been produced alone: the first handheld computer with bus architecture, a powerful operating system, and a fast 8-bit microprocessor.

Conclusions

•The Quasar and Panasonic HHCs are certainly impressive first entries into the new market of hand-held, consumer-oriented computers. Great emphasis has been placed on human engineering. This is important for any device marketed to the general

public, even more so when so many functions are being placed into such a small package.

- •The HHC was designed as a basic unit augmented by an extensive complement of peripherals. This "debundled" approach allows you to buy only those peripherals you want, giving you a customized computer at minimal cost.
- •Several innovations in the HHC computer allow it to have the power of conventional personal computers while retaining the portability of a hand-held unit. The use of data compression techniques and program capsules enables very large amounts of data to be contained within the hand-held unit.
- •The HHC is supplied with internal application programs that include a clock, an electronic secretary that reminds you of future appointments, and a file system for user data contained completely within the programmable memory of the computer. These are nice touches that add to the utility of the computer.

A Fictional Hand-Held Computer

Duncan's Minisec had been a parting gift from Colin, and he was not completely familiar with its controls. There had been nothing really wrong with his old unit, and he had left it behind with some regret; but the casing had become stained and battle-scarred, and he had to agree that it was not elegant enough for Earth.

The 'Sec was the standard size of all such units, determined by what could fit comfortably in the normal human hand. At a quick glance, it did not differ greatly from one of the small electronic calculators that had started coming into general use in the late twentieth century. It was, however, infinitely more versatile, and Duncan could not imagine how life would be possible without it.

Because of the finite size of clumsy human fingers, it had no more controls than its ancestors of three centuries earlier. There were fifty neat little studs; each, however, had a virtually unlimited number of functions, according to the mode of operation—for the

character visible on each stud changed according to the mode. Thus on ALPHANUMERIC, twenty-six of the studs bore the letters of the alphabet, while ten showed the digits zero to nine. On MATH, the letters disappeared from the alphabetical studs and were replaced by \times , +, \div , -, =, and all the standard mathematical functions.

Another mode was DICTION-ARY. The 'Sec stored over a hundred thousand words, whose three-line definitions could be displayed on the bright little screen, steadily rolling over page by page if desired. CLOCK and CALENDAR also used the screen for display, but for dealing with vast amounts of information it was desirable to link the 'Sec to the much larger screen of a standard Comsole. This could be done through the unit's optical interface—a tiny Transmit-Receive bull's-eye operating in the near ultraviolet. As long as this lens was in visual range of the corresponding sensor on a Comsole, the two units could happily exchange information at the rate of megabits

per second. Thus when the 'Sec's own internal memory was saturated, its contents could be dumped into a larger store for permanent keeping; or conversely, it could be loaded up through the optical link with any special data required for a particular job.

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[Editor's Note: The 'Duncan' referred to in the first paragraph is Duncan Makenzie, the main character in Clarke's Imperial Earth. Duncan's boyhood friend is Karl Helmer, a character whose name is a variant spelling on that of our Founding Editor, Carl Helmers. For a humorous (and somewhat eerie) commentary on the name similarity and the anticipated possibility of a hand-held computer, see Carl Helmers' editorial in the April 1977 BYTE (page 6), "How I Was Born 300 Years Ahead of My Time."]

• The HHC retains the contents of memory even when it is turned off. In addition, you do not lose what you are working on if you accidentally hit the OFF button. These are important features that indicate the amount and depth of human engineering that has been applied to the design of the HHC.

•The HHC will be marketed aggressively by both Quasar and Panasonic. The public reaction to this device, which is the first of its kind to be marketed on such a large scale, will be carefully observed by manufacturers and may determine the extent and direction of future consumer products in this area. We feel that the Panasonic/Quasar HHC is highly qualified to receive this scrutiny and that the public response will be favorable.■

Acknowledgment

The cover photograph and all interior photographs are by Ed Crabtree. Photo 2 is courtesy Quasar Electronics Company.

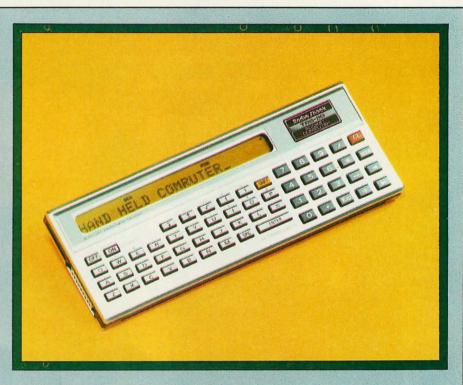
Another Pocket Computer

The internal architecture of the TRS-80 Pocket Computer is radically different from the other pocket computers now reaching the market. Instead of a single 8-bit microprocessor (such as that used in the Ouasar/Panasonic HHC and the Sinclair ZX-80), the designers of the TRS-80 Pocket Computer (Sharp Electronics of Japan) decided to use two 4-bit microprocessors in a unique serial configuration.

Both microprocessors are custom CMOS (complementary metal-oxide semiconductor) integrated circuits with built-in ROM (read-only memory). The purpose of microprocessor 1 is to arrange data and make decisions. It reads the data that is keyed in or fetched from programmable memory. It is also responsible for parsing arithmetic operations and interpreting the syntax of BASIC statements. It then arranges the data and provides instruction codes to microprocessor 2 through a transfer buffer. The actual execution of an instruction is performed by microprocessor 2, which also updates the display and notifies microprocessor 1 that it has finished its function. The respective duties of the microprocessors are listed at right.

Memory Organization

The programmable memory of the TRS-80 Pocket Computer is contained in four integrated circuits. There are three memory ICs, each containing 512 bytes of programmable memory. The three ICs which drive the liquid-crystal display each contain 128 bytes of programmable memory. Putting it all together, you end up with 1920 bytes of programmable memory. After you subtract memory space used for the transfer buffer, input buffer, display buffer, fixed mem-



Microprocessor 1

Key input routine

Acknowledgment of the remaining program

One instruction to one program step incorporation

Interpreter:

Program execute statement Cassette control statement Command statement Printer control (reserved)

Execution of manual operation

Power shut-off control

Clock stop control

Microprocessor 2

Display processing routine Input buffer Computational result Error

Arithmetic routine

Character generator

Cassette routine

Print routine

Buzzer

Recognition of printer (reserved)

Power off

Clock stop

ories, and reserved keys, you end up with 1424 bytes of user-addressable memory. Into this space you can easily fit a BASIC program of around 250 lines (average length)...SM

"Here's the best news yet about Fluke Digital Multimeters.

Now you can carry one home."

Right now, in selected electronics supply stores across the country, Fluke is introducing a new line of low-cost DMM's: the Fluke Series D. With their distinctive dark cases and full range of accessories, these five DMM's are designed to meet the test and measurement needs of the uncompromising service technician, home hobbyist, student or working engineer.

hobbyist, student or working engineer.
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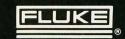
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Ciarcia's Circuit Cellar

Electromagnetic Interference

Steve Ciarcia POB 582 Glastonbury CT 06033

You may have noticed that certain household appliances such as a microwave oven or tools such as a power saw affect television reception

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when they are running. This television interference, or TVI, is caused by the electromagnetic energy which is radiated when these electrical devices are in use. The general term used to describe such noise is EMI (electromagnetic interference).

EMI emanates from both natural

and artificial sources. Natural terrestrial EMI sources include lightning discharges, precipitation, and storms. Man-made EMI can come from electrical-power systems, rotating electrical machinery, gaseous-discharge systems, and electronic equipment such as radar, computers,



Photo 1a: To illustrate the effects of radiated and coupled interference, a portable TV set is placed next to an operating TRS-80 Model I computer. The result is a very snowy picture, primarily the result of radiated noise. Also note a slight blurring of the characters on the TRS-80 display screen. A beat frequency caused by magnetic coupling between the two video displays causes the TRS-80 screen image to shake. In a longer exposure, the characters would be illegible.

and television transmitters. Natural EMI is usually beyond man's control, and attempts to reduce it must be centered on the susceptible equipment. Man-made EMI, on the other hand, can be suppressed at the source—this is the most satisfactory way to eliminate interference.

Various forms of EMI are a major concern today due to the rapid growth of digital electronic processing in business, industrial, and home environments. My mail has been overflowing with questions on computer-related interference. The letters have been almost evenly divided between readers who require help in cutting down the EMI emitted from their computers and those concerned with their computers' own susceptibility to noise.

The problem has received considerable news coverage lately, due to the FCC's (Federal Communications Commission's) stepping in to regulate noise emissions from personal com-

The relative effect of capacitive coupling of noise is dependent upon the distance between conductors.

puters and other electronic equipment. In the past, only equipment intended for certain military applications had to meet EMI limitations. The few EMI filters that were installed were primarily intended to protect the equipment in which the filters resided from the effects of EMI generated by external sources, entering through the AC (alternating current) power lines.

Little if any thought was given to attenuating electrical noise which was generated within the equipment, leak-

ing out through a variety of coupling paths. Because of the large volume of complaints about EMI that have reached the FCC, the Commission has set new regulations on the maximum level of electrical noise that can be emitted from electronic equipment. These regulations took effect on January 1, 1981. (See "FCC Regulation of Personal- and Home-Computing Devices" by Terry G Mahn, September 1980 BYTE, page 180.)

But what about the equipment you own now? What if you have an immediate noise problem? Where do you start to solve the problem? How do you detect where the noise is coming from? How do you break the path between the noise source and the affected receiver? Should you put noise filters on every electrical outlet in the house? How does shielding work?

Answering all these questions could easily fill a book. However, because EMI is such a pressing prob-



Photo 1b: Demonstration of the effects of shielding. We have added a line filter to eliminate conductive interference to the setup of photo 1a. In addition, two grounded copper sheets, one under the portable TV set and one to the left of it against the side of the TRS-80 video monitor, protect the TV set from radiated noise. The results can be seen as greatly improved picture quality.



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The three forms of noise coupling are conductive, commonimpedance, and radiated-field coupling.

lem for many computer owners, I think it needs to be addressed nonetheless.

This article is intended as an introduction. While not endeavoring to cover all sources and solutions, it will outline the common causes and paths of noise and suggest possible methods for controlling interference. For that reason, I am not limiting the discussion merely to computer-generated EMI and related suppression methods. I hope the result will be a better understanding of the entire problem.

First, a few definitions:

- Noise: any electrical signal present in a circuit other than the desired signal.
- Noise Path: the coupling medium that conducts the noise from the source to the receiver.
- Interference: the undesirable effect of noise.
- •Susceptibility: the capability of a device or circuit to respond to unwanted electrical noise.
- Receiver: any circuit or device being affected by interference.

If you own a typical computer purchased before the FCC regulations went into effect, then you no doubt have noticed that it emits considerable EMI. Depending upon the manufacturer and configuration of the system, the extent of the noise may range from a little extra fuzziness in television pictures to an actual blackout of TV reception. The effect upon nearby television sets is dependent upon the level of the emitted noise, the susceptibility of the receiver, and the coupling channel which conducts the noise from the source to the receiver.

Noise Coupling

In order for noise to be a problem, there must be a noise source, a receiver that is susceptible to the noise, and a coupling channel that transmits the noise to the receiver. The relationship is shown in figure 1a.

We start to analyze a noise prob-

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*Suggested single-unit U.S. retail price. †Apple is a trademark of Apple Computer Inc. ‡TRS-80 is a trademark of Radio Shack, a division of Tandy Corp. lem by defining what the noise source is, what the receiver is, and how the source and receiver are coupled together. It follows that there are three ways to break the path:

- 1. The noise can be suppressed at the source.
- 2. The receiver can be made insensitive to the noise.

3. The amount of energy leaking through the coupling channel can be minimized.

There are three forms of noise coupling: conductive, commonimpedance, and radiated-field coupling. Figure 1b demonstrates a typical situation. In this circuit, the commutator noise generated from the

motor is both conducted along and radiated from the leads going to the motor-control circuit. Also, the motor control and the television receiving set are plugged into the same long extension cord, so they share a common line impedance. The coupling channel consists of:

- conduction on the motor powersupply leads
- radiation from the leads
- common line impedance

To eliminate the motor's influence on the TV, all three parts of the coupling path must be broken. You can apply EMI controls to any or all of these elements.

Conductive Coupling

Conductively coupled noise is often overlooked. A wire passing through a noisy environment picks up noise either by capacitive or magnetic coupling and conducts it to another circuit. A simple representation of capacitive coupling between two conductors is shown in figure 2. When the resistance from conductor 2 to ground, R, is large, the voltage coupled from conductor 1 to conductor 2 is defined as follows:

$$V_{N} = \left(\frac{C_{12}}{C_{12} + C_{2G}}\right) V_{1}$$

where C_{12} is the stray capacitance between conductors 1 and 2, C_{1G} is the capacitance between conductor 1 and ground, C_{2G} is the capacitance between conductor 2 and ground, R is the resistance from conductor 2 to ground, V_1 is the interfering voltage, and V_N is the noise voltage produced on conductor 2.

Even though this may appear small (perhaps a few microvolts), remember that some receivers amplify input signals thousands of times. A few microvolts of noise on the antenna terminals of a television set could easily be greater than the desired video signal.

Figure 3 shows the effect of conductor spacing on capacitive coupling. The coupling factor is said to be 0 dB (decibels) when the two conductors are separated by a distance equal to three times the conductor diameter (for 22-gauge wire, d=0.71 mm or about 0.028 inches); the factor decreases rapidly as the spacing increases. Separating wires reduces the capacitive coupling between them. However, little is gained by spacing

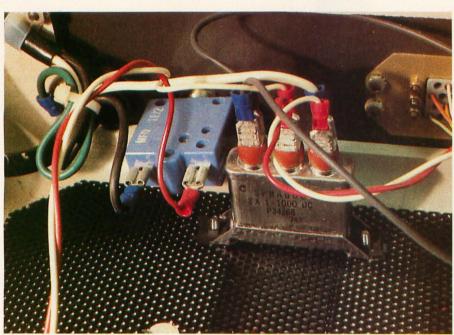


Photo 2: The simplest method of noise reduction is to use capacitors as simple filters. This photo shows two 0.1 μ F, 1000 V capacitors used to filter the AC power line in a video terminal.

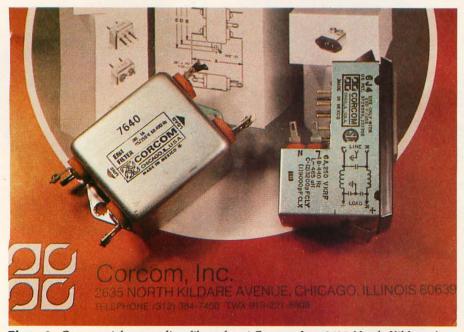


Photo 3: Commercial power-line filters from Corcom Inc, 2635 North Kildare Ave, Chicago IL 60639. Prices range from \$10 to \$20.



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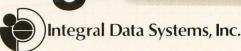
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Figure 1a: The general case of the transmission of electrical noise.

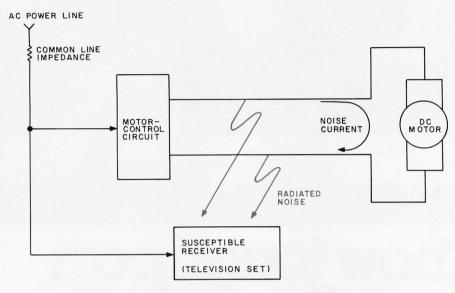


Figure 1b: A typical noise-coupling situation: commutator noise generated by the motor is conducted along and radiated from the connecting leads. Common line impedance shared by the receiver (a television set) and the motor cause motor noise to be imposed on the receiver's power input.

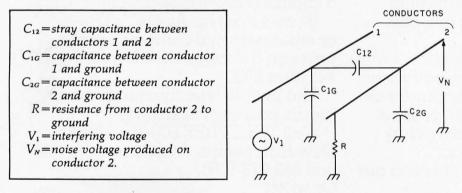


Figure 2: Representation of capacitive coupling between two conductors. The definitions of the symbols are listed above.

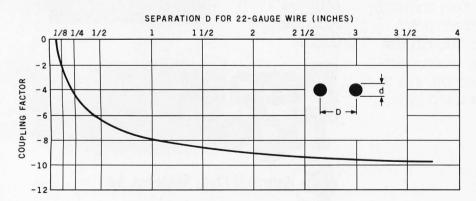


Figure 3: The relative effect of capacitive coupling of noise is dependent upon the distance between conductors. In the chart shown, for 22-gauge wire, coupling is significant only when the conductors are closer together than 25 mm (1 inch).

the conductors more than 40 diameters apart (about 25 mm or 1 inch).

Magnetic Coupling

Magnetic coupling is also a problem. When a current flows in a closed circuit, it produces a magnetic flux which is proportional to the current. If two wires are parallel, the flux produced in one wire will induce a voltage in the second wire. This induced voltage constitutes noise. When you are running wires between sensitive electronic components, avoid laying signal wires parallel to noisy, high-current AC power lines. If a signal line *must* cross a power line, have it do so at a right angle.

Common-Impedance Coupling

Common-impedance coupling occurs when currents from two different circuits flow through a common impedance. Two examples of this type of coupling are shown in figures 4 and 5. In figure 4, the ground currents of both circuits flow through a common ground impedance. The ground potential of circuit 1 is modulated by circuit 2, and vice versa. Any fluctuations in the ground current of circuit 2 will be coupled through the ground impedance, X_G , to circuit 1.

Another example is the powerdistribution schematic diagram shown in figure 5. Any change in the current required by circuit 2 will affect the voltage at the terminals of circuit 1. This effect is due to the common impedance of the power-supply lines and internal source impedance, R_s , of the power supply. Shorter leads will help reduce the line impedance, but the source impedance always remains. The typical computer system plagued with commonimpedance noise is one where the builder has attempted to use the processor power supply to run everything, including peripherals. The apparent economy is outweighed by periodic system crashes and unpredictable errors.

Radiated-Field Coupling

Radiated electric and magnetic fields provide the last form of coupling. This form of coupling can be most easily thought of as free-air radio transmission. The interfering circuit broadcasts noise just like a radio station, and every conductive surface in the receiver acts as an antenna. At close distances, the noise can in fact be much stronger than a real radio station. [Many readers



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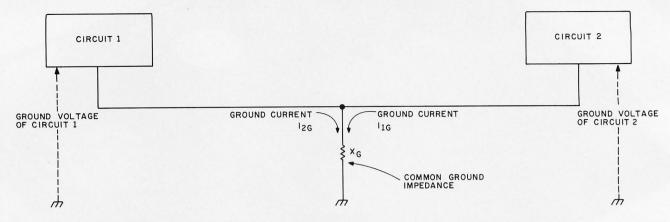


Figure 4: Common-ground-impedance coupling is caused by two pieces of equipment using the same electrical lead to ground. The ground current of one influences the ground-reference voltage of the other, and vice versa. One solution to this is a single-point grounding system.

probably know of methods for generating computer music by using an AM radio to pick up computeremitted noise while the appropriate program runs...RSS]

The characteristics of a field are determined by the source of the field and the distance between the source and the point of observation. When the receiver is near-field, closer than 1/6 wavelength, the electric and magnetic fields are considered separately. Any source/receiver distance greater than 1/6 wavelength is far-field, and the electric and magnetic fields are considered together and are called simply the electromagnetic field.

At frequencies below 1 MHz, most coupling is near-field, because the near-field boundary at the corresponding wavelengths extends out to approximately 45 meters (150 feet) or more. At 100 MHz, most coupling is far-field. For purposes of this discussion, however, radiated-fieldinterference problems within any given piece of equipment should be considered to be caused by near-field radiation unless the interference is clearly from far-field radiation.

Finding and Fixing a Noise Problem

The key to solving a noise problem is finding the source of the noise. In fact, your computer might not be the culprit. More than one computer owner has suffered complaints about his "computerized noise generator" only to later find that the real source of the interference was the solid-state light dimmer on the overhead light.

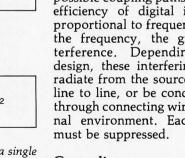
Continuous sources of noise are easier to identify than intermittent ones. The interference from appliances and computers is usually broadband, affecting the entire radiofrequency spectrum. Digital waveforms are especially rich in harmonic frequencies, as shown in figure Therefore, the continuous, harmonic-rich emissions of computers are relatively easy to find.

A standard battery-operated AM radio makes a good EMI detector. With it tuned to a frequency at which the noise is the loudest, just roam around the house looking for the place where the interference is the strongest.

If you suspect the computer, then move the radio around it and along the connecting cables. You will be surprised how much the cables contribute to radiated noise. Disconnect cables and peripheral devices selectively to further isolate interference sources. Often, the long leads between the computer and printer emit electromagnetic radiation as well as any transmitting antenna you could have possibly designed.

Finally, move the radio along the power cord you have supplying the computer system. If you are using a 15-meter (50-foot) extension cord without the ground lead connected, shortening the cord will reduce radiation considerably.

If the computer system is indeed found to be the source of the interference, there are a variety of possible coupling paths. The coupling efficiency of digital interference is proportional to frequency; the higher the frequency, the greater the interference. Depending upon the design, these interfering signals can radiate from the source, couple from line to line, or be conducted directly through connecting wires to the external environment. Each noise path



Grounding Grounding is the primary way to

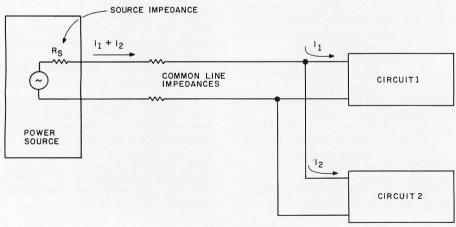
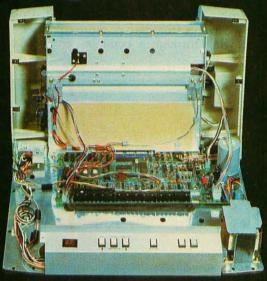


Figure 5: Common-power-source coupling occurs within a computer that uses a single power supply for multiple peripheral devices. Due to the impedances on the connecting lines, the current drawn by one circuit changes the voltage "seen" by another circuit.

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minimize unwanted noise and pickup. It is often the optimal solution to most problems. There are two basic objectives in designing proper grounding systems. The first is to minimize the noise voltage generated by currents from two or more circuits flowing through a common ground

impedance; the second is to avoid creating ground loops which are susceptible to magnetic fields and differences in ground potential. This ground is the reference point for all voltages in the system.

Signal grounds are generally classified as either single-point or



Photo 4: Switching-type power supplies, which use high-frequency pulse-width-modulated waveforms, are a potential source of noise. Most often they are contained in shielded enclosures, as in the Apple II, to eliminate possibly interfering radiation.

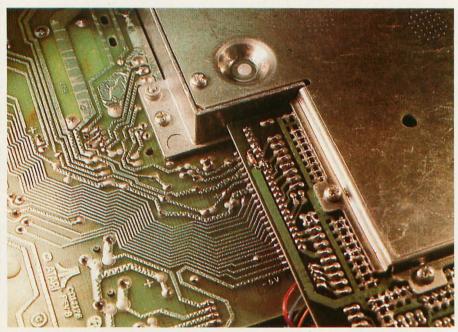


Photo 5: The Atari 400 and Atari 800 personal computers are designed to eliminate any forms of EMI coupling and to meet the new FCC standards. This requires considerable shielding. The high-frequency processor and memory sections of the printed-circuit board are segregated from the power supply and I/O (input/output) areas. A heavy-gauge aluminum enclosure encircles the high-frequency sections, as shown in this Atari 800.

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multipoint grounds. From a noisereduction point of view, the singlepoint ground is more desirable. Normally, with equipment operating at frequencies below 1 MHz, a singlepoint system is used. Above 10 MHz, a multipoint ground is best, to minimize ground impedance. Between these bounds, the type of grounding depends on the system configuration and layout. For personal computers, single-point grounding is advised.

The AC power ground is of little practical value as a signal ground. It is usually connected to signal ground as a safety measure only.

Shielding

When properly used, shielding is an effective means of reducing the coupling of noise between conductors. Shields consist of a variety of conductive materials (usually steel, copper, or aluminum), all of which serve in some way to reflect, absorb, or otherwise channel noise currents away from the protected conductor. Shields may be placed around components, circuits, complete assemblies, cables, or transmission lines.

A parallel-tuned trap cannot be used for broadband computergenerated noise.

The best way to minimize radiated noise and susceptibility on connecting wires is to use coaxial cable (coax) or shielded twisted-pair cabling between peripheral devices and the processor. If the coaxial-cable shield is grounded at one end, it will protect the central conductor from electric-field radiation. Grounding the shield at both ends creates a return current in the shield, which generates a field that cancels the conductor's electric field and any magnetic interference as well.

In twisted-pair shielded wire, grounding the shield at one end takes care of electric fields, while twisting the conductor with the return line serves to reduce magnetic susceptibility. (Twisted-pair shielded wire is especially useful on low-level signals.) The number of twists per foot determines the insensitivity to magnetic fields.

When comparing coaxial cable and shielded twisted-pair cable, it is important to recognize their differences in signal propagation, irrespective of their shielding characteristics. Shielded twisted-pair cable is very useful at frequencies below 100 kHz. Above 1 MHz the signal losses are considerable.

Coaxial cable, grounded at one end, provides a good degree of protection from capacitive pickup and can be used at all frequencies from DC (direct current) to UHF (ultrahigh frequencies). However, due to the potential for noise currents to flow through the shield (which is also part of the signal path), coaxial cable is better used at higher frequencies where such errors are minimized. Shielded twisted-pair cable, on the other hand, does not exhibit this problem and should be used for conducting low-frequency signals.

An unshielded twisted pair, unless it is balanced, provides very little protection from capacitive pickup, but can still be good for magneticfield protection. Plain untwisted-pair cable, such as the zip cord you might purchase from a hardware store, provides no electromagnetic-field protection and should be avoided if you

have a noise problem.

Multiple-conductor cables, including ribbon cables, are also available in twisted-pair configurations. A common cable used in data acquisition is a twelve-conductor shielded cable that consists of six twisted pairs surrounded by a single foil or braided shield. This cable is very expensive, however, and it is best acquired on the surplus market.

Shielding the connecting cables may eliminate only part of the problem, especially if you determine that the major source of radiation is the computer. Most computers are encased in metal chassis. If these are not properly grounded, the benefits of the metal as shielding material are lost.

On the other hand, if the computer is encased in plastic, the only solution is to coat the inside (or the outside) of the case with a conductive substance and connect it to signal ground. Aluminum foil, for example, could be used, but I suggest that you try all the other suppression measures before attempting this.

Encasing the entire computer in a conductive enclosure is not unthinkable. In fact, newer small computers such as the Atari 800 and Hewlett-Packard HP-85 are built ex-

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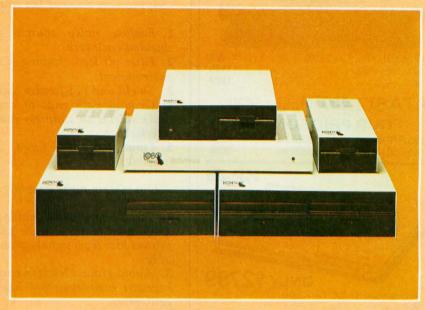
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609 Butternut Street Syracuse, N.Y. 13208 (315) 478-6800 actly that way. It is very effective in both containing the computer's electromagnetic fields and protecting the computer circuitry from external noise. When an EMI field impinges on a shield, some of its energy is reflected at the first surface, some is absorbed by the shield material, some is reflected by the second surface, and some passes through. In general the

following is true of enclosure-type shielding:

- Magnetic fields are harder to shield against than electric fields. Magnetic material should be used to shield against low-frequency magnetic fields.
- At high frequencies, a good conductor suitably shields against both elec-

Summary of Noise-Reduction Techniques

Suppressing noise at the source:

- **1.** Enclose noisy sources in a shielded enclosure.
- **2.** Filter all leads leaving a noisy environment.
- 3. Shield and twist noisy leads.
- **4.** Ground both ends of coaxial-cable shields to suppress radiated interference.
- **5.** Limit pulse-rise times where possible.

Eliminating noise coupling:

- 1. Twist and shield signal leads.
- **2.** Ground shielded leads used to protect low-level signals at one end only.
- **3.** Avoid ground leads in common between high-level and low-level equipment.

- **4.** Keep ground leads as short as possible.
- 5. Separate noisy and quiet leads.
- **6.** Use a single-point grounding system.
- 7. Avoid ground loops.
- **8.** Keep sensitive-signal leads as short as possible.

Reducing noise at the receiver:

- 1. Use frequency-selective filters where applicable.
- 2. Use shielded enclosures for sensitive circuitry.
- **3.** Provide proper power-supply filtering.
- **4.** Separate signal and hardware grounds.
- 5. Use shielded cables to protect low-level signals.

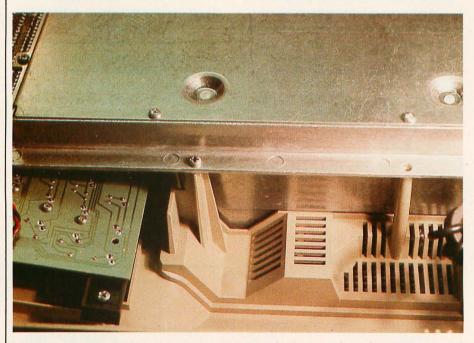


Photo 6: The underside of an Atari 800. Metal plates enclose the processor and memory. The green printed-circuit board on the lower left contains the keyboard circuit. Since it runs at low frequencies, it does not require a shielded enclosure.

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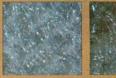
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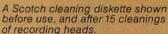
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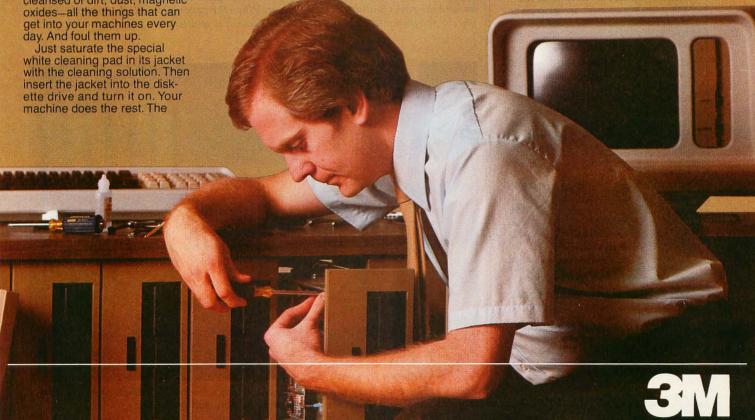
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tric and magnetic fields.

• Shielding effectiveness is increased with thicker shielding material.

•In practice, actual shielding effectiveness obtained is determined by the leakage through seams and joints, not by the shielding effectiveness of the material.

Filtering

Grounding and shielding were prescribed to eliminate noise at the source. The final measure, filtering, is applicable either at the source or at the receiver. Filtering is generally the easiest form of noise abatement. It is primarily used to reduce noise con-

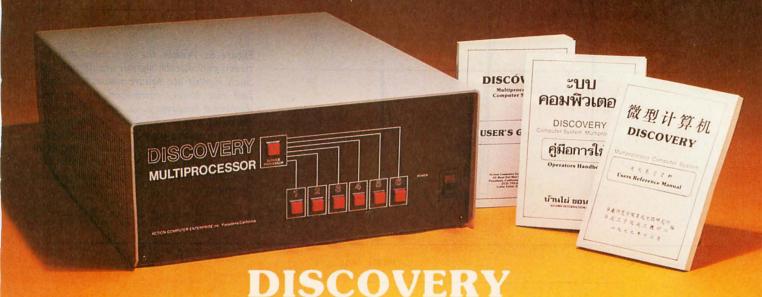


Photo 7: The Atari computers allow the user to plug in special game and business program cartridges. These ROM packs (read-only-memory modules), which are connected directly to the processor bus, must also be kept within the shield when the computer is running. This is accomplished using a special molded, %-inch (9.5 mm)-thick socket that is electrically part of the shield. A plate of aluminum with conductive gasket material around the edges is attached to the cover. When the cover is closed, the memory is completely shielded and virtually no electrical noise is emitted.



Photo 8: To reduce any high-frequency harmonics that might radiate from the videomonitor cable, a toroidal ferrite core may be wrapped in the line.

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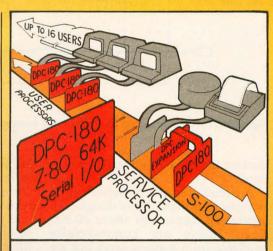
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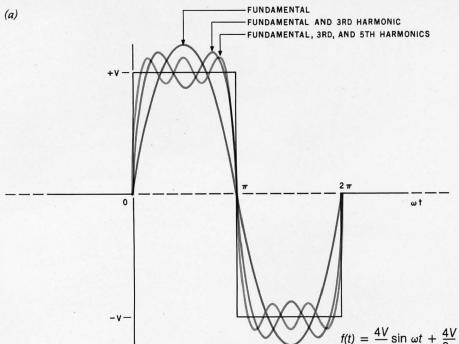
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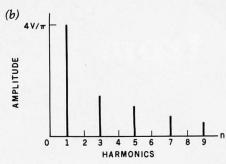
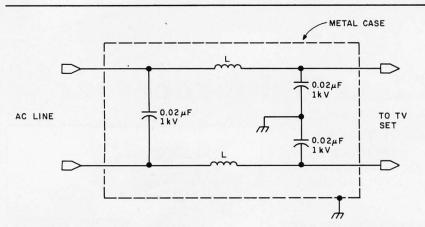


Figure 6: Within the computer and between peripherals, signals are digital. (6a) Such signals are square waves with very fast rise times, composed of the fundamental frequency, ω, and all the odd harmonics of the fundamental frequency. In a computer with a clock frequency of 8 MHz, there will be radiated noise at 8 MHz, 24 MHz, 40 MHz, etc. (6b) The amplitude becomes less at each higher harmonic.

 $f(t) = \frac{4V}{\pi} \sin \omega t + \frac{4V}{3\pi} \sin 3\omega t + \frac{4V}{5\pi} \sin 5\omega t + \frac{4V}{7\pi} \sin 7\omega t + \dots$



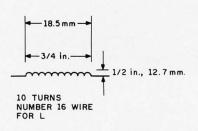


Figure 7: A simple low-pass line filter with homemade inductors.

duction into or out of the AC power lines.

A circuit used as a power-line filter is a low-pass filter ideally designed to supress all frequencies above 60 Hz. Such filters are commercially available from many sources but are also easy to construct.

If you prefer to build a simple line filter, figure 7 shows the schematic diagram of a typical circuit. This circuit is applicable for use in instances of minor television interference. It should clear up most line-coupled noise problems.

As a practical matter, simple line filters are less than ideal. Typical commercial single-section line filters use toroidal inductors and provide about 55 dB of attenuation at 3 to 5 MHz. Attenuation can be typically increased to 70 dB by adding a second

LC (inductance/capacitance) section. A line filter should be used on the computer and any susceptible receivers.

If your TV reception is still garbled or nonexistent after you install a line filter, then your set is picking up radiated noise through the antenna input. Generally, you will find the VHF (very-high-frequency) channels to be affected much more than the UHF channels. This is because most of the noise energy generated by the computer is at frequencies below 100 MHz (VHF channels 2 thru 6 are between 54 and 88 MHz). At frequencies above 470 MHz, where channel 14 starts, there isn't much energy in the noise spectrum.

The process of eliminating radiated-noise pickup starts with replacing the 300-ohm twin-lead cable

from the antenna to the television receiver with 75-ohm coaxial cable. If the problem persists after you do this, then additional filtering is in order. If the noise is determined to be a single frequency, such as that emitted from a Citizens' Band radio transmitter next door, then a parallel-tuned trap that singles out this one frequency should be used. Figure 8 shows such a filter circuit.

Computer-generated noise is broadband rather than narrow-band. A parallel-tuned trap cannot be used, and a different filtering technique must be employed. A high-pass filter on the set's antenna input may be needed. The system clock frequency of most computers is between 1 MHz and 8 MHz. Harmonics will, of course, reach much higher frequencies. The harmonic amplitude



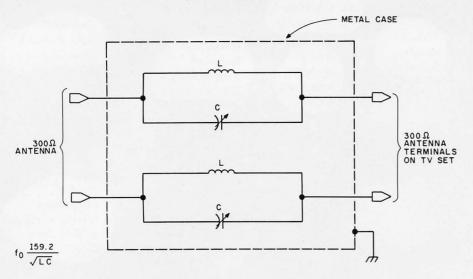


Figure 8: A parallel-tuned trap filter for use on FM-radio or television sets. Each LC combination is set for resonance at the frequency that is causing the interference. Trap filters are suitable only for eliminating narrow-band interference such as that from Citizens' Band radio transmitters.

Here, the center frequency trapped by the filter can be calculated from the equation $f_0 = 159.2/\sqrt{LC}$, where f_0 is the resonant frequency in Hertz, L is the inductance in microhenrys, and C is the capacitance in microfarads.

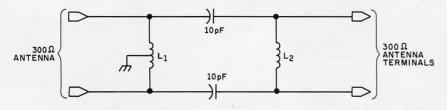


Figure 9a: A high-pass filter for use with 300-ohm antenna cable. A high-pass filter can be used on television-receiving sets and FM-radio receivers to reduce or eliminate noise at frequencies under 50 MHz, such as that produced by personal computers. These filters pass frequencies above 54 MHz (where the VHF-TV broadcast band lies) and attenuate any lower frequencies where noise may reside.

In this design, the inductors L₁ and L₂ are made from eight turns of 18-gauge wire in a coil 19 mm (¾ inch) in diameter, 25.4 mm (1 inch) long.

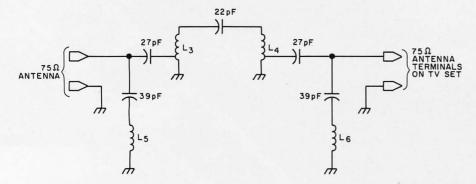


Figure 9b: A high-pass filter for use with 75-ohm coaxial antenna cable. In this design, inductors L_3 and L_3 are made from four turns of 14-gauge wire in a coil 6.35 mm ($\frac{1}{4}$ inch) in diameter and 12.7 mm (1/2 inch) long, tapped one-half turn from the end. Inductors L_5 and L_6 are made from ten turns of 22-gauge wire in a coil 6.35 mm (1/4 inch) in diameter, with the turns spaced at 3.175 per cm (8 per inch).

diminishes with each successive frequency multiplication.

If we can presume that practically all of the radiated noise is below 54 MHz where channel 2 starts, then we can construct a filter that passes only the frequencies above 54 MHz. The filter should actually be set for a cutoff frequency of 45 MHz to reduce attenuation at the desired frequencies above 54 MHz. In combination with coaxial cable, the high-pass filter usually remedies 80% of all interference problems. Figure 9 shows the schematic diagram of a typical high-pass filter.

The use of a coaxial cable, a line filter, and an antenna filter should get you out of the digital doghouse.

In Conclusion

EMI is but one of the many problems confronting computer users. I have only touched on a few of the basics in this short article, with my concern obviously centered on the effect the computer has on other equipment. I hope that I have provided you with some solutions.

The effect the environment has on the computer is an entirely different matter. You have probably noticed that I have tactfully avoided discussing things like voltage spikes, line fluctuations, frequency variations, and line interruptions. While often included in the consideration of EMI, problems of power-line performance is an entirely different subject, requiring different solutions.

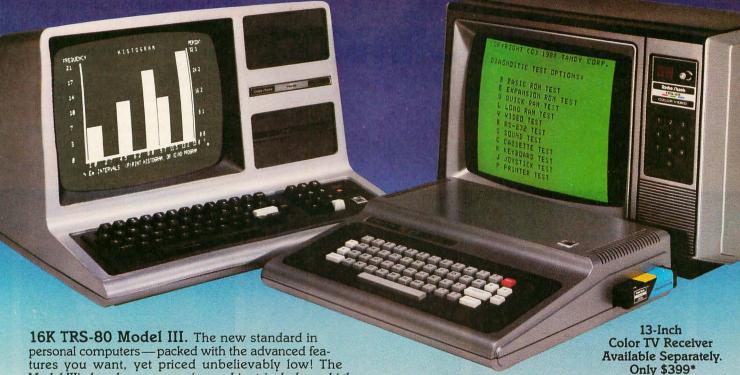
Noise filtering may improve your relations with your neighbor, and reduce the susceptibility of your equipment to transients, but it will do nothing to save you from the power company. It remains for me to cover this latter problem in a separate discussion.

Next Month:

Milton-Bradley's Big Trak is a clever toy. Wireless remote control makes it even more clever.

Editor's Note: Steve often refers to previous Circuit Cellar articles as reference material for the articles he presents each month. These articles are available in reprint books from BYTE Books, 70 Main St, Peterborough NH 03458. Ciarcia's Circuit Cellar covers articles appearing in BYTE from September 1977 thru November 1978. Ciarcia's Circuit Cellar, Volume II presents articles from December 1978 thru June 1980.

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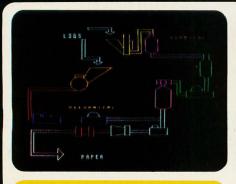
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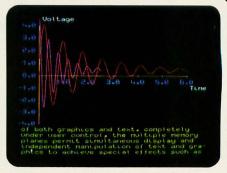
While some of the features of Terak's new 8600 can be found in other computer graphic systems, no other system in the \$5K-\$20K price class (and even those costing thousands more) provides a comparable combination of features and benefits. Features such as

Low Entry Cost The basic 8600 color system is priced at about \$15,000. It can be upgraded to higher resolution and a greater number of colors, but even fully expanded it still comes in at less than \$19,000.

Or, you can start with a black and white system for less than \$8,500 and upgrade to color at any time by the addition of a color processor and monitor.







Simultaneous Graphic/Text Display

The 8600 offers outstanding control and formatting of both graphics and text. Completely under user control, the multiple memory planes permit simultaneous display and independent manipulation of text and graphics to achieve special effects such as overlays, scrolling and zoning. This capability, in conjunction with Terak's unique flexible character generation, enables the 8600 to present visual displays that are unequalled by any other system of its class.

Broad Spectrum of Color Selection

The number of color maps and the colors in each map is completely under software control. With a 6-plane memory (640 x 480 x 6), up to 64 colors can be displayed on the screen simultaneously. With a 3-plane memory (320 x 240 x 3), up to 8 simultaneous colors can be displayed from any one of eight color maps. The output of the color map produces eight levels each for red, blue and green. The result is the selection of 512 possible levels of intensity, saturation and hue. Switching from map to map is under software

Zoning

The 8600 monitor screen can be divided into a maximum of four variable size zones. In a typical application, the upper three zones can display graphics while the lower zone displays text. The text can be scrolled or slow scrolled while the graphics are changing to coincide with the text changes.

Dual Processors For Speed and Flexibility The two 16-bit processors (each with its own memory) are assigned those tasks which they can accomplish most efficiently and with the fastest throughput. The result is more available user space in memory, faster processing and increased flexibility of operation.

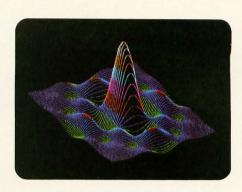
DEC Based Hardware and Software The DEC based hardware and software includes the LSI-11 main processor, RT-11 operating system and Q bus compatibility. As a result, the 8600 will support a variety of software and easily integrates peripheral devices.

USCD Pascal, Too The 8600 also supports the easy to use USCD Pascal operating system for pro-

gram development, text editing, word processing and interactive applications.

Siggraph Core Standards, 2D1 Level Graphic support is provided for USCD Pascal and RT-11 for Fortran, Basic and Pascal.

The Other Reasons? Add such things as graphics display list processing, a high resolution quadrant, four modes of display blanking, emulation, remote on-line diagnostics, etc. The list goes on and on. But to fully appreciate the system you should see one in action. We'll be happy to set up an appointment. Just contact us.



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DYNAMIC FEATURES

The 8600 also offers several dynamic features that are impossible to illustrate and must be seen to fully appreciate.

Flexible Character Generation

Unlike the rigid cell sizes of many graphic display systems, the 8600 character generation is under software control. Characters can be programmed to any size or shape including the creation and display of foreign languages such as Arabic, Hebrew, Russian, etc., mathematical symbols, primitives, specially configured letters, characters or symbols and a host of others.

Fill Algorithms

Terak's fill algorithms are fast and allows you to fill the inside of simple or complex geometric figures without calculating points. This not only helps define charts, graphs, etc., but greatly enhances the appearance of presentation material.

Smooth or Line Scrolling

The speed of the vertical, bi-directional scrolling is under operator control. It can be slowed down for text editing or speeded up for search. And, unlike most terminals that jump a line at a time, the 8600 moves in increments of one scan line. The result is a smooth moving text that is easy to read.

External Video Synch

The 8600 can be synchronized to receive externally generated RGB signals or transmit 8600 signals to external video monitors. This lets you combine and/or overlay internally and externally generated characters and graphics onto a single screen if mixing hardware is incorporated in the system.

The NEC PC-8001: A New Japanese Personal Computer

Michael Keith D46 Abbington Dr Hightstown NI 08520

C P Kocher 505 South 42nd St Philadelphia PA 19104

One of the products attracting a lot of attention at the 1980 NCC (National Computer Conference) in Anaheim, California was the PC-8001 personal computer produced by NEC (Nippon Electric Company). Because this well-made little machine has been selling briskly in Japan, NEC was trying to gauge consumer reactions to the PC-8001 that would aid them in deciding whether or not to sell it in the US.

This article is based on our evaluation of a PC-8001 that some colleagues purchased in Japan. When we first received it, we were bewildered because all the instructions and documentation were in Japanese (with only the BASIC commands in English). After several months of poking, playing, and progamming, some syllable-by-syllable transliterations of the katakana (a Japanese syllabary) instruction manual, and a few puzzled visits to Hiro, a Japanese-American co-worker, we believe that we have a good understanding of the PC-8001's most important features, its strong points, and its limitations.

Photo 1 shows the basic components of the computer. It consists of two units: a keyboard (including both the processor and memory) and

The processor is an NEC version of the Z80 running at 4 MHz.

a color monitor, and it features a 24 K-byte version of Microsoft BASIC in ROM (read-only memory). The dollar equivalent prices of the keyboard unit and monitor are \$700 and \$910, respectively. [These prices, however, may be only distantly related to the final price of the American version of this microcomputer....GW

Kevboard

The eighty-two-key keyboard has a high-quality standard English alphabet keyboard, five user-definable function keys, and a separate numeric keypad. In the normal mode, the user can enter uppercase and lowercase Roman characters; if he presses a locking shift key, he can enter characters in the Japanese katakana syllabary as well. Pressing a letter key and the nonlocking "graph" key causes one of a set of graphic characters to be displayed; this set includes bars, arcs, crosses, hearts, spades, clubs, and diamonds. (Although the katakana character set may appear useless to most American users, the characters are visually interesting and nicely augment the set of graphics characters.) All the characters available are shown in photo 2. There is also a reset button on the back of the console, so it can't be hit accidentally.

Inside the keyboard unit, the most noticeable feature is the switching power supply, which is mounted in a long, thin metal cage (approximately 38 by 6.35 by 3.175 cm [15 by 2½ by 11/4 inches]) extending along the entire rear of the keyboard enclosure. (See photo 3.) The elongated shape allows the entire power supply to be suspended over the printed-circuit board under the only portion of the cabinet that can be vented. During operation, however, the power supply remains cool.

The 22.9 by 38.1 cm (9 by 15 inch) printed-circuit board has three layers, but the center layer does not appear to be nearly as extensive as the other two layers. There are at least sixteen test-point posts staked into the board.

Most of the integrated circuits are mounted directly on the board, but the circuits that are either expensive or might have to be replaced (the memory, central processor, DMA [direct-memory access] controller, USART [universal synchronous/asynchronous receiver-transmitter], video display device, and font memory) are all in sockets. The board is easy to remove because all connections to it—power, keyboard, beeper—are made with plugs and sockets; there are no external connections or even jumpers soldered to the board.

The processor is an NEC version of the Z80 running at 4 MHz. The BASIC ROM occupies the 24 K bytes of memory from hexadecimal 0000 to 5FFF, and hexadecimal locations 6000 to 7FFF are available for an expansion ROM. Standard programmable memory extends from hexadecimal locations C000 to FFFF, with locations 8000 to BFFFF available for expansion. The board has empty sockets available for both expansion ROM and programmable memory. A time-of-day clock is included on the board (see figure 1).

The video controller is a custom NEC integrated circuit. There are two separate video output connectors on the back of the keyboard unit. A 5-pin DIN (Deutsche Industrie Norm) connector provides a baseband video signal for a black and white monitor and a similar 8-pin connector provides red-green-blue signals for a color monitor. With a black and white display, colors appear as different shades of gray.

In addition to a video-out signal and ground, the 5-pin connector provides V_{DD} (+12 V) and horizontal and vertical sync signals. The 8-pin connector provides VDD, ground, color-clock signal, horizontal and vertical sync signals, and red, green, and blue signals. Although the color monitor has an audio amplifier and speaker, the processor does not use them. The only sound made by the PC-8001 is provided by a 2-inch speaker mounted on the power supply. The user can only control the duty cycle of a fixed-frequency beeper.

Another DIN connector and an adapter cable provide an interface to any standard cassette recorder for program loading and storage. The encoding scheme is 600 bps (bits per second) FSK (frequency shift keyed) Kansas City format (which uses 1200 and 2400 Hz frequencies). This encoding scheme is very robust—unlike many computers, almost any volume setting on the tape recorder is okay.

A relay inside the console controls the tape recorder motor (or any other motor for that matter—a MOTOR command in BASIC allows a user to toggle this relay).

A 16-pin socket on the printedcircuit board serves as an RS-232C connector, while cutouts at the back of the cabinet give access to a pair of edge connectors on the board. One is for a printer and one is a DMA channel. An expansion unit is available to interface the DMA channel to up to four disk drives, two RS-232C serial



Photo 1: The NEC PC-8001 personal computer system. Shown here is the basic system: high-resolution color monitor, keyboard unit, and documentation (reference manual, BASIC manual, and BASIC reference card).

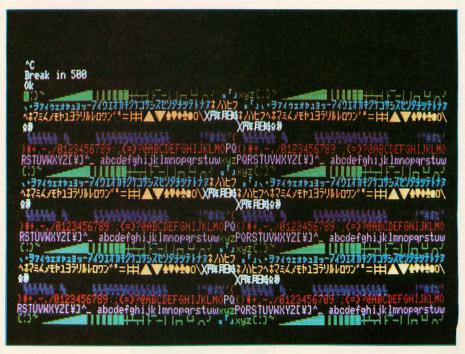


Photo 2: A display illustrating the colors and the character set on the PC-8001. In addition to complete ASCII, there are various graphics characters, control characters, and katakana characters.

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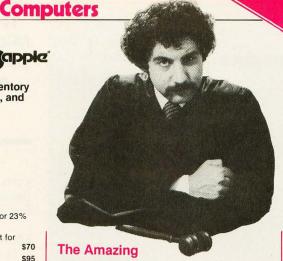
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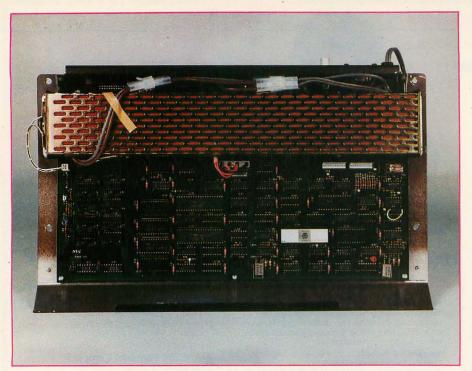
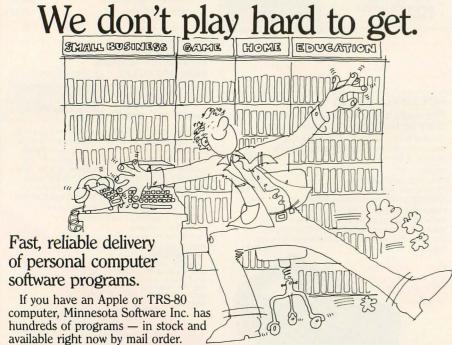


Photo 3: Inside the keyboard unit. The bottom of this photo corresponds to the front of the keyboard. Along the top edge is the power supply and, below it, the main printedcircuit board. The reset button can be seen at the rear of the keyboard near the power cord.



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N-BASIC, written by Microsoft, is a floatingpoint BASIC capable of operating in either single or double precision.

ports, a parallel port, and an IEEE (Institute of Electrical and Electronics Engineers)-488 bus (see figure 2).

Monitor

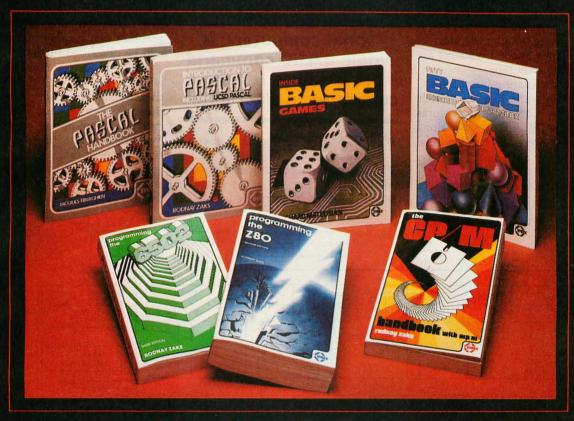
Everyone who has seen the NEC color monitor has commented favorably on its convergence and overall quality of construction. The CRT (cathode-ray tube) is a 30.48 cm (12inch) diagonal tube and has an in-line gun structure and dot screen face with 12-mil (0.012-inch) dot spacing. The deflection yoke is the precision wound torodial type. Convergence is excellent: during construction, wedges were inserted between the yoke and the neck of the tube to shim the yoke into correct alignment.

The chassis is transformer powered. Almost all the electronics are mounted on one large single-sided printed-circuit board. The horizontal scan frequency is 15,974.4 Hz, and the vertical scan frequency is 60 Hz. The monitor uses an RGB (redgreen-blue) signal interface with separate horizontal and vertical sync signals. All signals are at TTL (transistor-transistor logic) levels. Although the monitor has an audio amplifier and speaker, the audio line on the connector is tied to V_{DD} on the Z80 microprocessor. The computer generates a format of up to 80 characters per line and 25 lines, noninterlaced. The image quality is excellent, as can be seen from photo 2.

The monitor power supply apparently has some sort of time delay element, either intentionally or unintentionally, that prevents the user from turning on a set that is still warm. If you turn the monitor off and then try to turn it back on again without waiting a minute or so, the screen remains dark.

Software

As mentioned previously, the BASIC by Microsoft, called N-BASIC, is contained in three 8 K-byte ROMs. Contained within these 24 K



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bytes of ROM is a very complete BASIC, as well as a system monitor program. Advertisements in the Japanese computer magazine ASCII indicate that a number of user programs (including a color version of the ever-popular Space Invaders) are readily available on tape.

N-BASIC is a floating-point BASIC capable of operating in either single or double precision. All the features of standard BASIC are present, along with a few interesting extensions, such as:

• SWAP: exchanges value of two variables;

The PC-8001 has one feature that ought to be included in all personal computers: a single BASIC command that changes it from a computer to a terminal.

- BEEP, MOTOR: toggles beeper or motor relay;
- HEX\$: decimal to hexadecimal conversion:
- STRING\$ (X,Y): string equal to X

copies of the character with ASCII (American Standard Code for Information Interchange) code Y.

In addition, there is a whole set of graphics and display commands that will be described further.

There is also a monitor program which gives the user direct access to the Z80 machine code. After entering the monitor by typing MON, the user can test, manipulate, load or store bytes of blocks of memory using the commands in table 1.

Another useful feature of N-BASIC is the use of the ESC (escape) key on the keyboard as a pause function. It

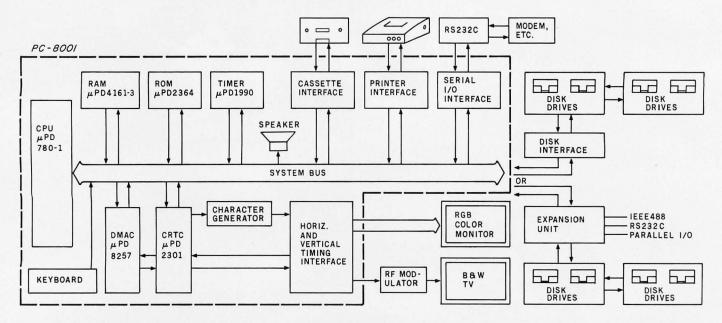


Figure 1: Block diagram of the NEC PC-8001 system. The modules within the dotted lines are contained in the PC-8001 keyboard unit.

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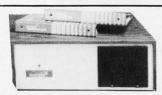
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can be used to pause in the middle of a program execution, program listing, monitor dump, and just about any other process. Pressing ESC again resumes the program or listing. This is very handy for debugging or for reading parts of a long program listing.

Finally, the PC-8001 has one

feature that ought to be included in all personal computers: a single BASIC command that changes it from a computer to a terminal. The TERM command allows the user to select either ASCII or JIS (Japanese Industry Standard) coding, parity, and clocking options. A jumper inside the keyboard unit selects data

Command	Meaning
S xxxx	displays the byte whose address is xxxx and changes it to the value to be entered
D xxxx, yyyy G xxxx	displays the hexadecimal values stored in locations xxxx to yyyy goes to byte xxxx and starts executing
W xxxx, yyyy L	writes to tape the block from xxxx to yyyy loads a stored block from tape back to memory
LV	loads a stored block from tape and verifies that it has been correctly loaded
TM control-B	tests memory and returns to BASIC returns to BASIC

Table 1: Monitor commands within the NEC PC-8001. These commands are available to the user for work in machine-language programming.

CONSOLE <T>, <N>, <K>, <M>Sets the following display parameters:

T = top line of scrolling window

N = number of lines in scrolling window

K= key list flag; if 1, displays identity of programmable function keys M= color mode: 1= color, 0= black and white

COLOR <C>, , <M>

Sets the following parameters:
C = color (or attribute in black and white mode):

In Black and White Mode: In Color Mode: Bit 0 = visibility (0 = visible)0 = black

1 = blueBit 1 = flashing (1 = flash)Bit 2 = reverse video (1 = reverse) 2 = red

3 = magenta4 = green

For example, color 6 in black and white mode would pro-5 = cyan

6 = yellowduce flashing, reverse-video characters.

7 = white

B = background character; fills the background with the character whose ASCII code is

M = mode flag, 1 = graphics mode, 0 = text mode

WIDTH < H> , < V> sets screen format (H by V); (H = 80, 72, 40, or 36; V = 25 or 20)

LOCATE <X>, <Y>

moves cursor to character position (X,Y)

PSET (<X>, <Y>, <C>)

draws a graphics dot at graphics coordinate X,Y in color C

PRESET $(\langle X \rangle, \langle Y \rangle)$

erases a graphics dot at X,Y

LINE (<X1>, <Y1>)-(<X2>, <Y2>), "<char>", <C>, [B [F]]
Draws a line from (X1,Y1) to (X2,Y2). The line is a line of text characters "char". If
"char" = PSET or PRESET, the line is a graphics line and X and Y are interpreted as
graphics coordinates. <C> is the color of the line. If present, B causes a rectangle
(block) to be drawn with (X1,Y1) and (X2,Y2) as opposite corners, and F causes the rectangle to be filled.

GET @ (<X1>, <Y1>)-(<X2>, <Y2>), X stores characters from the specified rectangular area of the screen into array X

PUT @ (<X1>, <Y17>)-(<X2>, <Y2>), X puts characters from array X to the display

Table 2: Commands for color-graphics display from within N-BASIC.

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transfer rates of either 4800, 2400, 1200, 600 or 300 baud; the function keys on the keyboard determine whether the terminal operates in halfor full-duplex modes. The only apparent deficiency is the lack of a shift lock key for the terminal mode.

Graphic and Display Features

The display features of the PC-8001 include:

- eight-color display (both text and graphics);
- 248-symbol character set (complete

ASCII, katakana, and graphics characters-lines, arcs, card symbols);

• variable screen format: (80, 72, 40, or 36 characters by 25 or 20 lines);

• two display modes: text and medium-resolution (160 by 100 pixels) graphics (these two modes can be intermixed on the same display);

• flashing, reverse video, and underlined text.

Table 2 lists the graphics and display-related extensions in the PC-8001 dialect of BASIC. These include commands for cursor positioning, changing various display parameters, and plotting points and drawing lines in gaphics mode. Two particularly worthwhile instructions are GET and PUT. GET allows the user to store the image in a specified rectangular area of the screen in an array, which can then be PUT at another location on the screen. This allows the user to define complex shapes that can then be drawn on the screen with a single instruction. Repetitive erasure and redrawing of a shape also provides a simple method of animation.

Photo 4 is a sample of what can be done with the PC-8001 graphics. This display uses most of the commands in table 2 and, in addition, illustrates the use of some of the Japanese characters for graphics purposes (the invader figures and the television speakers are made from these characters).

Problems with Video Displays

Upon further experimentation with the computer, we discovered that certain graphics operations can sometimes produce strange and unexpected results. A sampling of some of the display anomalies which can occur is shown in photo 5. The following unexpected things happen in this display:

- 1. Each column of Xs in the upperleft corner should be a different color, but after eighteen columns, the display remains in one color.
- 2. The two pairs of intersecting lines should be the same, but in the one on the left, extra areas are colored in near the intersection.
- 3. The width of the white diagonal line should stay constant, but it becomes much thicker in the middle.
- 4. The two rows at the bottom left should be all dots, but some of the dots are printed as text characters.
- 5. The figure on the right of the

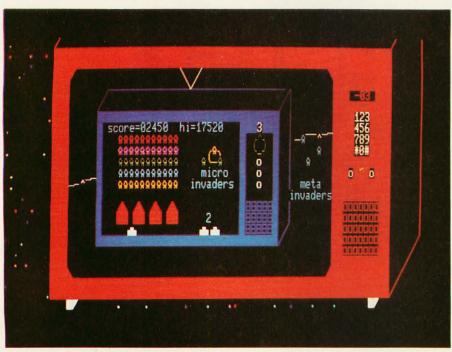


Photo 4: Sample display created on the PC-8001 by the authors. Note the use of the Japanese characters for graphics—the little invaders are actually the Japanese characters for the word "minute.

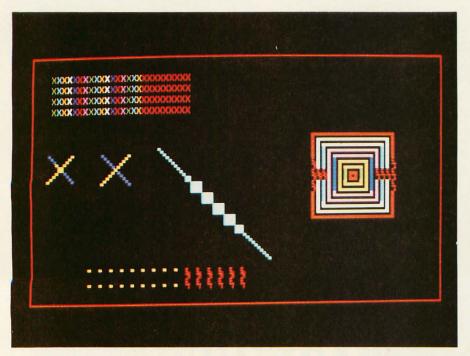
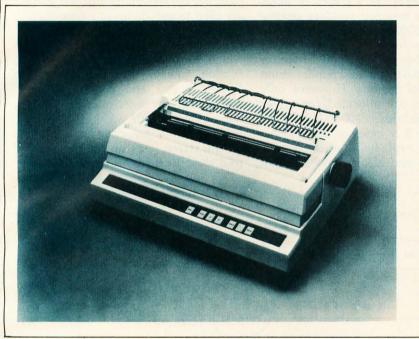


Photo 5: Illustration of some of the display restrictions of the PC-8001. See text for explanation.

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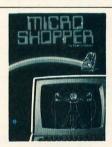
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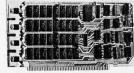


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RESEARCH & DEVELOPMENT ITD 333 Litchfield Rd., New Milford, CT 06776 display should be a ring of concentric squares, each a different color, but the line thickness varies and some dots are replaced by text characters.

The explanation for all these anomalies lies in the way the text and graphic information is represented in memory. For example, consider the full 80-character by 25-line screen format. To represent a screen of information in memory requires storage space for 2000 characters and their attributes (color, flashing, etc). At 1 byte for the character and 1 byte for its attributes this would require about 4 K bytes of memory. However, only 3 K bytes are allocated for screen storage (addresses F300 to FEB8). The way these 3 K bytes of memory are organized explains all these display anomalies and also provides insight

into a useful feature that makes the PC-8001 unique.

As shown in figure 3, each row of characters on the screen is represented by 120 bytes in memory. The first 80 of these 120 bytes contain the ASCII codes for the 80 characters in the row. The remaining 40 bytes are organized into twenty pairs. We have not determined the use of the first pair, but the remaining nineteen pairs are used to encode up to nineteen attribute fields for that row. Each pair P_i points to the beginning of the field, which runs to position $P_{i+1}-1$ (the Pi are always ordered so that $P_1 < P_2 < ...$ etc) and contains characters with attributes a_i (where a_i is the 1-byte attribute within pair P_i).

Whenever a program, in printing on the screen, uses up the first eighteen attribute fields for a row, all suc-

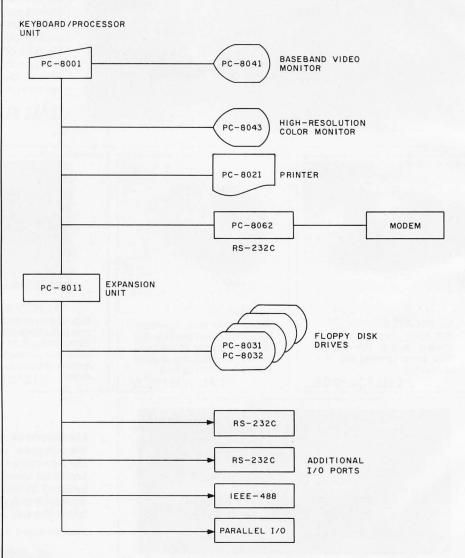


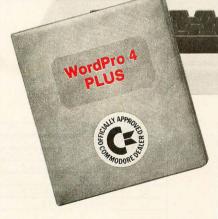
Figure 2: Interconnection block diagram of the NEC PC-8001 system. While many peripherals can be directly connected to the PC-8001, disk drives and I/O ports must be connected through the PC-8011 expansion unit.

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cessive characters on the same line that occur after these fields are given the attribute a_{19} . This is the default attribute for that row that is set to the current attributes in effect whenever a clear-screen command is received.

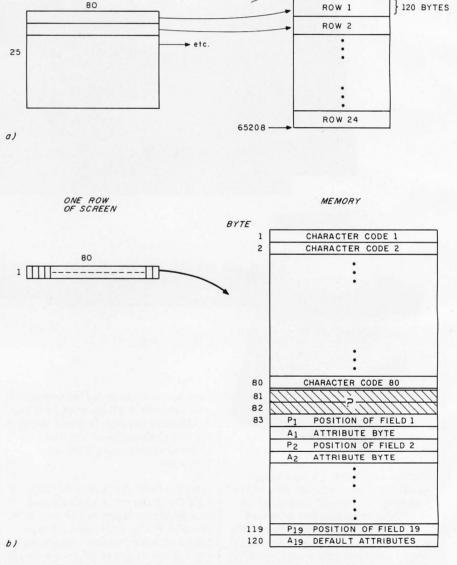
This explains the first anomaly in photo 5. After eighteen different-colored columns, the computer "runs out of colors," and the remaining columns default to red. Red is not specified in the program; it just happened to be the color in effect when the program started.

Another problem occurs when plotting color graphics because the

SCREEN

PC-8001 has character-oriented (not bit-mapped) graphics. (In this respect, it is closer to the Radio Shack TRS-80 than to the Apple II, for example.) Each character space is divided into a 4 by 2 array of cells, each of which can be "on" or "off." This provides an alternate character set consisting of the 256 possible arrays of on and off cells. When points, lines, or graphics shapes are drawn, the computer automatically converts the points to the required graphics characters and displays these, thus providing an effective graphics resolution of 160 by 100 cells.

MEMORY



62208

Figure 3: Format of the NEC PC-8001 memory-mapped video display. Figure 3a shows how each row of the video display translates into a block of programmable memory. Figure 3b shows how each 80-character row is stored in memory. A row can be broken into a maximum of nineteen fields, the position and attributes of which are described in the last 38 bytes of the memory associated with one row. All numbers shown are in decimal. See the text for further details.

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However, a problem occurs when, for example, two lines of different colors intersect. Because a character cannot be two colors at the same time, the algorithm used by the computer gives the most recently plotted points precedence. Any cells within the same character space that are already "on" are changed to the new color. Thus, an adjacent pair of horizontal lines for which different colors are specified may be displayed in either the same or different colors, depending on whether or not they lie on opposite sides of a character cell boundary. We can show that this is a

limitation of the software and not of the hardware video-controller device: the command OUT 63,41 (presumably an output to part of the videocontroller device) fills the screen with adjacent horizontal lines of different colors.

This also explains anomalies 2 and 3 in photo 5. The two crosses look different because they intersect in different positions relative to cell boundaries. The white diagonal line changes width because it crosses a black graphics rectangle. Even though the black rectangle is invisible to the casual observer, it changes the appearance of the intersecting diagonal line: every cell in each character space changes to white.

This alternative graphics-character set is selected with one of the bits in the attribute byte. This implies that the user can also "run out of graphics" on a horizontal line. This is what happens in anomalies 4 and 5 (bottom and far right of photo 5). The default attribute byte happens to specify text mode. Hence the remaining characters on the line are displayed as their text equivalents.

It is unclear why the designers chose this display approach, particularly since a full character- and attribute-mapped display would have required only 4 K bytes of memory instead of 3 K bytes. But even though this implementation imposes some restrictions on the types of displays that can be generated, it also provides an interesting capability which, to our knowledge, is not found on any other

personal computer.

This capability is a consequence of the fact that the attributes of a character on the screen are specified indirectly. That is, each character is identified with a field number which in turn is associated with an attribute byte. Thus, by a direct POKE into memory (a 1-byte change), the user can change an attribute (specifically, color) of a character or group of characters (up to an entire field) without altering the character or field codes. This allows a sophisticated method of animation called color table animation in which the user first prints a number of images in different fields on the screen, then changes the color of the fields to make each image appear in succession. As an example, we have written a BASIC program which animates a large flying saucer flying amidst a field of stars at 20 images per second. This is very fast for an interpretive BASIC animation.

Summary

The PC-8001 appears to be an attractive, well-planned, and wellmade personal computer. The graphics, though somewhat rudimentary, are more than adequate for charting, graphing, and business applications, and they can do a creditable job on many games as well. Most people who have seen our PC-8001 feel that, if it were sold in this country, it would provide strong competition for any of the color-based home computers currently being sold.

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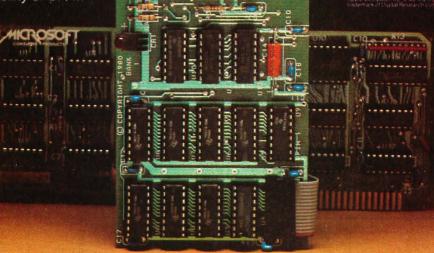
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AGRES FI

Technical Forum

SC/MP Instruction Set Summary

Professor Walter E Burton Jr Electrical Engineering Technology Department Southern Technical Institute Marietta GA 30060

If you hand-assemble or debug programs for National Semiconductor's SC/MP processor, here is a simplified instruction-set summary to speed you on your way. Table 1 contains the hexadecimal codes, the standard SC/MP mnemonics, and the SC/MP addressing modes.

Hexadecimal codes are separated into the high-order digits, which are in the left-hand column, and the low-order digits, which are in the top row. Mnemonics are located within the table. The abbreviation *PTR* refers to

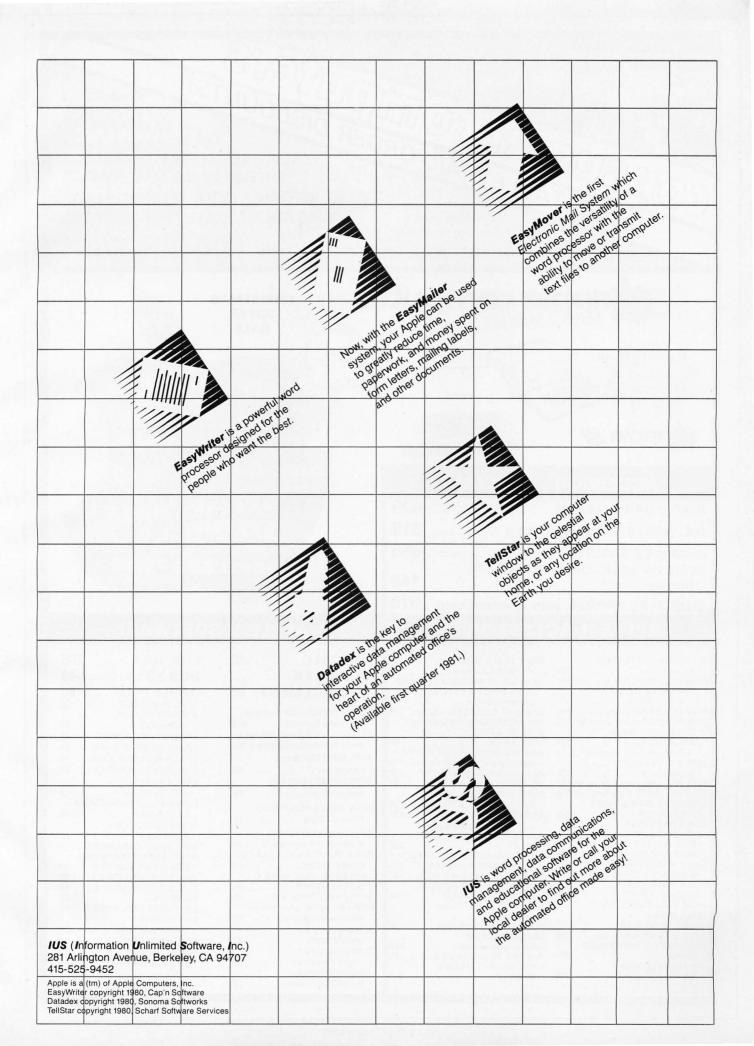
the four SC/MP pointer registers 0 thru 3. The register numbers are associated with the related instructions in the same column in table 1.

Different addressing modes associated with two-byte instructions are located along the bottom of the table. Blanks identify areas of illegal code.

As a reference I used the SC/MP Technical Description, Publication Number 4200079B (Santa Clara CA: National Semiconductor Corporation).■

	High Hexadecimal Digit	Lov	w Hexad	lecimal	Digit												
	Hexa	0	1	2	3	4	5	6	7	8	9	Α	В	С	D	Е	F
S	0	HALT	XAE	CCL	SCL	DINT	IEN	CSA	CAS	NOP							185
ion	1				THE REAL PROPERTY.						SIO			SR	SRL	RR	RRL
One-Byte Instructions	2						100										
ıstr	3	XPAL	XPAL	XPAL	XPAL	XPAH	XPAH	XPAH	XPAH					XPPC	XPPC	XPPC	XPPC
e L	4	LDE															
Byt	5	ANE								ORE							
ne-	6	XRE								DAE							
0	7	ADE		J. 1903						CAE				d August			
SI	8								Sale 1								DLY
Two-Byte Instructions	9	JMP	JMP	JMP	JMP	JP	JP	JP	JP	JZ	JZ	JZ	JZ	JNZ	JNZ	JNZ	JNZ
S S	Α									ILD	ILD	ILD	ILD				
ıst	В									DLD	DLD	DLD	DLD				
<u>-</u>	С	LD	LD	LD	LD	LDI	LD	LD	LD	ST	ST	ST	ST		ST	ST	ST
Byt	D	AND	AND	AND	AND	ANI	AND	AND	AND	OR	OR	OR	OR	ORI	OR	OR	OR
NO.	E	XOR	XOR	XOR	XOR	XRI	XOR	XOR	XOR	DAD	DAD	DAD	DAD	DAI	DAD	DAD	DAD
F	F	ADD	ADD	ADD	ADD	ADI	ADD	ADD	ADD	CAD	CAD	CAD	CAD	CAI	CAD	CAD	CAD
	PTR	0	1	2	3	0	1	2	3	0	1	2	3	0	1	2	3
	Address	PC-Relative		Indexed		Immediate		Auto-Indexed		PC-Relative		Indexed		Immediate		Auto-Indexed	

Table 1: Instruction set summary for National Semiconductor's SC/MP processor.





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DISASSEMBLER75
APPLE DOS TOOL KIT 65
PIMS Personal Information
Management System 23
ADVENTURE by
MICROSOFT27
SUB-LOGIC FS-1
Flight Simulator 34
SARGON II Chess

by HAYDEN (Cass.)..... 27

FORTRAN 16	5
DOS 3.3 4	
APPLEDICE	-
APPLE PLOT (
TAX PLANNER	65
SARGON II Chess	
on Diskette	
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SORCERER
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(specify old or new kybrd). 115
ALF MUSIC SYNTHISIZER 235
BRIGHTPEN LIGHTPEN 32
M&R SUPER-TERMINAL
80 COLUMN CARD 335
SMARTERM 80 COL 335
Mountain Computer
APPLE CLOCK/CALENDAR
card
SUPERTALKER SD200 SPEECH
SYNTHISIZER SYSTEM 245
ROMPLUS w/kybrd filter 165
INTROL/X-10 BSR REMOTE
CONTROL SYSTEM 245
CONTROL SYSTEM 245 INTROL/X-10 controller
CONTROL SYSTEM 245 INTROL/X-10 controller card only 165
CONTROL SYSTEM 245 INTROL/X-10 controller card only 165 ROMWRITER SYSTEM 155
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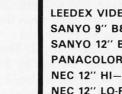




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System Review

The Sinclair Research ZX80

John C McCallum, Department of Computer Science York University, 4700 Keele St Downsview, Ontario, M3J 1P3 Canada

The new ZX80 microcomputer from Sinclair Research Ltd is a remarkable device. Although first announced to the North American public in February, 1980, the microcomputer did not become available until the fall. During the wait, the price has dropped from the expected \$245 to just under \$200. Because of this, the ZX80 is being

At a Glance

Name

Sinclair ZX80

Manufacturer

Sinclair Research Ltd 475 Main St POB 3027 Wallingford CT 06492 (617) 367-1988

Price \$199.95

Dimensions

15.9 by 20.8 by 3.7 cm $(6\frac{1}{2}$ by $8\frac{1}{2}$ by $1\frac{1}{2}$ inches)

Processor Z80A, 8-bit

System clock frequency 3.25 MHz

Memory

1 K-byte static memory, 4 K-byte system ROM (includes BASIC interpreter)

Mass storage

Uses standard cassette recorder (not included)

Other hardware features

Forty-key pressuresensitive keyboard; builtin RF (radio-frequency) modulator (for channel 2); creates video display of 24 lines of 32 characters each; includes AC adapter, cables to cassette recorder

Software

4 K-byte system ROM, which includes a BASIC interpreter and necessary internal software

Options

8 K-byte BASIC module and 16 K-byte programmable memory module (see "New Sinclair Modules" text box for details)

Comments

Contains introductory BASIC book, *A Course* in BASIC Programming, 128 pages, 20 by 14 cm (8½ by 5¾ inches) widely advertised as the first personal computer for under \$200.

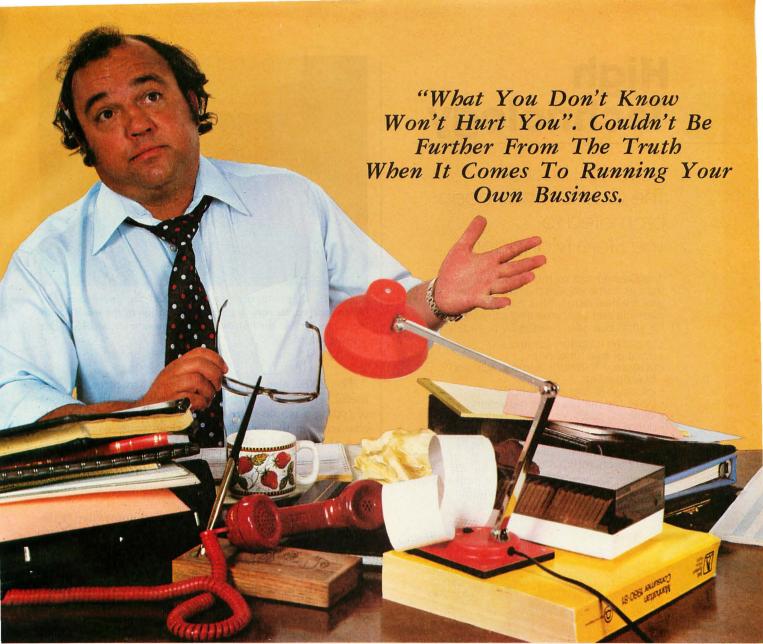
The ZX80, shown in photo 1, is a new design from Clive Sinclair, a well-known British electronics innovator. Sinclair is best known for his previous products: a miniature television, low-cost calculator and digital watch kits, and miniature stereo components. All of his products have stressed small size, low cost, and high-quality operation—usually at the expense of packaging. The same is true of the ZX80.

Can it be any good if it sells for under \$200? This is a reasonable question, but the question that is most important when buying a computer is, "Will it do the job I want it to do?" The only way to tell is to look at its features in some detail. In order to design a very low-cost computer, some features had to be cut. However, the new features that have been added are rather impressive. The good features include low price, small size, high microprocessor speed, ease of program entry, and real-time BASIC syntax checking.

The price of \$199.95 includes the assembled computer, an AC (alternating current) power adapter, a cable to connect the ZX80 to a standard television set (channel 2), connectors for a cassette recorder, and a well-written book on programming in BASIC for the ZX80. For those interested in building kits, a kit version is available. However, you will not save money by doing so, and the kit involves some steps that are rather involved for an inexperienced kit builder.

The ZX80 is small. The actual dimensions are 15.9 by 20.8 by 3.5 cm ($6\frac{1}{2}$ by $8\frac{1}{2}$ by $1\frac{1}{2}$ inches), or about the size of a hardcover book. It is not the smallest personal computer—the new pocket computers from Sharp, Panasonic, Quasar, and Radio Shack have that honor. Also, because the ZX80 has to be attached to its AC adapter and a television set to work, some of its size advantage is lost.

As part of this evaluation, several benchmark programs were run in BASIC to compare the ZX80 to other personal computers. Although the ZX80 is not as fast as advertisements imply, it does run faster than many other personal computers, including the Radio Shack TRS-80 Model I.



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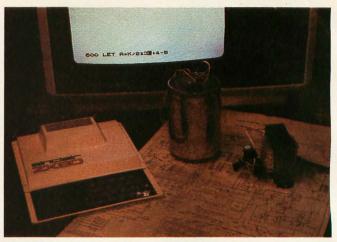


Photo 1: A photograph of the ZX80 in operation. The homemade power supply gives an indication of the small size of the computer. At the bottom of the television set, a BASIC line is being edited.

The ZX80 also has a few software features that are useful. The single-keystroke keywords mean that, instead of typing a whole word, you have to type only a single character on the keyboard. This can cause some confusion at first, and it takes some time to remember not to type the whole word. But it does speed up the typing process when entering a program. Because the keywords are stored in 1 byte each, you save memory space that can be used for extra program storage.

Another BASIC feature that I found impressive is the syntax checking of the program as you type it in. I have always been disappointed that most other versions of BASIC do not do this. The ZX80 actually prompts you with the type of input it is looking for—a keyword, a literal, a string, or a number. If you enter an illegal statement, it indicates where the statement is wrong and will not let you enter that statement into the program. It also does a similar check on input data requested by a running BASIC program. In fact, it allows you to enter simple expressions for numeric input and calculates the value while reading the value into the program; a very nice feature.

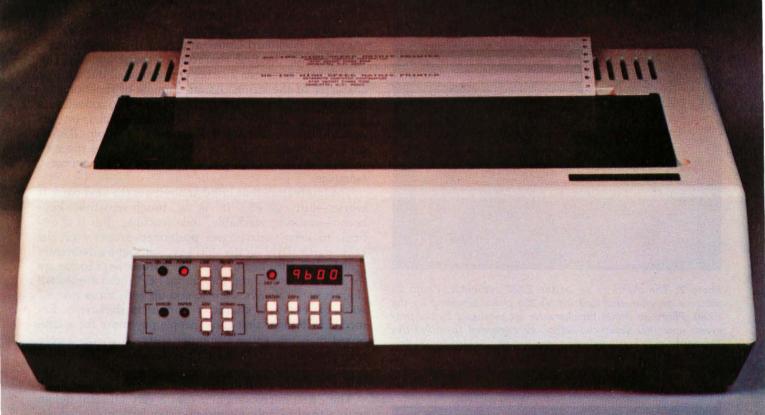
At \$200, though, everything cannot be optimum. There are objectionable features too. The most annoying or limiting features of the ZX80 are its small memory size, screen blanking during program execution, its limited BASIC, and its keyboard.

The ZX80 comes with 1 K bytes of programmable static memory, although a memory-expansion board allowing 16 K bytes of memory is expected soon (see text box). These 1024 bytes of memory are shared by system variables, your BASIC program, the program variables, working space, the video-display memory and the stack. Although the space is used very efficiently, 1 K bytes of memory do not store a large program, no matter how efficiently it is squeezed.

Perhaps the most limiting characteristic of the ZX80 is the screen-blanking behavior. When the ZX80 is executing a program, the TV screen goes black. This happens because the processor is used to control the display as well as to do the processing, and the design decision was made to have the processor devote its time to only one of these. The effect of this trade-off is to increase pro-

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Photo 2: The character set of the ZX80 computer. Photo 2a shows a program that will list all 256 characters used by the ZX80. Photo 2b shows the character set produced by the program; note that some characters are expanded to multiletter keywords and that undefined codes are represented by a question mark.



Photo 3: Editing on the ZX80. The cursor (at line 510 at the top of the screen) can be moved via arrow keys to different lines of the program. When the Edit key is pressed, the line being pointed to is copied at the bottom of the screen, where it can be edited. The cursor on the bottom line can be moved right and left; characters can be deleted or inserted at the current cursor position. When the Newline key is pressed, changes made in this line are added to the existing program.

cessing speed at the expense of limiting the interactive quality of the ZX80. It is not going to have the same types of games as the Commodore PET or the Apple II computers. However, when performing long calculations on the ZX80, it is easy to tell when the program ends—the room bursts into light!

The limited features of ZX80 BASIC are also frustrating. This is a result of the limited amount (4 K bytes) of ROM (read-only memory) available. This memory contains the software used for the BASIC interpreter, for the character generator for the TV display, for decoding the keyboard, and for cassette reading and writing. This squeeze results in many useful BASIC functions being omitted.

When dealing with strings, for example, you can break up a string using two functions: CODE gives the ASCII (American Standard Code for Information Interchange) equivalent of the first character of the string; the TL\$ (tail) function returns a string containing all but the first character of the string. As an example of functions left out, you cannot put two strings together (no concatenate operation or function exists). However, Sinclair intends to bring out an optional 8 K-byte floating-point BASIC on a single ROM. With more than double the space to work with, it should be a very rich and impressive language.

The last feature that I find annoying is the keyboard. It works—but @"#\$. It is a touch-sensitive keyboard—smooth, washable, indestructible. But it is difficult to keep your fingers positioned properly on the keys, particularly on the shift key, without inadvertently pressing an extra key or two. The hardest keys to use are the cursor controls and the rubout keys (both are shifted characters). I always seem to end up with zeros where I want to remove a character (rubout is *shift-zero*). Remember, though, that some people pay more for a keyboard than this entire computer costs. This was a very wise place to save money on the design.

Some Technical Details

The ZX80 microcomputer uses a very efficient design with a total of only twenty-two standard integrated circuits, including the voltage regulator. The main processor is a Z80A processor running at a speed of about 3.2 MHz. The programmable memory is a pair of 4 K-bit static memory devices. The ROM is a single 4 K-byte part that includes both the BASIC interpreter and the other functions listed above.

The operation of the ZX80 is—so far as I understand it—quite complicated because it works on a mix of hardware and software. The overall concept is that the refresh counter of the Z80 is used to control the generation of the lines of the video display, producing dots on the TV screen at twice the frequency of the processor clock. The keyboard is scanned under software control as I/O (input/output) port number 1, a port that is also shared by the cassette input circuitry. The cassette output signal is the same as the video synchronization signal; it is also under software control. It is an interesting design, but you will need to study the ZX80 ROM carefully before you can really understand it.

The character set is also a little strange. The keywords that are entered with single strokes are stored as single tokens and are expanded when displayed. Photo 2 shows

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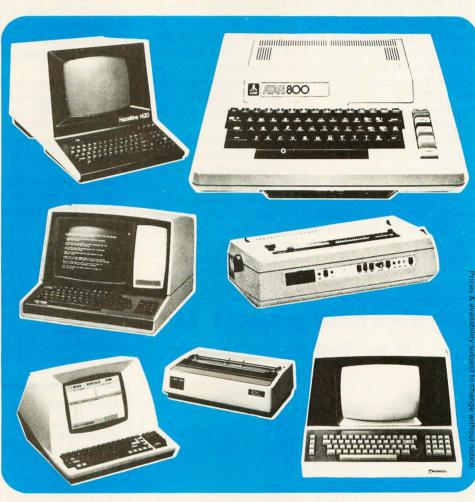
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a program for generating all 256 codes.

The high quality of the black-and-white display can be seen in the photograph of the TV screen, which is a standard 12-inch color TV set (see photo 2b). The question marks are undefined codes, and the keywords (which are spelled out) are fairly obvious. The graphics characters allow a limited 46- by 64-pixel graphics resolution. However, since the ZX80 is not primarily designed for interactive graphics applications, the existing resolution on the ZX80 should be sufficient.

Software Features

The ZX80 system is excellent for learning introductory programming concepts. This is in large part due to the immediate feedback about errors. For the student at the introductory level, the limited features of the language are useful in preventing confusion; compare this with the extreme detail taken to describe some complicated versions of BASIC. When you are ready to progress at a later time, the expanded version of BASIC will be available.

ZX80 BASIC not only prevents you from making syntax errors, but it also prompts you with a cursor that tells you what it is expecting—a keyword (denoted by a K inside the square cursor) a literal (denoted by an L), or a numeric literal (denoted by an LS). When a program is expecting string input, it puts the cursor between quotes, then expands the quotes as you enter the text. With the ZX80, you never get the string errors during data entry that are so common with other personal computers.

The method of editing programs is also well planned. A cursor, controlled by the † and ‡ cursor keys, is used to

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When text is printed, The Electric Pencil automatically inserts carriage returns where they are needed. Numerous combinations of Line Length, Page Length, Character Spacing, Line Spacing and Page Spacing allow for any form to be handled. Right justification gives right-hand margins that are even. Pages may be numbered as well as Itiled.

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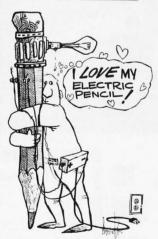
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point to the "current" line. When the Edit key is pressed, the current line moves down to the bottom of the screen to the program-entry line. There is always at least one line between the program and the text-entry line, so you will not get the areas confused.

Once the line is in the program-entry area, the line is treated exactly like a program line that you are typing except that the cursor is at the beginning of the statement. The cursor control keys ← and → are used to move the cursor within the line. Typing anything just inserts it at that point in the line, and the rubout key is used to delete the previous character. When you are finished editing, just press Newline and the edited line replaces the old line in the program (see photo 3). If you modify the line number during editing, you create a new line in the program. This feature makes it very easy to duplicate lines in

The best way to describe the features of the ZX80 BASIC language is to add to the comparison table used by Creative Computing in their "BASICs Comparison Chart" (July 1980 issue, pages 28 and 29). The major features of the Sinclair Research ZX80 4 K-byte BASIC are given in table 1.

Performance of the ZX80

At some time, all users become concerned about the speed of their computers. There is no simple way to compare the speed of various personal computers without running actual programs. Two standard benchmarks have been used to compare a wide range of computers running BASIC. These have been run on the ZX80 to get a valid estimate of its speed.

The system clock frequency of the Z80A processor is 3.2 MHz. This compares to about 1.77 MHz for the Radio Shack TRS-80 Model I or to the 4 MHz of the TRS-80 Model II, both of which also use the Z80 as the main processor. A Z80 running at 2 MHz should be

Integer variables ves; names must contain letters and numbers only, but can be any length. Real variables String variables yes; names must be one letter followed by a dollar sign (eg: A\$, B\$, ..., Y\$, Z\$). integer and one-dimensional (eg: Arrays C(N)) only; names must be one letter long and are initialized to zero values. Arithmetic operations performed on 16-bit signed integer values. +, -, *, /, ** (exponentiation)
=,>, <, on either string or integer argument pairs. Arithmetic operations Relational operations NOT, AND, OR performed on cor-Boolean operations responding bits of integer arguments. CHR\$(X), TL\$(X\$), STR\$(X\$)
CLEAR, CLS, DIM, FOR, GOSUB, GO
TO, HOME, IF, INPUT, LET, NEXT,
POKE, PRINT, RANDOMIZE, REM, String operations **BASIC** statements RETURN, STOP BASIC expressions ABS(X), CODE(X\$), PEEK(X), RND(X), USR(X) BASIC commands CONTINUE, EDIT, LIST, LOAD, NEW, RUN, SAVE Graphics 20 graphics characters; effective resolution is 46 rows of 64 squares per row, plus some graphics characters for shading

Table 1: Summary of the Sinclair Research ZX80 4 K-byte BASIC.

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Benchmark 7 Number Execution 25.3 38.5 Time (Seconds) 9.0 8.5 12.2

Table 2: Execution times of BASIC benchmark programs on the Sinclair ZX80. See text for details

similar in speed to a 6502 running at 1 MHz (as used in the Commodore PET or the Apple II). These estimations, however, do not consider the efficiency of the BASIC interpreter, which is often the most important speed factor. Thus, the execution-timing test of actual BASIC benchmark programs is the most important way of comparing the speed of various personal computers.

The ZX80 ranked between second and third places in the BASIC benchmarks done for Kilobaud magazine (see "BASIC Timing Comparisons" by Tom Rugg and Phil Feldman, October 1977, page 20). It was beaten only by a 6502 microprocessor running at 2 MHz (an Ohio Scientific Challenger II running its 8 K-byte BASIC), and by a Z80 running at 4 MHz (Zapple 8 K-byte BASIC). For those interested in the actual times of the benchmark programs, they are given in table 2.

The prime-number program used for benchmarking BASIC processors by Interface Age was also run (see "Assignment: Benchmark," by Tom Fox, June 1980, page 130). [A similar benchmark program was given in "TRS-80 Performance: Evaluation by Program Timing" by James R Lewis, on page 84 of the March 1980 BYTE....GW This benchmark is particularly interesting because it was run on several of the fastest small computers, as well as on a DEC (Digital Equipment Corporation) PDP-10 computer. The program given in the Interface Age article had to be modified slightly to allow for integer BASIC. However, the only major effect was to change an INT function to an integer multiply. The execution time for the program running on the ZX80 was

1604 seconds. Although this was not very fast compared with many of the computers in this benchmark, it was not the slowest either (the TRS-80 Model I took 1928 seconds). The execution time was decreased to 1513 seconds by removing the comment statements from the program (a 5% increase in speed). This is a typical way of speeding up BASIC interpreters.

The ZX80 might be summarized as a high-performance, very low-cost, portable personal computer system. It is best used for home or school use in learning the concepts of programming. When the memory-expansion and floating-point-BASIC modules become available (see the "New Sinclair Modules" text box), it will also be good for low-cost mathematical, scientific, and engineering applications. If you are looking for your own home computer, the ZX80 is a good starting point.

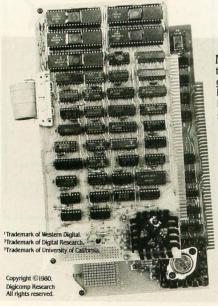
New Sinclair Modules

As this article goes to press, Sinclair Research Ltd has announced two new modules for the ZX80, an 8 K-byte BASIC in ROM and a 16 K-byte programmable-memory module. According to an American representative of Sinclair Research Ltd, the programmable-memory module and a later version of the BASIC module currently being sold in England will probably be available soon on the American market. The prices are expected to be "under \$100" for the 16 K-byte programmable-memory module and "about \$40" for the 8 K-byte BASIC module. The BASIC module will be slightly different from the one now being sold in England in that it will add printer support to the ZX80.

References

1. Davenport, Hugo. A Course in BASIC Programming-ZX80 Operating Manual. Sinclair Research Ltd, 1980.

"Personal computer looks to open up the market with an ultralow price." Electronics, Volume 54, Number 4, February 14, 1980, pages 80 thru 82.



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Multi-Micro Learning Environments:

A Preliminary Report on the Solo/NET/works Project

Dr Thomas A Dwyer, Soloworks Laboratory, University of Pittsburgh, Department of Computer Science, Pittsburgh PA 15260

Inventive Learning

It's a good idea to "back off" occasionally from the tough problems of education in the real-school world and spend some time thinking about what it would take to develop learning systems that go beyond training in the basics. In particular, it is valuable to contemplate the intricacies of some of the impressive natural-learning phenomena that surround us. For example, when a twoyear-old child startles her parents by speaking an adultsounding sentence (one recently heard was, "No garage sales today—that's ridiculous") it's worth contemplating the significance of such a minor miracle as a key to understanding later cognitive developments. In a similar manner, when a six-year-old masters the "solution" to a complex system of differential equations in the eminently practical form of learning to ride a bicycle, we should spend more than a few moments asking what made such a remarkable conquest possible.

An examination of these and similar examples of complex human learning reveals that in addition to the intrinsic (and still quite mysterious) human potential for developing an ever expanding "life of the mind," there are two important external elements at work. These elements can be described as *supportive-social* and *supportive-physical environments*. In the case of the loquacious two-year-old quoted above, the supportive-social environment was the constant flow of conversation between parents and child as they made their rounds of local garage sales in search of fun bargains. The supportive-physical environment was the set of real places that were visited as the child took part in the fascinating process of finding and acquiring some well-remembered objects, including, of course, a few toys.

The learning-to-ride-a-bicycle phenomenon is supported from the same two bases. The social environment is the neighborhood full of other kids who can handle a two-wheeler and the fun that is promised to anyone who can participate in the local rites of pedal-pushing. The physical environment is the pavement on which to pedal and of course the bicycle. When similar examples connected with older students are analyzed (eg: learning to fly an airplane solo in 10 hours), it is evident that the

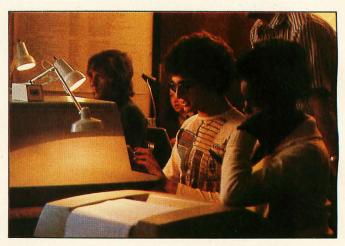


Photo 1: Students from a local high school learn to play N-Trek. The terminals being used were connected to a PDP-11 RSTS time-sharing system, with each terminal controlling a job related to a function of one starship crew member. The jobs interacted through use of shared variables in a common segment of memory.

heritage of ideas built into complex mechanisms is often a crucial part of supportive-learning environments.

It was another example of such environmentally supported human learning that triggered the idea behind the Solo/NET/works project. The example came out of something called the Soloworks project in the mid 1970s. The Soloworks project involved the use of computer technology to support a complex multiplayer version of the popular game Star Trek. (See photo 1.) Written by student Don Simon, the game was nicknamed N-Trek. This was because it allowed a variable number of players to interact in a cooperative simulation/game setting.

In its original version, N-Trek was run on a PDP-11 minicomputer time-sharing system. The general idea of the game was similar to more conventional versions, with the starship Enterprise commissioned to explore the unknown while doing battle with the evil Klingon forces. The big difference was that in N-Trek, the Enterprise really was run by a crew. Each member of this crew manned a terminal on the computer system, and depending on how the game was initialized, each crew member played a specific role. Thus, one terminal was run by the commander of the ship, another was manned by the weapons officer, a third was dedicated to navigational tasks, and so on. A separate graphics display showed the various sector maps and status tables of the game, while an added element of feedback was provided by a colored light display and a voice synthesizer that intoned such messages as "RED ALERT" or "SHIELDS UP."

All in all, the many dramatic sessions played on this system were rated as some of the best examples of environmentally supported learning that took place during the project. The word *learning* is used here with deliberation. The rules for handling the various roles in N-Trek

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2a





Photo 2: The equipment currently available in the Solo/NET/works laboratory. The terminal at the lower left in Photo 2a is used for the WAG display (as explained in the text). To its right is the IMSAI S-100 computer that emulates the unrooted-tree network and performs the managerial WAG functions. Further to the right are the system console and bus-status monitor; the other microprocessors operate as nodes in the network. Photo 2b shows MATSRCH designer Ivan Zatkovitch using an Apple II computer in a version of the game that requires only one player.

were extremely complex, yet it was possible to bring in a group of neophytes and have them playing well in very short order. The most remarkable thing about this learning was that it took place with surprisingly little explanation time; it happened mostly as a result of doing whatever was necessary to handle the task at hand. It was also a form of learning that prompted students to develop new strategies and theories. It was, to use a phrase we later coined as being particularly appropriate, *inventive learning*.

The Generalization of N-Trek

The new Solo/NET/works project (which like its predecessor is supported in part by the National Science Foundation Development in Science Education program) can be looked upon as an extension and generalization of the N-Trek experience. The goal of the project is to develop a prototype learning environment that will support a variety of multiprocess simulations.

Physically, the environment will consist of a room (or several rooms) in which there is a variety of microcomputers interconnected via a loosely coupled network. The phrase loosely coupled is used in two senses. Technically, it means that the microcomputers in the network have independent (and very likely differently designed) system buses, and that they do not share memory. Pedagogically, it is used to mean that each microcomputer node will be running an independent program (ie: process) that uses its own independent memory. The node processes will be able to cooperate, but only in ways determined by the program designers, and only via data communicated over the network.

The reason we have kept the prefix Solo in the project name is to emphasize that the student controlling a given process (which may or may not have been designed by that student) is in charge of that aspect of the overall simulation. The sharing of data and the choice of which processes are to be cooperative is to be a student-team decision, and modifications of this decision will be

viewed as an integral part of the learning process. We want the student activities to mirror the team efforts of professional scientific and engineering projects, but with strong emphasis on independent thought within a group effort.

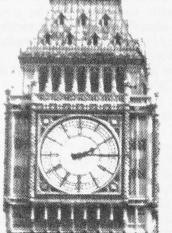
Educational Applications

The tasks we have set in the first phase of the project (1980 thru 1982) are technical in nature. The first issue we must address is that of finding simple ways to interconnect low-cost hardware in a cooperative network setting. For this reason, it is premature to talk about applications. Of course, they will eventually be the most important aspect of the project.

Our approach to applications in this first phase has been to outline scenarios describing how the system might be used, but to do most of our initial network testing with simplified surrogate applications (an example will soon follow). The purpose of the scenarios is to help us verify the accuracy and workability of the various system hardware and software decisions that must be made right away, while helping point the way to the best use of new technology sure to be available by 1982 and beyond.

One example of a scenario we have found useful is based on the use of the Solo/NET/works system to model both realistic and futuristic air traffic-control systems. In this application, some students will play the role of pilots flying a variety of aircraft. Each student will control a microcomputer at a node of the network. The principal process running in the computer at one node will be a program that simulates the flight characteristics of a given (or imagined) aircraft. The other microcomputer nodes will be manned by air-traffic controllers. The principal process running at each of these nodes will be one that interprets data returned from aircraft transponders (a transponder is an "encoded" transmitter located in an aircraft), along with data on the position of ground-based navigational aids.

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There will also be a distinguished node in the network which we call the WAG (Weltanschauung, or "world view," Generator). This will calculate all the data needed to generate a graphic display of the total universe within which these pilots and controllers function. Normally, the total WAG display will be visible only to observers or visitors who are not engaged in the simulation. However, windows on this universe appropriate to the functions at specific nodes will be available to these nodes. For example, an air-traffic controller will be given a graphic display of the aircraft in the specific sector he controls. This corresponds to the way in which radar displays are actually used today.

What will be learned by students working in such an environment? Specific learning will be in the areas of aerodynamics, navigation and geometry, piloting, and air-traffic control (for those so inclined vocationally). Also involved are large-system design, distributed computing, data-base design, and, of course, the physics and mathematics of Newtonian dynamics.

The Solo philosophy assumes that students will play an active role in the design and modification of the programs for the node processes. More importantly, we believe that the participants who design, develop, debug, and use such a system will learn to be *inventive*—to devise strategies and procedures that transcend anything that even the best teacher or text could hope to transmit.

The ultimate power of a multi-micro network is found in the fact that all the processes are run on general-purpose computers. This means that entirely new applications, and an entirely new set of challenges to be inventive, are only as far away as the imaginations of the users. We have found that visitors often suggest ingenious examples of such applications and that these represent a multitude of disciplines. Some of the other scenarios that we are working on as a result of such discussions are in the areas of corporate-business management, computer-operating systems, economic models, the colonization of space, and models of human physiology that could be used in medical education.

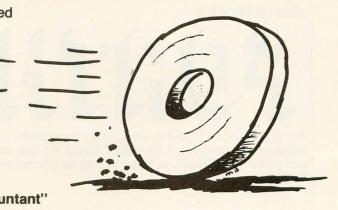
Network-Architecture Considerations

The subject of computer networking is extensive, and a substantial amount of literature detailing a variety of approaches has developed over the years. For our purposes, with our constraint to work with low-cost, off-the-shelf microcomputers, most of the options discussed in the literature were not directly applicable. It also became clear that, as with any new development, the promises of what could be done tended to be ahead of the availability of actual products. However, we spent some time thinking through the consequences of trying to apply the most recent ideas about local-area networking to our application, subject to the constraint that costs had to be minuscule compared to those associated with the commercial and scientific networks in use today.

We decided that even with this constraint, it would be advantageous to work *conceptually* with the unrooted-tree *passive-bus* configuration, considered one of the most powerful local-network architectures. Another name for this arrangement is the *global multiple-access*

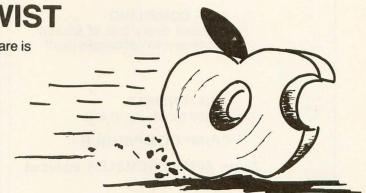
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During 1978 a set of four business systems was developed for sale in the CP/M environment under the name "Peachtree Software". Since then, this software has been used in over 3000 installations, and sold through more than 150 retail outlets. During this period, however, there was a split among the principals which resulted in a parting of the ways. It was decided one group would retain the "Peachtree Software" trademark and the second group would be restricted from competing with them for one year. We are now ready to market our business software to you under the name of "Insoft Accountant"



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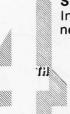




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bus. Recent applications of this architecture are the Xerox Company's Ethernet, and the Ungermann-Bass Net/One

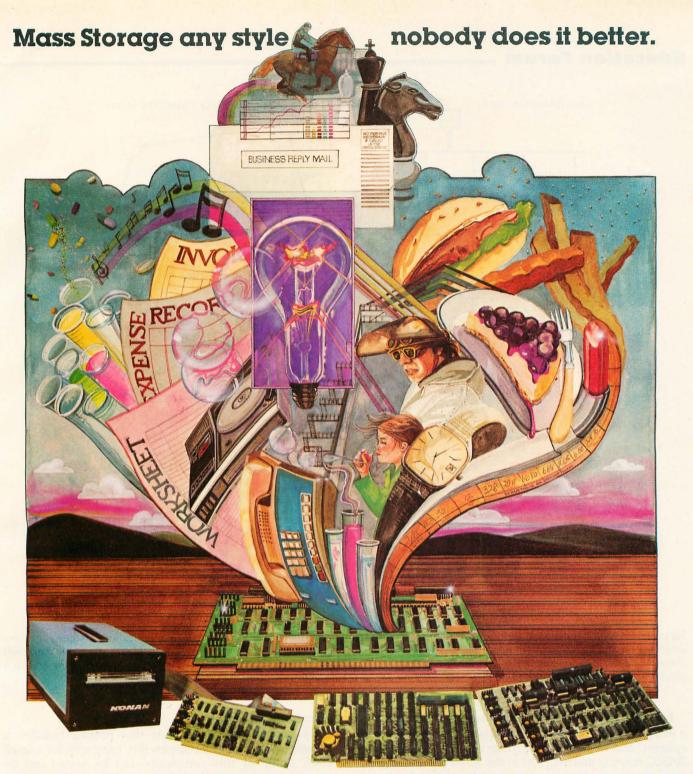
Figure 1 gives a brief summary of some of the network architectures in use today. Although the passive-bus configuration appears to lack the complexity of the others, it is in reality a very general arrangement. This is because the bus (the heavy horizontal line) is assumed to be a wideband communications medium (usually a coaxial cable) to which any node can be connected by means of a transceiver. The transceiver contains sophisticated circuitry that allows the nodes to contend for access to other nodes without waiting for their turn in a polling scheme. This circuitry also allows for flexible addressing schemes that allow the access paths in the network to be configured in any way desired. Logically, this configuration is equivalent to a fully connected distributed system, with no limitations or dependencies on which nodes are to act as control centers.

Since it is not yet possible to buy low-cost bus hardware such as transceivers off-the-shelf for use with the popular microcomputers, we are simulating the passive bus-architecture with an S-100 microcomputer. The other node microcomputers in the network connect to standard serial I/O (input/output) ports on the S-100 machine. The idea is to have a program segment running in the S-100 computer that makes these ports appear to be "taps" onto a passive bus. Actually, all communications from the nodes will be via RS-232C ports which are available at a low cost. In the spirit of limiting costs even further, we are experimenting with having the same S-100 computer also act as one of the nodes.

Hardware and Software

There are many ways to put together a system that acts like a general microcomputer network. One approach would be to use a single machine running a sophisticated operating system like UNIX (a development of the Bell System Laboratories), which allows the various users on the system to set up "pipelines" with each other. Bill Gates of Microsoft has indicated that they will soon have such a system for use on the newer 16-bit microcomputers. This product will undoubtedly be worth investigating when it becomes available.

Two other products we considered were the Nestar system and the Corvus Constellation system. The Nestar system is designed specifically for Apple computers and the Apple II bus. The Corvus system was not in use anywhere that we could visit. Although both these products are ingenious developments, we felt that with the lack of generality and experience with their use, it would not be wise to acquire the Corvus and Nestar systems at this time. This decision was further supported by our equipment-budget limitations and our desire to test the feasibility of using a variety of low-cost microcomputers as network nodes. Once we have a better feel for the capabilities of the various machines, we will not be hesitant in choosing the models that perform the best for us. It is pretty clear that trying to accommodate all the differences found in the various brands of microcomputers today can create lots of problems.



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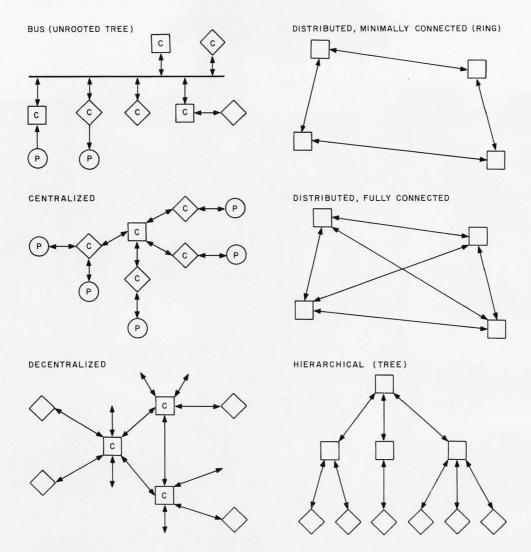


Figure 1: Examples of network architecture. A network consists of nodes that are linked through communications channels. In these diagrams, square boxes represent nodes that act as resources in the network, circles represent users of these resources, and diamonds show devices or persons that act as intermediaries (buffers, terminals, displays, etc). The letters P and C indicate that the node is a person or a computer; a blank node means that the nature of the node is not specified.

Fortunately, the lack of standardization is not as severe a problem with microcomputer languages and operating systems, and we had no misgivings about using Microsoft BASIC running under CP/M in the S-100 computer. Both products have proven to be sophisticated and reliable. Being able to count on this kind of stability has been a big plus. We may look into using the C or Pascal languages later on, but the microcomputer versions of these are still relatively new.

The simplest choice of system software for low-cost computers like the Apple, Atari, and Radio Shack's TRS-80 is to use whatever is supplied by the manufacturer. This can cause problems, however, and since it is now possible to add the CP/M-Microsoft BASIC combination to both the Apple and TRS-80, we may take this route later on. For the time being, we are trying to work with the system software supplied with each of these machines, supplementing it where necessary with bus interface programs written in machine language.

Surrogate Applications

By now it should be clear that putting together a system of this type is a complex job, especially for a small staff. Some of this complexity can be sorted out by recognizing that we (and, later on, others who wish to replicate the system) must wear three hats. The most important of these will eventually be that of the educator who uses the system. The second will be that of the application-program designer. The third is the one we are wearing most of the time at present, namely that of a multisystem designer. The job of a multisystem designer has to come first since the others build on its products. The problem is that any decisions at the system level can't be made without experience at the application level.

At this time, our strategy for dealing with this dilemma is to give consideration to a variety of educational applications, but to hold off on implementing them fully. A considerable effort in software engineering will be needed to implement the more advanced applications we have in



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mind, and for these we feel that it is wisest to confine ourselves to the highest level of the application design process at present. The catch to this strategy is that it doesn't get into the nitty-gritty detail that can have important repercussions on network-level design decisions. To handle this obstacle, we are also working with the complete design and testing of what we call surrogate applications. These are highly simplified but fairly accurate mappings of what we believe will be the essential ingredients of real applications.

The first surrogate application we have worked with is a game called MATSRCH. It was designed by Ivan Zatkovich as an undergraduate. He has since graduated and moved on to bigger and better things as a computer scientist. His application was designed to work with a minimal system in which an S-100 computer provides the network-bus function, while also handling several node

The arrangement of components used in MATSRCH is shown in figure 2 and photos 2a and 2b. The S-100 computer consists of an IMSAI mainframe equipped with an Ithaca Intersystems Z80 processor board and memory boards, and a Morrow disk controller and I/O boards. The computer runs Microsoft 5.1 BASIC under CP/M. The nodes controlled by persons P1, P2, P3, and so on, are equipped with low-cost machines such as the Apple II, the Atari 800, and the TRS-80. The processes in each of these machines are written in the BASIC supplied with the machine (usually a variant of Microsoft BASIC).

The idea of MATSRCH is to allow several players, each with his own computer, to move a spaceship through a world defined by a matrix-like coordinate system. Players issue commands that move their ships, ask for scans of the area in which they are located, and rendezvous with other ships. The program running in the S-100 computer performs three tasks: it manages the communication of data between nodes (ie: it emulates the network bus function), it keeps track of where everybody is in the matrix world of the game (supplying this information to the WAG display), and it displays bus-status information on the system console. This last function is not essential to the game, but it is a revealing way to keep tabs on where the bottlenecks in communications occur.

The present version of this simplified net monitor shows whether the S-100 program is doing network polling (and buffer management), interpreting data received from the nodes, or handling the WAG display.

The programs in the spaceship nodes are quite simple at present. They allow the players to issue commands that control the motion of their ships, and ask for information about the presence of other ships. The game limits the range that a player may ask to scan. In effect, individual nodes are able to look into small windows on the global space known to the WAG. Each node application program is also able to call upon a suitable driver program that can transmit or receive data from the bus. The programs in the nodes are actually parallel processes that cooperate in the MATSRCH game. The important point to note is that these processes can be expanded to take advantage of all the power of the microcomputer in which they reside. This is an important point; the local nodes

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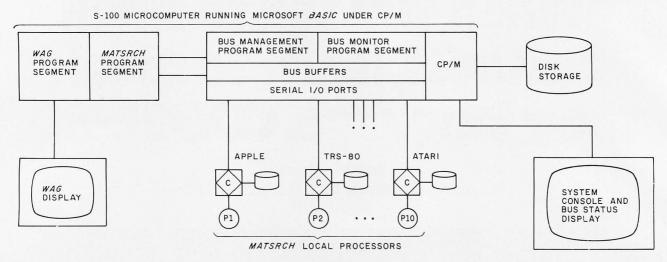


Figure 2: The hardware and software arrangement for MATSRCH. This application uses an S-100 computer (indicated at the top of the diagram) for a variety of functions: the segment labeled "BUS" is involved in emulating the unrooted-tree network shown in figure 1. Each microprocessor node has a principal function (the task assigned to that node, indicated by a square) and a driver program that handles communications (indicated by a diamond).

are not just terminals connected to a central processor.

As was noted earlier, all communications between nodes are via RS-232C serial lines. Thus, even though our work is primarily concerned with a local network, there is still the capability of connecting several schools together via telephone lines and modems. The potential

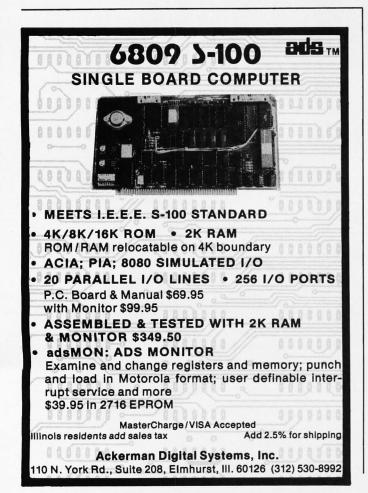
of interscholastic simulation gaming between several local high schools and colleges is intriguing, especially in terms of the higher levels of supportive social environments that could result.

Acknowledgments; Further Information

The Solo/NET/works project derives many of its ideas from its two predecessors, Project Solo and the Soloworks Laboratory. All three projects were funded in part by the Education Directorate of the National Science Foundation. Examples of early curriculum units from Project Solo were reprinted in Creative Computing in 1979 and 1980. Articles describing some of the activities of Soloworks appeared in BYTE in December 1976, August 1977, March 1978, and May 1978. A description of the educational ideas that underlie the Solo philosophy was given in the article "Books As an Antidote to the CAI Blues" which appeared in the Education Forum of BYTE in June 1980, page 74.

Documentation of the Solo/NET/works project will initially be in the form of working papers. These are for internal use only, but revised versions will later be submitted for publication in the Education Forum of BYTE. If you'd like to be placed on a mailing list for a notice of what has been published and where it appeared, send your name and address to Margot Critchfield, Department of Computer Science, University of Pittsburgh, Pittsburgh PA 15260. However, please understand that it will be some time before a complete list is available.

The material in this preliminary report is based in part on working papers by faculty associate Dr Sig Treu, and project staff members Margot Critchfield, Bob Hoffman, and Blaise Liffick. The material on the MATSRCH application was derived from a paper in preparation by Ivan Zatkovich.



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System Review

The HP-41C: A Literate Calculator?

Brian P Hayes Scientific American 415 Madison Ave New York NY 10017

Calculator vs Computer

The computer and the programmable calculator seem to be following paths of convergent evolution. As the one is made smaller while the other gains in capability, the line of demarcation between them becomes more and more arbitrary. For now at least, the programmable calculator remains a distinct and lesser species, but it shares many of the attributes of the computer. Moreover, the shared attributes are chiefly the ones that make the computer an interesting machine. Both devices offer an intimate acquaintance with the powers and pleasures of algorithms. Both exhibit an enigmatic unpredictability: the response of the machine to any given stimulus is wholly deterministic, yet the behavior of a large program



Photo 1: Components of the Hewlett-Packard HP-41C calculator system. Shown here are the calculator itself and three peripheral devices: a magnetic-card reader, a wand for reading printed bar codes, and a thermal dot-matrix printer. The peripheral units plug into four ports at the top of the calculator, which can also receive modules containing additional memory or precoded applications programs. The HP-41C alone costs about \$300; a system including all three peripheral devices and two memory or applications modules is about \$1000. (Photo by Ed Crabtree.)

can be full of surprises, often to the frustration of the programmer

The HP-41C, which was introduced by the Hewlett-Packard Company about a year ago, is among the programmable calculators that lie closest to the computer borderline. It comes close enough for the jargon of computers to be useful in describing it. At the Corvallis Division of Hewlett-Packard, where the HP-41C is made, they refer to the calculator itself as the "mainframe" and to its accessory devices as the "peripherals." The calculator comes equipped with four input/output (I/O) ports, through which the various elements of the system are interconnected. Because the peripherals do some data processing internally, the system might even be said to have "distributed intelligence."

When compared with a computer, most programmable calculators have a rich instruction set, but they are deficient in memory capacity and in facilities for communication with the user. A calculator comes with such amenities as trigonometric, logarithmic, and statistical functions built in; with a computer, even floating-point arithmetic must usually be constructed out of software. On the other hand, no calculator has the memory needed to store large tables or other data structures. And it is the communication problem that most seriously limits the utility of the calculator. A display that can represent only the 10 digits, a decimal point, and a minus sign does not have much range of expression. Even for problems that have entirely numerical results, such a display is not always adequate, since without labeling of any kind it is easy to become confused about what a number means.

The HP-41C

In the HP-41C, the instruction set is at least the equal of that in any other calculator and the potential memory space is large (although it can never be large enough). The most conspicuous distinguishing features, however, have to do with communications and "human factors" (or, in other words, those things that aid in writing programs and in interpreting their results).

All three of the peripheral units now available serve to get information into or out of the HP-41C; they are a printer, a magnetic-card reader, and a wand for reading bar codes. But perhaps the most significant innovation of all is in the calculator itself: a liquid-crystal display that can represent not only numerals but also the complete uppercase alphabet and a few lowercase letters and other

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symbols. The letterforms are crude but perfectly legible; what they bring to the calculator is literacy, and it makes all the difference in man-machine relations.

The architecture of the HP-41C is not fundamentally different from that of its predecessors in the Hewlett-Packard line. There is a four-level stack of registers where pending operands are generally held; other registers are identified by a 2- or 3-digit address. The internal memory consists of 63 registers, but this number can be increased by plugging memory modules into the ports. Each module adds 64 registers, so that a full complement of four modules yields a total capacity of 319 registers; with all the ports occupied, however, no peripheral devices can be connected.

The memory available can be divided in any way desired between data storage and program storage. When allocated to data memory, a register holds a single floating-point number (10-digit mantissa and 2-digit exponent). Program capacity is more difficult to measure because instructions have varying space requirements. Without extra memory and with a reasonable allowance for data storage, the maximum for an unassisted HP-41C usually falls between 150 and 200 program lines. By adding three modules and keeping the same data space, the program capacity is expanded to about 1200 lines.

An additional wider register is dedicated to alphabetic operations. Up to 24 characters can be accumulated in the alpha register, although only 12 at a time fit in the liquid-crystal display; the extra characters scroll in to the left, marquee-style. The alphabetic capability is not a mere frill. The extent to which it is called upon in the everyday

operation of the calculator can be illustrated by considering one of the curious challenges of calculator design.

Mnemonic Functions

The problem is that most scientific calculators have more instructions than they have keys; in the case of the HP-41C, there are more than 130 instructions and only thirty-five keys. A *shift* function doubles the number of distinguishable key sequences, but that still leaves almost half the instruction set without a home on the keyboard. Rather than further increase the number of keys or the number of shifted modes, Hewlett-Packard has adopted a solution familiar in larger systems: all instructions, whether or not they appear on the keyboard, can be executed by spelling out their mnemonic in the display. Programs resident in memory and instructions associated with peripheral devices can be executed in the same way.

Execution of a mnemonic label has the significant advantage of eliminating all dependence of the instruction set on the layout of the keyboard. It also has certain potential drawbacks that the designers of the HP-41C have gone to some lengths to remedy, largely by exploiting the alphabetic display. For example, if the spelling of a mnemonic is forgotten, a complete listing of the instruction set can be called up by the CATALOG function.

Another objection is that repeatedly spelling out a function can be tiresome on a keyboard smaller than the human hand. This burden has been relieved by the radical strategy of allowing all the keys to be redefined by the user. Any instruction (with the exception of a few program-editing pseudoinstructions) and any program can be assigned to any key.

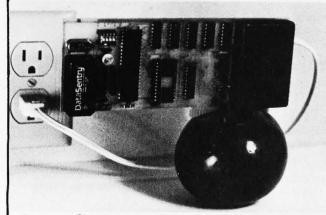
The fluid indeterminacy of the keyboard leads to a further possible complaint: the user may lose track of what function has been assigned to a particular key. Two devices come to the aid of the forgetful. A keyboard overlay slides into place to relabel the keys according to the chosen assignments; if several programs require different key assignments, a separate overlay can be made up for each one. The second aid is more elegant: the current function of any key can be verified merely by pressing the key and holding it down a moment. The mnemonic of the function appears in the display. If the key is released, the function is executed; otherwise, the word "null" appears and the command is canceled.

[A third aid to the use of the HP-41C keyboard is the selection of the user/standard mode. The key redefinitions are valid only when the calculator is in the user mode. To use a key that has been redefined for its original function, the user has only to press the USER key to toggle the calculator back to its standard mode. In the standard mode, the HP-41C behaves as it would before any keys were assigned, thus giving the user the best of both worlds. . . . GW]

Further Features for the Programmer

The versatility of the liquid-crystal display is exploited in several other ways to make the HP-41C friendly and fool-resistant. A row of indicators below the main display provides various indications of mode and status. Error messages can be reasonably explicit: an attempt to divide by 0 elicits "data error," and a number greater than 1099 is flagged as "out of range." When a conditional

REMOTE I/O

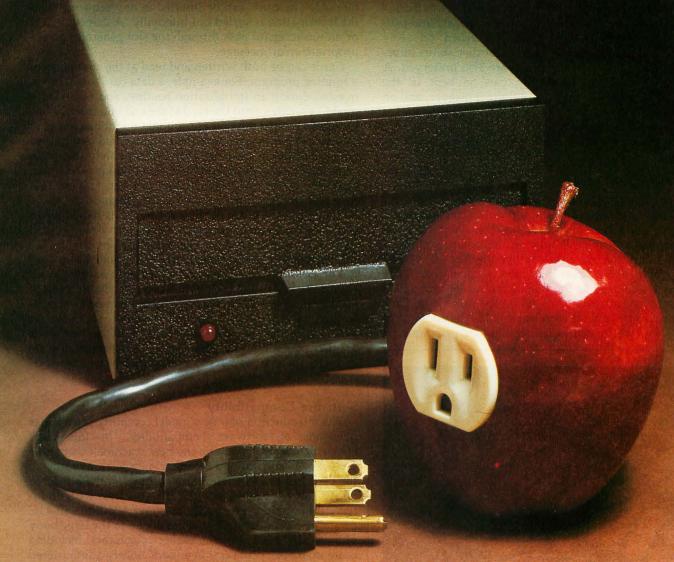


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test, such as "X = 0?", is executed from the keyboard, the display answers the question "yes" or "no."

Alphabetic text can also have a valuable role within a program. How it is employed is largely up to the programmer, but two obvious uses are prompting for inputs and labeling outputs.

Even with the best of keyboard technologies, entering a long program is inevitably tedious. A feature of the HP-41C that helps in avoiding needless repetition of effort is a continuous memory, which maintains all data and programs even when the calculator is turned off. Key assignments, the settings of flags, and other status information (such as the angular mode) are also preserved. A program that is run frequently can be kept in the calculator. Memory resources are finite, however, and on occasion a program must be cleared to make room for another and later reloaded. It is for such purposes that the magnetic-card reader and the bar-code reader are intended.

Using Cards

The magnetic-card reader, which occupies one port, is a small unit that clips onto the top of the calculator and can be left in place. The cards are the standard 1 by 7 cm magnetic strips (slightly smaller than a stick of chewing gum) that are also employed by the HP-67 and HP-97 and by some Texas Instruments calculators. They are inserted in a slot at the side of the reader and pulled through by a motor for retrieval on the other side. Each card has two tracks and each track holds the contents of 16 registers, which can be either data or programs. A

But 27/80 But 27/80 But 32/80 But 33/80 long program requires several cards, and a routine that saves the state of the entire machine sometimes calls for a whole deck of them.

Cues provided by the calculator make operations with the cards almost mindless. When writing a program onto cards, a message in the display indicates how many tracks will be needed; when reading a program, the same message gives the lowest-numbered track that has yet to be read. The cards can be inserted in any sequence, and the information is sorted out internally. A defective card or an unsuccessful pass through the slot generates an appropriate error message.

Cards can be both written and read at the command of a running program. For example, a data card might be requested during an initialization routine, and new values might be written onto the card at the end of a calculation. Or one of several possible subroutines might be appended to a running program once the program had determined which subroutine was needed. Unfortunately, all these procedures still require human intervention for the actual insertion of the card. Thus, the user must attend the machine and feed it by spoonfuls on demand.

An amusing feature of the card reader is its ability to create "private" program cards. When such a card is read back into the calculator, the program appears in the catalog and becomes available for execution, but it cannot be examined, modified, or copied onto another card. Any attempt to do so is blocked by the imperious message "private." The security measures seem to be effective (although I have not worked seriously at penetrating them); how often they will be needed is another question. In the realm of very-small-scale systems, the major worry is theft of hardware, not software.

Software Compatibility

The introduction of a new model computer often raises questions of software compatibility. In this case, Hewlett-Packard has made the new machine compatible with the old software by including a translator routine in the card reader. Magnetic cards written on the HP-67 or HP-97 can be entered into the HP-41C and, with no intervention by the user, will be converted into HP-41C programs. Thus, the machine has access to the large body of software written for the earlier calculators, including more than 3000 programs in a users' library administered by Hewlett-Packard.

An incidental benefit is the addition of more than a dozen instructions peculiar to the HP-67 and HP-97 that become available on the HP-41C whenever the card reader is plugged in, even though most of those instructions have nothing directly to do with card operations. For example, there is a block-memory swap that comes in handy occasionally.

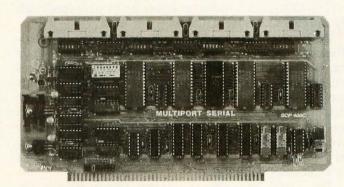
Bar-Code Wand

One drawback of magnetic-card recording is the cost of the medium: roughly fifty cents a card, plus the considerable expense of the card reader itself. There is also the delicacy of the iron-oxide surface, which necessitates careful storage and the maintenance of duplicate copies for backup. A second input device for the HP-41C, the bar-code reader, relies on the most inexpensive of all known storage media, ink on paper. The reader is a

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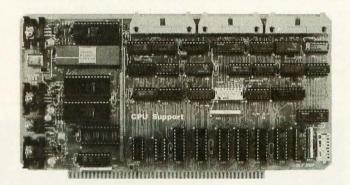


Both of these cards are IEEE 696 (S-100) compatible. They
have DIP switches for I/O port address and all options are
set with switches or easy-to-use pin shunts.

Price: 2-port, \$250; 4-port, \$350. Manual only, \$3. Cables \$15 each in 14, 18, or 28 inch lengths.

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Seattle Computer Products, Inc.

1114 Industry Drive, Seattle, WA. 98188 (206) 575-1830 hand-held wand similar to a general-purpose one introduced some months ago (the Hewlett-Packard HEDS-3000), but it has an interface and a plug specifical-

ly adapted to the HP-41C.

With programs encoded and printed by Hewlett-Packard, the wand works extremely well. A line of code can be scanned in either direction, although multiple lines must be read in sequence. The calculator display prompts for the lowest-numbered line not yet read. Even more helpful is audible confirmation. After each successful pass, the calculator emits a high-pitched beep; a failure results in a lower-pitched tone. The speed and orientation of the wand are not critical, and with practice the success rate becomes quite high.

The wand can also do a few things besides the straightforward loading of programs. Individual instructions can be executed from a "paper keyboard" (which is a table of bar codes, each of which is a single HP-41C instruction); data can be entered directly into designated storage registers; subroutines can be appended and programs merged. One wand function, instead of translating the scanned bar code into HP-41C operation codes, displays the actual binary value represented by the bars.

Printed machine-readable code is an ideal medium for the mass distribution of programs, and Hewlett-Packard will reportedly make all its software for the HP-41C available in this form. Programs from the users' library will also be offered in bar code, presumably at a lower price than programs on magnetic cards. For frequent users of such prepared software, bar code seems to be the medium of choice. The situation is somewhat different, however, for those whose main interest is in writing their own programs rather than in running other people's. The trouble is that bar code, for now, remains largely a one-way channel of communication.

It is possible to assemble by hand a bar-code representation of a program. The basic materials are adhesive labels, each bearing the code for a single instruction or a single numeric or alphabetic character. [The "paper keyboard" can also be photocopied, with a program being created by cutting and pasting photocopied bar-code keystrokes. . . . GW] A long program, however, would require several hundred labels; moreover, they must be scanned as a series of many short strokes. The ability to reproduce the program by photocopying might sometimes compensate for this inconvenience, although the wand owner's manual warns that such copies may not always give acceptable results. (Three copying machines I tried all produced readable images, although the error rate was somewhat higher than with originals.)

For those who have access to a computer system that includes a daisy-wheel printer or a plotter, Hewlett-Packard will supply programs in BASIC or FORTRAN that will generate bar code in the HP-41C format. A far more appealing method would be to produce the bar code on the printer in the HP-41C system; if that could be done, the wand might entirely displace the magnetic-card reader. The HP-41C printer can readily be made to generate patterns that superficially resemble bar codes. In several weeks of experimenting, however, I have been unable to persuade the wand to recognize those patterns

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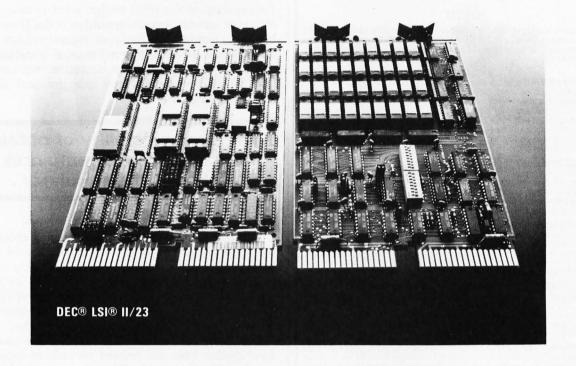
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QS SMART TERMINAL by Bob Pierce. Convert your Sorcerer to a smart terminal. Used with a and the mindle by bob fierce. Convert your sorderer to a smart terminal. Used with modem, this program provides the capability for you to communicate efficiently and save connect time with larger computers and other microcomputers. The program formats incoming data from time-sharing systems such as The Source for the Sorcerer video. Incoming data can be stored (downloaded) into a file in RAM. Files, including programs, may be saved to or loaded from cassette, listed on the video, transmitted out through your modem, or edited with an on-board text editor. Interfaces with BASIC and the Word Pac. \$49.95 Processor

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reliably. The printer output itself, which is made up of blue or purple characters, is not recognized at all by the wand, and photocopies give erratic results.

Even if the problems of color, contrast, and resolution could be solved, there would remain other impediments. The bar pattern for most of the instruction codes exceeds the capacity of the print buffer; what is more, with no means of summoning up operation codes from program memory, printing the bar-code representation of a program would necessarily entail manual translation. With the system in its present configuration, bar-code output from the printer does not seem to be practical, although it is tantalizingly close.

The mere possibility of obtaining hard copy greatly enhances the utility of the calculator . . .

The Printer

The printer is easily the most engaging component of the HP-41C system. The mere possibility of obtaining hard copy greatly enhances the utility of the calculator, since it relieves the operator of the need to transcribe results as they become available. The printer for the HP-41C does more than that: it will reproduce anything that appears in the display and much else besides.

The print mechanism is a thermal, dot-matrix one; 24-character lines are printed on rolls of heat-sensitive paper about 6 cm wide. There is a standard set of 127 characters, including full uppercase and lowercase alphabets, the ten numerals, a few Greek letters, and miscellaneous other symbols and punctuation marks. All characters can be printed in a standard 5 by 7 matrix or in a double-width format. A few of the standard calculator instructions trigger printing and, in addition, the printer has its own repertoire of about twenty-five instructions.

Programs can be listed in their entirety, or a designated number of lines can be printed out; in either case, the listing shows the same mnemonics that appear in the display. The path followed by the calculator through a program being executed can be traced, providing a record of all instructions and operands; this is a useful facility when the program does not function as expected. The contents of the operand stack can be printed out with a single command; so can the contents of all allocated memory registers, or of a defined block of registers. In addition, assignments of nonstandard functions to the keyboard and the status of all flags can be listed. All of these functions can be executed manually or within a pro-

The most commonly invoked print functions are those that print the contents of the X register (roughly equivalent to an accumulator), the alpha register, or a print buffer. The variations offered by these instructions allow the output of a program to take almost any format within the physical capabilities of the printer. The main limitations are the time and space the programmer wishes to dedicate to format commands. It is easy to list a series of variable names, each followed by a colon or an equals sign and a value. Tabulating two or three columns of numbers so they line up vertically on their decimal points



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demands a somewhat larger investment of program memory and execution time.

The dot-matrix print head is a single vertical row of print elements that sweeps across the paper forming characters as a series of columns (see table 1a). A special set of printer instructions brings this process under program control so that nonstandard characters can be created. Indeed, the printer reproduces any pattern that can be defined by a matrix 7 dots high and no more than 40 dots wide. If the pattern fits in a 7 by 7 box, it can be treated as a special character, stored in a register, and called up as needed. In principle, a complete font could be built up in this way, although its usefulness might be somewhat impaired by the limited capacity of the print buffer: only 6 special characters per line can be printed. A more practical application is the creation of schematic symbols and markers, such as playing-card suits, chess pieces, or the phases of the moon (see table 1b).

Another capability of the printer is the plotting of graphs for any function that can be expressed in the form y = f(x). The graph is drawn under the direction of a

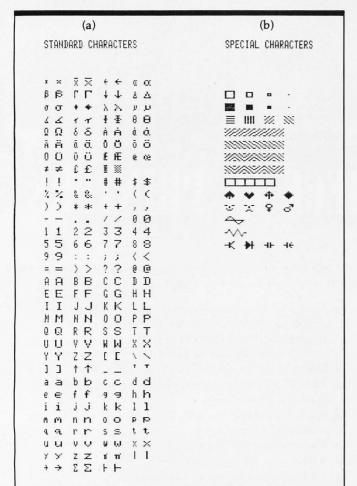


Table 1: Character set as printed by the HP-41C printer. The standard character set, shown in table 1a, contains 127 letters, numbers, and other symbols. About sixty of them, including the full uppercase alphabet, can also be represented in a somewhat different form in the display of the HP-41C itself. Each character can be printed in a standard 5 by 7 dot matrix or in a double-width format. Special characters (table 1b) can also be created by specifying the pattern of dots in each column of the character.

program called PRPLOT (print plot), which is committed to read-only memory in the printer. When PRPLOT is executed (see listing 1), it first asks the user to supply certain information that determines the form of the graph, such as the range of x and y. It then calls on a named program, also supplied by the user, that for each given value of x must return a value f(x). The resulting graphs cannot compare to the product of an x,y plotter, but they can be run off quickly and are adequate for gauging the basic form and range of a function. PRPLOT can also be executed from within a program without the prompting for input values, and various parts of it can be called independently.

Programming with Labels

An organizing principle of programs for the HP-41C is that all references and transfers of control are made by means of *labels*. The name given to a program constitutes a global label, one that can be accessed from any point in program memory. By invoking the name, a program can be called as a subroutine and can even call itself, although there are limits to such recursion.

Labels within programs are generally local, so that the same labels can be repeated in different programs without interference. Subroutine calls and branches can be made only to a label; there is no absolute addressing by line number. As a result, all programs and procedures within programs can be relocated at will. Lines can also be freely inserted or deleted without adjusting references elsewhere.

Instructions that require an address or a numerical argument can be given it either directly or indirectly. The addressing modes are uniform for all memory operations, subroutine calls, branching, loop control, the setting, clearing, and testing of flags, and even such functions as setting the display format and determining the pitch of the beeper. A subroutine is called by the XEQ (execute) function, which must be followed by a local label or the name of a program.

If the instruction is an indirect one (XEQ IND), the 2-digit number that follows is interpreted as the register where the subroutine name or label will be found. Any register, including those of the stack, can hold the indirect address. Subroutines can be nested six levels deep before the return address of the highest-level routine is lost.

Conditional tests of numerical data include various combinations of "less than," "greater than," "equal to," and "not equal to"; alphabetic strings can also be compared, but only for equivalence. All the tests have the same format, in which a false result causes the instruction following the test to be skipped. Tests of flags (set or clear) employ the same scheme. The complement of fifty-six flags seems particularly generous. Eleven flags are completely unencumbered for use in programs; the rest control the status of the HP-41C and its peripherals, thereby affording the calculator a valuable amount of self-knowledge.

Loops

The control of loops in HP-41C programs is facilitated by two instructions that store all the needed information in a single register. The instructions, ISG (increment, skip if greater) and DSE (decrement, skip if equal), refer

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directly or indirectly to a register holding a number of the form *nnnnn.tttcc*. Here *nnnnn* is the number to be tested, ttt is the value against which it is tested, and cc is the amount by which nnnnn is incremented, or decremented. The compacted form is a convenience, although I find it odd that the incremented number has a range of up to 99,999, whereas a jump must take place whenever it exceeds 999.

Other Programming Features

The HP-41C cannot realistically be said to support structured programming, not as I understand the term. The rule that all procedures should have a single entry point and a single exit, which is one of the precepts of structured programming, cannot be observed without extreme awkwardness. On the other hand, the programcontrol structures of the HP-41C strongly encourage the composition of modular programs, where each procedure is a self-contained unit, small enough to be fully understood and capable of being tested independently. In a program longer than a few hundred lines, some such technique for imposing order is obligatory.

In the end, the capabilities of the HP-41C can be exhibited best by real programs and their output. A few short utility routines and a longer program, called CHART, are given in listings 2 and 3. CHART, which incidentally shows off to good advantage the versatility of the printer, produces a bar graph, a form of display that is more appropriate for some kinds of data than the line graphs of PRPLOT.

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The main program in CHART (listing 2), which is confined to the first 20 lines, is little more than a list of XEQ statements. It first prompts the user for needed information, then does some preliminary calculations and prints a header that will identify the graph. An external program (see listing 4) is then called once for each bar; it is expected to return a value defining the length of the bar and a label of not more than 4 characters.

It is worth noting that the actual calculation of the bar length is a trivial operation. The bulk of the program is taken up with input and output routines, which are intended to minimize the burden on the user's memory and faculties of interpretation. A bar graph generated by the CHART program is shown for data on the distribution of digits obtained from the RDM LN pseudorandomnumber generator; see listing 5.

Next Generations

What more can one ask for in a programmable calculator? Quite a lot; there is much to look forward to in the next generation. More memory is always near the top of such a wish list. One way of supplying it, which might be compatible with the present mainframe, would be in a double-density memory module. The entire address space could then be utilized without filling all the

The very existence of ports inspires thoughts of other Text continued on page 136

Listing 1: Graph of the function (sin x)/x was drawn by PRPLOT, a program that resides in read-only memory in the HP-41C printer. The function itself is defined by a separate program (at bottom), which evaluates the expression each time it is supplied with a value of x and called PRPLOT.

```
PLOT OF SIN/X
    X ⟨UNITS= 1.> ↓
    Y (UNITS= E-2.) +
     -0.50
        0.00
-360.
-331.
-302. × 1
-274. ×
-245. ×
-216. × 1
-187.
        x!
-158.
-130.
-101.
 -72.
 -43.
 -14.
 14.
  43.
 72.
 101.
 130.
 158.
187.
216. ×
                                      01+LBL "SIN/X"
245. 2
                                      02 RCL X
 274. ×
                                      83 SIN
 302.
                                      84 X()Y
331.
                                      95 /
        x !
 360.
                                      06 END
```



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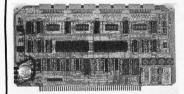
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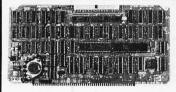


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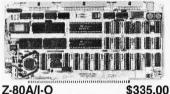
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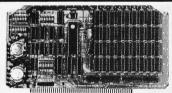
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Listing 2: A bar-graph program. CHART, the HP-41C program for generating bar graphs, is written as a series of modules. The first of these prompts the user to supply certain initial information that will determine the form of the graph. An alternative entry point, CHARTP, is intended for occasions when the bar-graph routine is called from another program; this entry point bypasses the prompting. For each bar drawn, CHART calls on a user-supplied program, which must return two items, the value to be plotted in the X register and a label for the bar no more than 4 characters long in the alpha register. The bar is actually formed in subroutine 08 out of a standard character and additional print columns for fine adjustment of the length.

01+LBL "CHART"		61 RCL 15		117 ADV	
02+LBL a		62 XEQ 10		118 RTH	
03 XEG 00	Initialization; can	63 STO 17			
04+LBL "CHARTP"	be executed from	64 5	Calculate ab1t-		
05 XEQ 01	the keyboard by	65 X<>Y	Calculate absolute		
06 XEQ 02	pressing "A."	66 X<=Y?	position of axis;		
07 XEQ "BAR"		67 ST- 17	if beyond the range of the graph, axis	119+LBL 05	
98+LBL A		68 132	the state of the s	120 0	
09 XEQ 03		69 X<>Y	is suppressed.	121 STO 10	
10 XEQ 04		70 X>Y?		122 RCL 17	
11 XEQ 05	Main calculation and	71 ST- 17		123 X=0?	
12+LBL 30	printing of bars. Calls	72 RTN		124 RTN	
13 XEQ 07	a user program whose			125 119	
14 RCL 18	name is stored in	Co INTRACTOR		126 ACCOL	
15 INT	register 11.			127 RDN	
16 XEQ IND 11				128 RCL 15	
17 XEQ 08		73+LBL 02		129 XEQ 11	
18 ISG 18		74 ADV		130 ST+ 10	
19 GTO 30		75 ADV		131 2	
20 XEQ 07		76 "P"		132 /	
21 GTO 50		77 ACA		133 -	Labels axis
		78 SF 13		The state of the s	within graph,
		79 "LOT OF "	Print identifying	134 5	if it has not
		80 ACA	header:	135 X>Y?	been suppressed.
		81 CF 13	"Plot of 'PGM NAME' "	136 GTO 52	
22+LBL 00		82 SF 12		137 RDN	
23 CF 23		83 RCL 11		138 132	
24 "PGM HAME?"		84 ACX		139 RCL 10	
25 AON		85 CF 12		149 -	
26 PROMPT		86 PRBUF		141 X(Y?	
	Subroutine that	87 RTN		142 GTO 52	
27 FS? 23	prompts for inputs.	ragina de la composición dela composición de la composición de la composición dela composición dela composición dela composición de la composición dela composición dela composición del composición dela composición dela composición del composición dela co		143 RDN	
28 ASTO 11	In each case the			144+LBL 52	
29 AOFF	prompting message			145 INT	
30 CF 22	appears in the			146 SKPCOL	
31 "NO. OF BARS?"	display but is not	88+LBL 03		147 ST+ 10	
32 PROMPT	printed. If no value	89 SF 12		148 RCL 15	
33 FS?C 22	is input following the	90 "X"		149 ACX	
34 STO 12	prompt, the program assumes the value	91 ACA		150 XEQ 12	
35 "Y MIN?"	supplied on the previous	92 7	Print labels	151 RTN	
36 PROMPT	run is still valid.	93 ACCHR	for X and Y		
37 FS?C 22	run is sim vana.	94 29	axes.		
38 STO 13		95 SKPCOL			
39 "Y MAX?"		96 "Y"		THE RESERVE OF	
40 PROMPT		97 ACA		152+LBL 07	
41 FS?C 22		98 125		153 119	
42 STO 14		99 ACCHR		154 ACCOL	Accumulates markers
43 "AXIS?"		100 CF 12		155 0	for the extrema points
44 PROMPT		101 PRBUF		156 STO 10	and the axis in spaces
45 FS? 22		102 RTN		157 XEQ 17	between bars.
46 STO 15				158 XEQ 12	
47 RTH				159 RTN	
		Sar Sar Sar			
		Section Assets		DIVERTING TO	
		103+LBL 04		SUBSTITUTES .	
48+LBL 01		104 RCL 13		160+LBL 98	
49 RCL 12	Cat	105 ACX		161 ACA	
50 1	Set up register	106 XEQ 11		162 3	
51 -	for looped calls	107 STO 10		163 SKPCOL	
52 1 E3	to user program.	108 RCL 14		164 · RDN	Master subroutine
53 /		109 XEQ 11	Labels extrema	165 XEQ 10	for accumulating and
54 STO 18		110 ST+ 10	of Y axis.	166 X<=0?	printing a bar. Checks
55 137		111.144		167 GTO 07	if the length is zero;
56 RCL 14		112 RCL 10		168 127	if so, executes LBL 07.
57 RCL 13	Calculate coefficient	113 -		169 ACCOL	Checks if the length is
58 -	relating Y-axis	114 SKPCOL		170 RDN	
59 /	scale to graph width	115 RCL 14		171 136	
60°STO 16	of 137 columns.	116 ACX			Listing 2 continued on page 134

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Listing 2 co. 172 X<=Y? 173 GTO 09 174 RDH 175 STO 10 176 XEQ 15 177 RDH 178 XEQ 16 179 127	greater than the maximum; if so, executes LBL 09. Otherwise, the bar is built up by LBL 15 and LBL 16.	204+LBL 16 205 1 206 X>Y? 207 RTH 208 X=Y? 209 RTH 210 42 211 ACCOL 212 RDH	Finishes a bar by accumulating individual	239+LBL 10 240 RCL 13 241 - 242 RCL 16 243 * 244 FIX 0 245 RND 246 FIX 2 247 RTN	Calculates the length of the bar.
180 ACCOL 181 XEQ 17 182 XEQ 12 183 RTH 184+LBL 09 185 STO 10 186 XEQ 15 187 RDH 188 XEQ 16	Special routine for a bar that must fill the entire width of	213' - 214 1 215 X>Y? 216 RTH 217 X=Y? 218 RTH 219 85 220 ACCOL 221 RDH 222 - 223 GTO 16	columns until actual length equals specified length.	248+LBL 11 249 ABS 250 SF 25 251 LOG 252 CF 25 253 INT 254 5 255 + 256, 7 257 * 258 RTN	Calculates width of a number (eg: axis or extrema labels) in number of columns.
189 127 190 ACCOL 191 ABV 192 RTN 193*LBL 15 194 7 195 XYY? 196 RTN 197 X=Y?	Accumulates the maximum integer number of gray-tone	224+LBL 17 225 RCL 10 226 1 227 + 228 RCL 17 229 X≠0? 230 X<=Y? 231 RTH 232 STO 10	Inserts space from end of bar to maximum then adds a marker for maximum Y	259+LBL 12 260 135 261 RCL 10 262 - 263 SKPCOL 264 119 265 ACCOL 266 ADV 267 RTH	Adds space to fill out a line, other than a line with a bar, then prints a Y-maximum marker.
198 RTN 199 31 200 ACCHR 201 RDN 202 - 203 GTO 15	characters (standard character 31) that will fit in the bar.	233 X<>Y 234 - 235 SKPCOL 236 119 237 ACCOL 238 RTN		268+LBL 50 269 ADV 270 ADV 271 BEEP 272 END	Beeps to mark finish.

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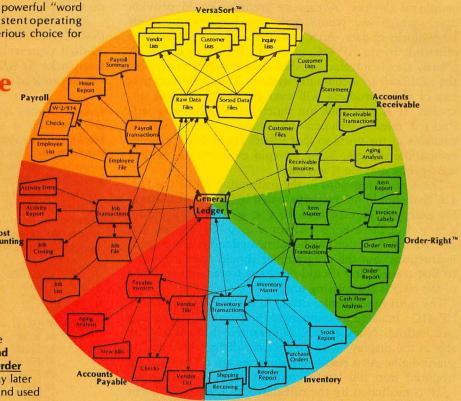
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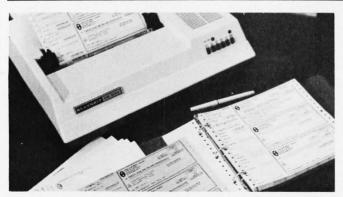
Text continued from page 130:

peripheral devices. A cassette recorder could provide mass storage and would make feasible operations on large blocks of data. An x,y plotter could be driven very efficiently by the HP-41C, albeit at a leisurely pace. With a fairly simple interface, it should be possible to connect the calculator to a computer system. The likelihood that any of these products will ever be forthcoming is unknown. It is probably too much to ask that Hewlett-Packard release technical information on the signals available at the ports so that others could develop plugcompatible devices. Some intrepid experimenter with a logic probe may do it anyway.

There are a few gaps in the instruction set of the HP-41C that should not be perpetuated in future calculators. For example, there are tests for x < y, for $x \le y$ and for x > y, but there is no test for $x \ge y$. Of course, any desired logic function can be fabricated out of the existing instructions, but the programmer should not have to go to that trouble and should not have to remember which

of the tests is the missing one.

The most fundamental defect in the architecture of the HP-41C, inadequate numerical precision, is a serious flaw indeed. Numbers are represented, both internally and in the display, with 10 decimal digits; there are no guard digits. As a result, inaccuracies are quite often introduced into the least-significant digit. For example, $(\sqrt{2})^2$ is evaluated by the calculator as 1.999999999. For operations on some data, the corruption goes still deeper and 2 or 3 digits become suspect. There is something absurd about the world's fanciest calculator not being able



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to give results accurate to more than seven or eight decimal places.

Actually, a subsidiary problem is more serious than that. Conditional tests on data are carried out on the full 10-digit representation. Consequently, a test that effectively asks "Is $(\sqrt{2})^2$ equal to 2?" will give a false result, which can lead a program far astray.

Listing 3: Utility routines for the HP-41C. These two routines are the kinds of programs that can remain in memory as resources to be drawn on by other programs, somewhat like macro instructions in an assembly language. BAR simply prints a heavy bar across the width of the paper to separate different kinds of information. TAB handles the spacing of numbers to be printed in vertical columns. It must be supplied with the number to be printed (in the X register) and the number of character spaces to be measured from the present position in the line of print to the decimal point. TAB was employed in formatting the random-number data in listing 2.

01+LBL "BAR"	01+LBL *TAB*
02 ADV	02 A8S
03 .023	03 SF 25
04 31	04 LOG
95+LBL 01	05 CF 25
06 ACCHR	06 X<=0?
07 ISG Y	07 CLX
08 GTO 01	08 INT
09 PRBUF	09 1
10 ADV	10 +
11 ADV	11 RCL X
12 END	12 3.1
	13 /
	14 INT
	15 +
	16 CHS
	17 +
	18 SKPCHR
	19 END

Listing 4: Random-number routines for the HP-41C. These two random-number generators, standard coding exercises for programmable calculators, both calculate a pseudorandom real value, then select a single pseudorandom digit for return to the calling program. RDM LC employs the standard linearcongruential method, which has virtues and failings that are well understood. In this example, R_{n+1} is equal to $[24,298R_n +$ 99,991] mod 199,017.

RDM LN is an algorithm the author stumbled upon but has not seen in the literature. R_{n+1} is defined as $1/\ln R_n$. Experimental runs of up to several thousand iterations have given good results, but the behavior of the algorithm is not understood. A sample test is shown in listing 5.

01+LBL "RDM LH"	01+LBL "RDM LC"
02 RCL 20	02 RCL 20
03 ABS	03 24298
94 LN	94 *
95 1/X	05 99991
96 STO 29	96 +
07 1 E3	07 199017
98 *	08 MOD
09 FRC	09 STO 20
10 10	10 1 E3
11 *	11 /
12 INT	12 FRC
13 ABS	13 10
14 END	14 *
	15 INT
	16 END



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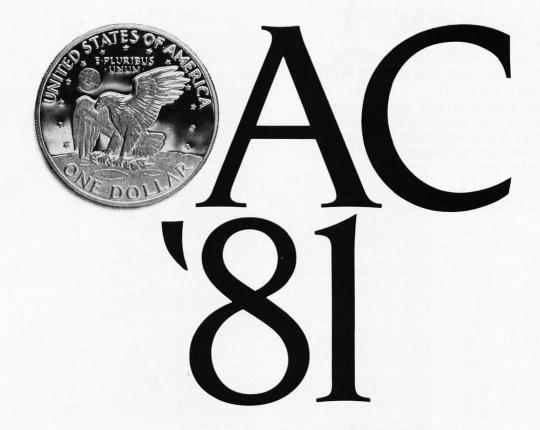


Listing 5: Bar-graph results of the CHART program, given in listing 2. The graph represents pictorially the distribution of the 10 digits in a sample of 2500 pseudorandom numbers. The numbers were generated by another program, RDM LN (shown in listing 4), with the bookkeeping done by a third program.

Test of "RDM LN"		Plot of "RANDOM"		
Number of trials 2500		**********************		
Seed = 1.2	34567890			
		X↓ Y→		
Interestation of the state of t		200.00 300.00		
		1 250.00		
		〈②〉[蓬蓬莱莱莱蒸湿]		
DIGIT	COUNTS	1 1		
MEAN =	250.00	1		
		〈2〉 産業長業監察3		
(0) =	237.	1 1		
⟨1⟩ =	259.	〈3〉謹重至蒸至		
⟨2⟩ =	234.	1 1		
⟨3⟩ =	228.	〈4〉應整業業费業務基礎報		
⟨4⟩ =	256.			
⟨5⟩ =	265.			
<6> =	268.	1 1		
⟨7⟩ =	251.	<6>		
<8> =	259.			
<9> =	243.	<7> \$\frac{1}{2} \frac{1}{2} \frac{1}		
		(8) EEK EXXXXXXX		
RUNS OF 2:	211.	1 1		
RUNS OF 3:	14.	< 9> 種種新養業業業 :		
RUNS OF 4:	3.	1 1		
RUNS OF 5:	0.			
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HIGH/LOW =				
ODD/EVEN =				

It is easy to imagine that some programmable calculator evolved from the HP-41C would have instructions much like those of a higher-level language. Having introduced named programs, the next obvious step is named variables, which would relieve the programmer of much tedious worry over memory allocation. Let the machine keep track of where the numbers are; it does so better than people can. The existing conditional tests, which act directly on particular registers, might be recast as a more general if . . . then . . . else construction, employing the named variables. Also, do . . . while and repeat . . . until commands would be a welcome addition; indeed, the loop-control instructions of the HP-41C already come close.

One essential capability must be added to the calculator before such higher-level commands can be made available. A higher-level language is a program whose output is another program, and so it is necessary that instructions be allowed to operate not only on data but also on other instructions. In this context, it seems significant that the inability of a calculator to alter its own instructions is what most clearly distinguishes calculators from computers.



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	North Star Single Density . North Star Double/Quad . Durango F-85 . iCOM Micro-Disk 2411 iCOM 3712 for MITS	.2.x 170/25 ® .2.x 170/25
	88-2SIO Console iCOM 3712 for 3P + S/MIT SIO Rev non-zero console iCOM 3812	S e 1.4 170/25 *
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- ☐ XASM-48 As XASM-68 for Intel MCS-48 and UPI-41 families\$200/\$25
- □ XASM-18— As XASM-68 for RCA 1802\$200/\$25
- □ DISTEL Disk based disassembler to Intel 8080 or TDL/Xitan 280 source code, listing and cross reference files, Intel or TDL/Xitan pseudo ops optional. Runs on 8080 \$65/\$10
- ISMAL/80 Structured Macro Assembler Language Package of powerful general purpose text macro processor and SMAL structured language compiler. SMAL is an assembler language with IF-THEN-ELSE, LOOP-REPEAT-WHILE, DO-END, BEGIN-END constructs. Not compatible with CP/M version 2 or greater.\$75/\$15

RAID—8080/8085 debugging aid and general system utility program. Both a software emulator and real-time debug monitor. Features multiple breakpoints, symbolic input, symbolic display and altering of registers, built-in assembler & dis-assembler, tracing, single-stepping, memory protection, histograms, memory search, direct disk access. Requires 32K CP/M ...\$250/\$25

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- and parameter passing.

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disk.

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for all library modules. Variant records, strings
and direct I/O are supported. Requires 56K
CP/M \$395/\$25

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© Generates ROMable 8080 machine code.
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Benot dependent program does complex active. Report generator program does complex calculations with stored and derived data, record selection with multiple criteria, and custom for-mats. Sample inventory and mailing list sys-tems included. No support language required ...\$395/\$40

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WORD-STAR-MAIL-MERGE - As above with

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Program names trademarked

† Recommended system configuration consists of 48K CP/M, 2 full size disk drives, 24 x 80 CRT and 132 column printer.

Modified version available for use with CP/M as implemented on Heath and TRS-80 Model I computers

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This product Includes/eXcludes the language ® manual recommended in Condiments

Serial number of CP/M system must be supplied with orders

@ Requires Z80 CPU.



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LIFEBOAT ASSOCIATES MEDIA FORMATS LIST. Diskette, cartridge disk and cartridge tape format codes to be specified when ordering software for listed computer or disk systems. All software products have specific requirements in terms of hardware or software support, such as MPU type, memory size, support operating system or language.

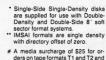
Computer system	Format Code	Computer system	Format Code
Altair 8800 Disk		ICOM 4511 5440 Ca	
Altos		CP/M 1.4	D1
Apple + SoftCard 1:		ICOM 4511 5440 Ca	rtridge
Apple + SoftCard 16		CP/M 2.2	
AVL Eagle		IMS 5000	
BASF System 7100		IMS 8000	
Blackhawk Single D		IMSAI VDP-40	
Blackhawk Micropoli		IMSAI VDP-42	
CDS Versatile 3B .		IMSAI VDP-44	
CDS Versatile 4	Q2	IMSAI VDP-80	
COMPAL-80		Intecolor	See ISC Intecolo
Cromemco System 3	3	Intel MDS Single De	
Cromemco Z2D		Intel MDS Double D	ensityAs
CSSN BACKUP (tap	e)T1#	Intertec SuperBrain	DOS 0.1R7
Delta		Intertec SuperBrain	DOS 0.5-2.X R.
Digi-Log Microterm I		Intertec SuperBrain	DOS 3.XRK
Digital Microsystems		ISC Intecolor 8063/8	
DiscusSe	e Morrow Discus	Kontron PSI-80	
Durango F-85		Meca 51/4"	P6
Dynabyte DB8/2		Micromation	
Dynabyte DB8/4		(Except TRS-80 b	
Exidy Sorcerer + Lif		Micropolis Mod I	
Exidy Sorcerer + Ex	idy CP/M Q4	Micropolis Mod II .	
Heath H8 + H17/H2		MITS 3200/3202	
Heath H89 + Lifebox		Morrow Discus	
Heath H89 + Magno		Mostek	
Hellos II .See Proce		MSD 51/4"	
Horizon		North Star Single De	ensityP1
ICOM 2411 Micro Flo		North Star Double/C	
ICOM 3712		Nylac Single Density	/
ICOM 3812		Nylac Micropolis Mo	
		Ohlo Scientific C3 .	
Prices reflect dist	tribution on 8"	Onyx C8001	T2
single density dis		Pertec PCC 2000 .	
format is request		Processor Technolog	
requires addition	al dickattee a	Quay 500	RC
requires addition		Quay 520	

surcharge of \$8. per additional diskette will be added.

Prices F.O.B. New York Shipping, handling and C.O.D. charges extra.

Manual cost applicable against price of subsequent software

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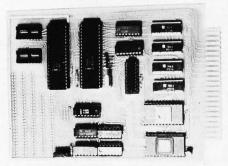
A media surcharge of \$25 for or-ders on tape formats T1 and T2 and of \$100 for orders on disk formats D1 and D2 will be added.

The list of available formats is subject to change without notice. In case of uncertainty, call to confirm the format code for any particular equipment.

Computer system	roimat code
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Research Machines 8"	
Research Machines 51/	"RH
REX	Q3
REX	RQ
SD Systems 8"	
SD Systems 8" SD Systems 5¼"	R3
SorcererSee	Exidy Sorcerer
Spacebyte	
SuperBrain	See Intertec
Tarbell	
TEI 51/4"	
TEI 8"	
Thinkertoys See I	Morrow Discus
TRS-80 Model I 51/4"	R2
TRS-80 Model I + FEC	Freedom RN
TRS-80 Model I + Mici	romationA4
TRS-80 Model I + Om	
TRS-80 Model I + Omi	
TRS-80 Model I + Shuf	
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1234567891

STO 99

Op 17 GTO 000

LRN

Examine the LRN mode using SST; keep in mind that originally there was nothing in the LRN mode. Now, we examine the following locations:

000 90

001 00

002 00 003 91

004 78

005 56

006 34

007 12

The code in location 000 represents the type of number that was entered. In this case, the 9 stands for a number that consumed 9 memory locations (location 007 represents memory location 1, location 6 represents memory locations 2 and 3, location 5 is for memory locations 4 and 5, etc). Notice that the number entered as 1234567891 is stored as 9178563412 (starting at location 003). The empty registers 001 and 002 are used for the storage of up to thirteen digits (in location 001, the rightmost digit is always 0). If you entered 1234567891 and stored it in data register 98, your LRN mode would look like this:

000 00 008 90

001 00 009 00

002 00 010 00

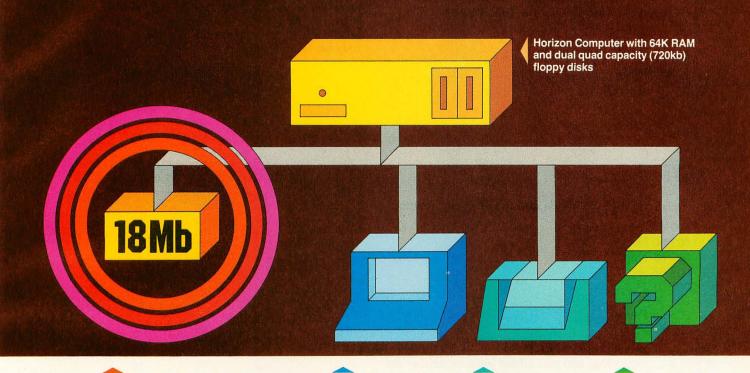
003 00 011 91

004 00 012 78

005 00 013 56 006 00 014 34

007 00 015 12

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To apply this principle, try the following example:

Op 17 8166950185 + .686 STO 99 0 Op 17

RST

Now examine the LRN mode and notice the following:

This is a counting program. Press RST, R/S, $1 \dots 2 \dots$ 3 . . . 4 . . . etc. The .686 was added because neither the Deg nor the Nop have any effect on numbers that are "carried" from one step to another.

There are drawbacks to this storage system. For instance, if the number 1 is stored in memory 99, all program locations 001 thru 006 are cleared, erasing everything between 000 and 007. Also, the instruction 000 90 appears to be troublesome and cannot be changed to a useful code; all it does is take up space. In addition, the code in 002 always has a 0 on the rightmost side, which disables the code. Keep in mind that this also applies to codes 008 and 009, 017 and 018, all the way up through 952 and 953.

Listing 1 is an actual program that will first begin as a counting program, then, after adding 1, it will modify its instructions so that it becomes a subtraction program. ■

Listing 1: A demonstration program showing self-modifying code on the Texas Instruments TI-58 or TI-59 programmable calculators. When run, the program adds 1 to the number on the display, then continually subtracts until R/S is pressed. Begin execution at step 950. As soon as the program begins, hold down the Pause key to see the program work. After the program has been run, examine the LRN mode to observe how the code has been modified.

Step	Code	Key
000 001 002 003 004 005 006 007 008 009 010 011 012 013 014 015 016 017 018 019 020 021 022 023 024 025 026 027 028	76 12 05 69 17 01 01 06 01 09 05 00 01 07 05 85 93 06 08 06 95 42 00 00 69 17 61 09 49	Lbl B 5 Op 17 1 1 6 1 9 5 0 1 7 5 + 6 8 6 = STO 00 00 Op 17 GTO 949 x≥t Lbl A + l = x≥t GTO B

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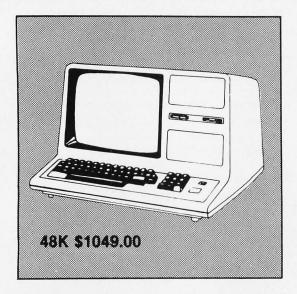
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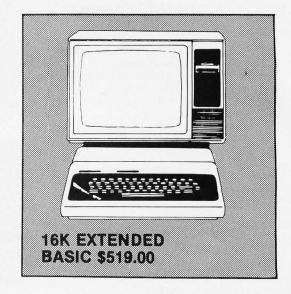
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Generating Bar Code in the Hewlett-Packard Format

Thomas McNeal Hewlett-Packard Cupertino Integrated Circuits Operation 10900 Wolfe Rd Cupertino CA 95014

The HP-41C is Hewlett-Packard's newest entry in the hand-held programmable calculator race. The main feature that distinguishes it from Hewlett-Packard's earlier calculators is its modular design, which allows the HP-41C to be extended by a line of peripheral devices. Up to four peripherals can be plugged into the calculator, and these include a magnetic card reader, a thermal printer, memory modules to increase the amount of memory available to the user, and "application pacs" that contain software for particular applications in read-only-memory module form. In addition, Hewlett-Packard has introduced the 82153A Optical Reader (also called a Wand), which is capable of reading bar codes that contain HP-41C programs, data, or function definitions.

This article describes the HP-41C bar-code format and includes a BASIC program that converts an HP-41C program into a series of bar-code rows that can be printed using a high-quality printer with incremental spacing.

HP-41C Bar-Code Format

The bar code that is read by the Wand is simply binary information represented by wide and narrow bars (representing 1 and 0, respectively). The space between each bar is nominally the width of the narrow bar and serves as a benchmark for the current unit bar width. The unit bar width must be greater than 15 mils. A narrow bar may be up to 20% wider than the unit bar width, which is

established by the previous bar and space. A wide bar should be twice the unit bar width, and a wide bar should vary no more than 20% from its standard value.

The bars are logically grouped into 8-bit bytes, and a bar-code program is organized into rows of a maximum of 16 bytes, with 3 bytes of header information and up to 13 bytes of data per row. Associated with each row are pairs of start and stop bits (binary 00 and 10, respectively) that allow the rows to be read in either direction. Figure 1 shows the format for a single row of program bar code.

The 13 data bytes contain the machine language of the HP-41C instruction set. Table 1 lists these instructions, with the first 8-bit byte of each instruction determining the instruction type. Additional bytes, if any, contain alphanumeric character data, numeric or stack operands, or linkage information.

All indirect instructions are 2 bytes long, with the high-order bit of the second byte set to 1 to signify an indirect operand. In the case of indirect numeric GOTO and EXECUTE instructions, the high-order bit is set to 1 for an EXECUTE instruction and cleared to 0 for a GOTO instruction.

The size of an instruction is determined by its position in the table. In order to save room in the HP-41C, some instructions may have two completely different representations, depending on the value of the operand associated with that instruction. For example, the numeric label instruction is represented by 1 byte if the

operand is less than 15 and, otherwise, by 2 bytes. The XROM (EXE-CUTE read-only-memory module) instructions seen in the function table also save room when a reference to an alpha label within a read-only-memory module is made by an EXECUTE instruction. The XROM instruction is a compact, 2-byte reference to a table of alphanumeric labels within the read-only-memory module; this replaces the EXECUTE instruction originally entered by the user.

HP-41C Internal Representation

The instructions generally are 1, 2, or 3 bytes long, with the 4 high-order bits of the first byte indicating the instruction length. The exceptions to this rule are the instructions containing alphanumeric character data. The HP-41C has an alphanumeric display that allows the definition of instructions with nonnumeric operands. These functions include an alphanumeric label instruction, which contains a label of up to seven characters, GOTO and EXECUTE instructions with alphanumeric label operands, and a text-entry instruction. This last instruction will either append or replace character data in a special alphanumeric register and may contain up to fifteen characters.

All character data is represented in ASCII (American Standard Code for Information Interchange), with one character per byte and a few exceptions for special characters not found in the ASCII character set. Since character-oriented instructions are of indeterminate length, their size is



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VALUE	00	nnnnnnn	nnnn	nnnn	nnnn	(UP TO 13 BYTES)	nnnn	10
FUNCTION	START BITS	CHECKSUM	TYPE	SEQUENCE NUMBER	LEADING PARTIAL FCN BYTES	DATA	TRAILING PARTIAL FCN BYTES	STOP BITS
NUMBER OF BITS	2	8	4	4	4	UP TO 104	4	2

Figure 1: Format for Hewlett-Packard bar codes. A maximum of 13 bytes can be encoded into one row of bar code.

embedded within a word in the instruction itself. For alphanumeric label operands, the number of characters is held in the 4 low-order bits of the second or third byte, with the 4 high-order bits set to hexadecimal F. The position of this byte is indicated in the documentation of the compile routine of the bar-code generating program. (See listing 1.) This convention allows differentiation between an alphanumeric label instruction and an

end instruction, in which the third word contains a hexadecimal F in the low rather than the high 4 bits.

In addition, the alphanumeric label and end instructions contain pointers that link them with other alphanumeric label and end instructions, creating an alphanumeric label chain. This chain is used to identify the position of labels and program boundaries within the HP-41C program memory and establishes entry points for each program. The chain is recompiled by the Wand software, so the bytes containing the chain pointers are set to 0 by this program.

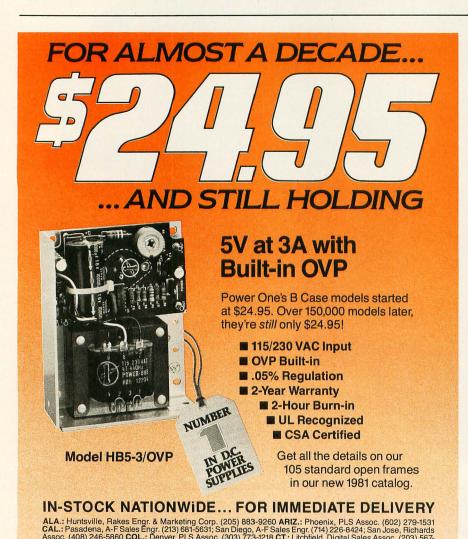
For a detailed discussion of the function table and other internal features of the HP-41C, refer to a series of articles that appeared in the Corvallis Division Column of the PCC Journal beginning on September 6, 1979. The PPC Journal is a publication of the PPC (Personal Programmable Calculator), an independent user group for Hewlett-Packard programmable calculators. Further information may be obtained by writing

> Richard Nelson, Editor PPC Iournal 2541 W Camden Pl Santa Ana CA 92704

The header information necessary for a bar-code program is contained in the left-most 3 bytes of each bar code row. The first byte is a parity check in the form of a running checksum (a summation modulo 256, with wrap-around carry, of the checksum of the preceding row and all other bytes of the current row).

The second byte is split into two parts. The 4 high-order bits contain the program type (1=nonprivate, 2=private), and the 4 low-order bits contain the sequence number, which is the bar-code row number minus 1, modulo 16. The sequence number will be inspected by the Wand software to assure that the correct row is being read.

Text continued on page 172



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					LOW O	RDER 4 BITS				
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	0	NULL	LBL 00	LBL 01	LBL 02	LBL 03	LBL 04	LBL 05	LBL 06	
	-	digit 0	1	2	3	4	5	6	7	
	7	RCL 00	RCL 01	RCL 02	RCL 03	RCL 04	RCL 05	RCL 06	RCL 07	
	က	STO 00	STO 01	STO 02	STO 03	STO 04	STO 05	STO 06	STO 07	1
	4	+	_	*	1	X <y?< td=""><td>X>Y?</td><td>X < = Y?</td><td>Σ +</td><td>BYTE</td></y?<>	X>Y?	X < = Y?	Σ +	BYTE
BITS	2	LN	X ²	SQRT	Yx	CHS	e ^x	LOG	10 ^x	
4	9	1/X	ABS	FACT	X≠0?	X>0?	LN(1 + X)	X<0?	X = 0?	
OEF	7	CL	X<>Y	PI	CLST	Rt	RDN	LASTX	CLX	
HIGH ORDER	æ	DEG	RAD	GRAD	ENTERT	STOP	RTN	BEEP	CLA	
HG	6	RCL nn	STO nn	ST + nn	ST - nn	ST* nn	ST/ nn	ISG nn	DSE nn	
	4	XROM	XROM	XROM	XROM	XROM	XROM	XROM	XROM	2
	В		GTO 00	GTO 01	GTO 02	GTO 03	GTO 04	GTO 05	GTO 06	BYTES
	O	-	A	LPHA LABEL AN	D END INSTRUC	CTIONS ———				
	۵	-			GTO nn				→ \	3
	ш	-			——XEQ nn——				 ∫	BYTES
	ட		TEXT 1	TEXT 2	TEXT 3	TEXT 4	TEXT 5	TEXT 6	TEXT }	UP TO 16 BYTES
1	Γab	le 1: A table	for the HP-410	C instruction se	t. Instructions i	n the HP-41C	are stored as on	e or more 8-bit	bytes.	

Combine accurate flight characteristics with the best in animation graphics and you'll have SubLOGIC's

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Table 1 continued on page 154.

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Return this coupon with (1) Customer Information Card (par January 15, 1981-March 31, 1981. Items must be postmarked prohibited. Offer good only in U.S.A. TI reserves the right to	by April 14, 1981. Plea	(2) Dated copy of proof of pase allow 30 days for delivery.	urchase between Offer void where

U.S. suggested retail prices. *For use with TI-59 only.

TEXAS INSTRUMENTS

Table	2 1 continued			LOW-OF	RDER 4 BITS				
	8	9	Α	В	С	D	E	F	
0	LBL 07	LBL 08	LBL 09	LBL 10	LBL 11	LBL 12	LBL 13	LBL 14	
-	8	9		EEX	(digit entry) CHS	GTOlpha	$XEQ\alpha$		
2	RCL 08	RCL 09	RCL 10	RCL 11	RCL 12	RCL 13	RCL 14	RCL 15	
က	STO 08	STO 09	STO 10	STO 11	STO 12	STO 13	STO 14	STO 15	
4	Σ –	HMS+	HMS-	MOD	%	%CH	P-R	R – P	BYTE
5 5	e ^x – 1	SIN	cos	TAN	ASIN	ACOS	ATAN	DEC	
44 E	INT	FRAC	D – R	R-D	HMS	HR	RND	OCT	
4DE	X = Y?	X≠Y?	SIGN	X < = 0?	MEAN	SDEV	AVIEW	CLD	
HIGH ORDER 4 BITS 9 8 7 6 5	ASHF	PSE	CLRG	AOFF	AON	OFF	PROMPT	ADV /	
9	VIEW nn	REG nn	ASTO nn	ARCL nn	FIX n	SCI n	ENG n	TONE n	
⋖	SF nn	CF nn	FS?C nn	FC?C nn	FS? nn	FC? nn	GTO/XEQ IND		2
В	GTO 07	GTO 08	GTO 09	GTO 10	GTO 11	GTO 12	GTO 13	GTO 14	BYTE
O							— > X<>nn	LBL nn	
Ω									3
ш								S	BYTE
ட	TEXT 8	TEXT 9	TEXT 10	TEXT 11	TEXT 12	TEXT 13	TEXT 14	TEXT }	UP TO 16 BYTE

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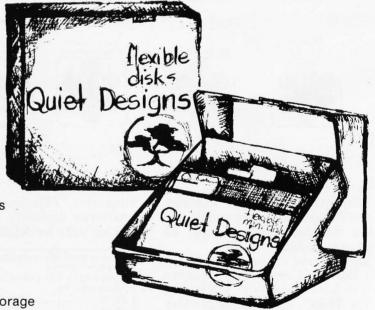
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APPENDIX B

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generates the necessary bar-code program rows. This program runs on an HP-9845A gram steps in mnemonic form, converts them to HP-41C machine language, and Listing 1: Bar-code generating program. This program accepts up to 2240 HP-41C prominicomputer attached to a Diablo 1650 printer; the print wheel used is a Titan 10 metallic daisy-wheel printer.

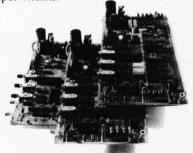
```
IF T#<>"RUNPRIVATE" THEN 605
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              IF T#="GETTEXT" THEN 3935
IF T#="SAVEPROG" THEN 4055
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             IF T#="GETPROG" THEN 4140
IF T#="SCRATCH" THEN 295
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             IF T#="EXIT" THEN STOP
                                                                                                                                                                                                         I=POS(T$," ")
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           [=1+LEN(T$)+1
                                                                                                                                                                                          *I', "<" INPUI
                                                                                                                                                                                                                                                                                                                                      I=LEN(T1$)+1
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            二十二次第二次第四三年日
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           G010 925
                                [=F6=F8=0
                                                                                                             G0T0 3500
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             G0T0 358
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           GOTO 350
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         G010 358
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       G0T0 350
                                                                                                                                                                                                                           V=K=D=0
                                                                                                                                                                                                                                                                                                                                                                                                                                                   K=K+1
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              P(V)=-1
NEXT I
                                                                                                                                                                                                                                                                                                                                                                                                                                                                  NEXT G
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          P(V)=T
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             . P5=2
                                                                                                                                                                                                                                           P5=1
                                                                                                                                                                                                                                                                                                                                                                                                  555
556
556
565
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              578
575
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            405
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 READ LOCAL LABELS & STACK REGISTER MNEMONICS INTO $1$
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              READ SORTED INSTRUCTION MNEMONICS INTO 14, INSTRUC.
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             READ IN VALID CHARACTER TABLES FOR CHARACTER CHECK
!CHARACTERS IN C≉; CHARACTER CODE IN C2
                                                                                                                                                                                                                                                                                     1)"NUMBER": THIS WILL GENERATE LINE NUMBERS FOR 41C INSTRUCTIONS 2)"LIST": THIS WILL LIST THE INSTRUCTIONS CURRENTLY ENTERED 3)"RUN": CHECKS FOR PRESENCE OF COMPILED CODE AND PRODUCES
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           INTEGER I,J,K,L,V,R,RI,C,CI,C2(60),T,P(2240),M(2240),M1,P1,P2,K1(2240)
INTEGER FI,F2,F3,F4,F5,F6,F7,F8,F9,E,E1,E2,E3(15),E4,I1(103),D,X,Y,Z
INTEGER I5,B1,B2,B3,T5,H1,H2,P5,L5,C5,S3,S1(16),B(132),V1,V2,V3
DIM T$[60],T1$(30],T2$[30],S$[50],A$[1500],B$[915]
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      THE TEXT INTO THE TEXT ARRAY, OR USES THE COMMAND
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     COMMRND JUMP TABLE TO JUMP TO THE CORRECT COMMRND
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                THIS PROGRAM PROMPTS FOR NUMBERED HF41C INSTRUCTIONS AND STORES THE SAME FOR LATER COMPILATION TO BE INITIATED UPON COMMAND. THE INSTRUCTION NUMBERS MAY BE FROM 1 TO 2240 (THE TOTAL NUMBER OF EVIES AVAILABLE FOR USE IN A PROGRAM) AND MUST BE INTEGERS. THE COMPILED CODE WILL BE USED TO DRIVE A BAR CODE GENERATION ROUTINE WHICH WILL CALCULATE THE BIT FATTERN FOR A ROW OF BAR
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        EITHER DECODES THE INSTRUCTION NUMBER AND ENTERS
                                                                                                                                                                                                                        CODE. THIS BIT PRITERN WILL RPPERR WITHIN R LOOP, RT WHICH POINT THE USER WILL BE ABLE TO CALL HIS OWN PLOTTING ROUTINES TO GENERATE 41C BAR CODE. THE COMMANDS AVAILABLE IN THIS
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         WRITES PROMPT FOR TEXT OR COMMAND ENTRY AND
                                                                                                                                                                                                                                                                                                                                                                                                                                                                11) "DELETE": DELETES THE NAMED INSTRUCTION NUMBER FROM THE
                                                                                                                                                                                                                                                                                                                                                      4)"COMPILE": COMPILES THE CURRENT TEXT INTO MACHINE CODE 5)"RENUMBER": ALTERS THE 41C INSTRUCTION NUMBER SEQUENCE
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              13)"RUNPRIVATE": GENERATES BAR CODE FOR A PRIVATE PROGRAM
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          ISET UP DIABLO CONTROL CODES
                                                                                *******************
                                                                                                                                                                                                                                                                                                                                                                                    6)"SAVETEXT": SAVES THE CURRENT TEXT ON CASSETTE TAPE
                                                                                                                                                                                                                                                                                                                                                                                                                   8)"SAVEPROG": SAVES THE COMPILED MACHINE CODE ON TAPE
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                12)"SCRATCH": ERASES THE CURRENT INSTRUCTIONS ENTERED
                                                                                                                                                                                                                                                                                                                                                                                                                                                 10)"EXIT": HALTS THE USER LANGUAGE COMPILER PROGRAM
                                                                                                                                                                                                                                                                                                                                                                                                                                  9)"GETPROG": RETRIEVES THE COMPILED CODE FROM TAPE
                                                                                                                                                                                                                                                                                                                                                                                                   7>"GETTEXT": RETRIEVES THE TEXT FROM CASSETTE TAPE
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          DIM S1$(27)[1],1$(104)[6],H1$[3],H2$[3],H4$[9],C$(60)[1]
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              IVALUES INTO 11 FOR TABLE DRIVER
                                                                                             HP41C USER LANGUAGE COMPILER
AND BAR CODE GENERATION PROGRAM
                                                                                                                                                                                                                                                                                                                                     THE BAR CODE BIT PATTERN.
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     H1#=CHR#(27)%CHR#(31)%CHR#(2)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 CURRENT TEXT.
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    H2#=CHR#(27)&CHR#(83)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         MAIN PROGRAM:
                                                                                                                                                                                                                                                                         PROGRAM ARE:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      ON ERROR GOTO 4250
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              READ I$(I), I1(I)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              READ C#(I), C2(I)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 FOR I=1 TO 103
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              FOR I=1 TO 68
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 FOR I=1 TO 26
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   [$=T1$=T2$=""
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  READ S1$(1)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             NEXT I
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 NEXT I
                                                                                \frac{1}{2}
```

```
FENTER TEXT AND DELIMITER INTO TEXT ARRAY AND STORE POINTER AT THE INDEX GIVEN BY INSTRUCTION NUMBER
                                                                                                                                                                                                                                                                                                                             SET PRIVACY FLAG TO INDICATE A NON-PRIVATE PROGRAM
               POINTERS INTO TEXT STRING
                                                                                                                                                                                                                                     BEGIN PROMPTER SECTION: ASK FOR COMMAND OR INSTRUCTION
                                                                                       INITIALIZE TEXT STRING POINTER AND FLAGS
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             ICALCULATE INSTRUCTION NUMBER VALUE
                                                                                                                            NPUT "DO YOU WANT A LIST OF THE AVAILABLE COMMANDS?",T1$
                                                                                                                                                                                                                                                                                                                                                                                T1$=T$C1,1-13 IEXTRACT FIRST WORD OF INPUT
IF 11$<>"DELETE" THEN 405 ICHECK FOR A DELETE COMMAND
               INITIALIZE ARRAY OF POINTERS INTO
INITIALIZE COMPILED PROGRAM ARRAY
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           DELETE INSTRUCITON IF FLAG IS
                                                                                                                                                                                                                                                                                        SET I TO FIRST WORD OF INPUT
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              PRINT "?? - GIVE NUMBERED STATEMENT OR A COMMAND"
                                                                                                                                                                                 IPRINT OUT REFERENCE TABLE
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         1F (11#[J,J]<"@") OR (T1#[J,J]>"9") THEN 500
V=V+(NUM(T1#[J,J])-48)*10^K
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   PRINT "STATEMENT NUMBER VALUE TOO LARGE"
                                                                                                                                                                                                                                                                                                                                               IONE WORD COMMAND
                                                                                                                                          IF (T1*="N") OR (T1*="NO") THEN 350
IF T*="SCRATCH" THEN 350
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       COMMAND JUMP TABLE
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    IF T$="NUMBER" THEN 670
IF T$="LIST" THEN 785
IF T$="RUN" THEN 925
IF T$=""COMPILE" THEN 1855
IF T$=""COMPILE" THEN 1855
IF T$=""COMPILE" THEN 3710
IF T$=""SRVETEXT" THEN 3845
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                FOR J=1-1 TO 1 STEP -1
IF (T1$[J,J]<"0") OR
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                ERROR MESSAGES
                                                                                                                                                                                                                                                                                                                                                                                                                        [1#=TRIM#(T#[I+1])
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          F V>2240 THEN 510
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                IF I-1>4 THEN 510
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     [$=TRIM$(T$[]+1])
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             IF D<>1 THEN 465
FOR I=1 TO 2240
                                                                                                                                                                                                                                                                                                                                                  IF I=0 THEN 530
                 P(I)=-1
                                   M(I)=-1
```

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QUALITY

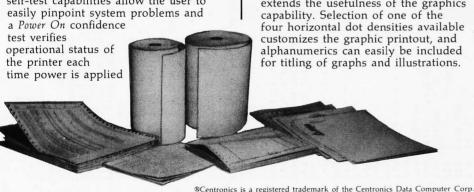
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The 88G prints a full upper and lower case 96 character ASCII set with a crisp, clear 7x7 matrix in 80, 96, or 132 column formats. For text processing and correspondence applications, an 11x7, 80 column serif style matrix can be selected by switch or software command. The dual tractor/pressure-feed paper drive system allows the user to choose either pin-feed, roll, or single sheet papers up to 9.5 inches wide.

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```
SIGB2><>0 THEN IS=IS+1 !IF B3<0 THEN INSTRUCTION IS STARTING; INCREMENT M<B1>>143 THEN 1300 (COUNTER IF NON-MULL INST. AND CHECK FOR LENGTH
                                                                                                                                                                                                   BYTES IN THE PROGRAM
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       (2)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            **PROCESS ONE BYTE INSTRUCTIONS**
! CHECK FOR AN ALPHA EXECUTE OR A GOTO ALPHA
!INSTRUCTION, (GET SIZE FROM:SECOND BYTE)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        H
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             Ø
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          IJF B3=0 THEN INSTRUCTION ENDS; RESET COUNTER
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      6
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       TRAILING PARTIAL FCN. BYTE COUNTER AT
                                                                                                                                                                                                                                                                                                                                                                                                                         ø
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          START LEADING PARTIAL FCH. BYTE COUNTER AT
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         ED TO
FLAG
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        START # OF WORDS SINCE LAST INST. COUNTER
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               FIRST INSTRUCTION OF ROW COUNTER AT
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            START CHECKSUM COUNTER (SUM MOD 256) AT 0
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      ITRANSFER WORD FROM "M'INTO 16 BYTE BUFFER
                                                                                                                                                                                                                                                                                                                                                                                                                      H
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       INSTRUCTION TRANSLATION SECTION: LOAD INSTRUCTIONS INTO A
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            BAR CODE ROW AND KEEP COUNTERS FOR THE HEADER DATA (NOTE THAT B3 IS SET TO THE NUMBER OF BYTES EXPECTED COMPLETE THE CURRENT INSTRUCTION, AND SERVES AS A FL
                                                                                                                                                                                                                                                                                                                                                                                                                      COMPILED DATA ARRAY 'M' POINTER
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     ENTRY INSTRUCTION
INPUT "ENTER THE TITLE OF THE PROGRAM (SØ CHARACTERS OR LESS)"
                                                                                                                                                                                                                                                                                                                                                                                                                                                                             INSTRUCTION LENGTH COUNTER AT
                                                                                                                                                                                                                                                                                                                                         *****
                                                                                                                                                                                                                                                                                                                                                                                                                                                BAR CODE ROW BYTE POINTER AT
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      B3>0 THEN INSTRUCTION CONTINUES
                                                                                                                                                                                                                                                                                                                                                                                            START 41C INSTRUCTION COUNTER AT
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  START BAR CODE ROW COUNTER AT 0
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         ZERO OUT THE BIT PATTERN ARRAY
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              FOR THE BEGINNING OF THE NEXT INSTRUCTIONS
                                                                                                                                                                                                   S2 CONTAINS THE NUMBER OF
                                                       DIABLO 1650
                                                                                                                                                                                                                                                                                                            RESET PRINTER BACK TO CRT
                                                                                       SET PRINTER TO DIABLO LU
                                                                                                                                                                                                                                                                                                                                       THE DIABLO OUTPUT CODE
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          DIGIT
                                                                                                                                                                                                                                                        PROGRAM REGISTERS NEEDED:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                UPDATE VARIABLES
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 AND (M(B1)<>30) THEN 1215
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       Œ
                                                  SECTION WRITES OUT TO THE
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  1F (M(B1)<16) OR (M(B1)>28) THEN 1278
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       I CHECK FOR
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      OR (M(I))28) THEN 1240
                                                                                                                                                                                                                                                                                                                                                                                                                                                                             START
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       START
                                                                                                                                                                                                                                                                                                                                                                                                                         START
                                                                                                                                                                                                                                                                                                                                                                                                                                                   START
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 START
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           4
```

PRINT "A PROGRAM MUST BE COMPILED FIRST!"

INTERTEC'S NEW \$2500 MULTI-USER SMALL BUSINESS COMPUTER

At last, there's a multi-user microcomputer system designed and built the way it should be. The CompuStar™. Our new. low-cost "shared-disk" multi-user system with mainframe performance.

Unlike any other system, our new CompuStar offers what we believe to be the most practical approach to almost any multi-user application. Data entry. Distributed processing. Small business. Scientific. Whatever! And never before has such powerful performance been available at such modest cost. Here's how we did it . . .

The system architecture of the CompuStar is based on four types of video display terminals, each of which can be connected into an auxiliary hard disk storage system. Up to 255 terminals can be connected into a single network! Each terminal (called a Video Processing Unit) contains its own microprocessor and 64K of dynamic RAM. The result? Lightning fast program execution! Even when all users are on-line performing different tasks! A special "multiplexor" in the CompuStar Disk Storage System ties all external users together to "share" the system's disk resources. So, no single user ever need wait on another. An exciting concept . with some awesome application possibilities!

CompuStar™ user stations can be configured in almost as many ways as you can imagine. The wide variety of terminals offered gives you the flexibility and versatility you've always wanted (but never had) in a multi-user system. The CompuStar Model 10 is a programmable, intelligent terminal with 64K of RAM. It's a real workhorse if your requirement is a data entry

or inquiry/response application. And if your terminal needs are more sophisticated, select either the CompuStar Model 20, 30 or 40. Each can be used as either a standalone workstation or tied into a multi-user network. The Model 20 incorporates all of the features of the Model 10 with the addition of two, double-density mini-floppies built right in. And it boasts over 350,000 bytes of local, off-line user storage. The Model 30 also features a dual drive system but offers over 700,000 bytes of disk storage. And, the Model 40 boasts nearly 11/2 million bytes of dual disk storage. But no matter which model you select, you'll enjoy unparalleled versatility in configuring your multi-user network.

Add as many terminals as you like - at prices starting at less than \$2500. Now that's truly incredible!

No matter what your application, the CompuStar can handle it! Three disk storage options are available. A tabletop 10 megabyte 8" winchester-type drive complete with power supply and our special controller and multiplexor costs just \$4995. Or, if your disk storage needs are more demanding, select either a 32 or 96 megabyte Control Data CMD drive with a 16 megabyte removable, top loading cartridge. Plus, there's no fuss in getting a CompuStar system up and running. Just plug in a Video Processing Unit and you're ready to go . . . with up to 254 more terminals in the network by simply connecting them together in a "daisy-chain" fashion. CompuStar's special parallel interface allows for system cable lengths of up to one mile . . . with data transfer rates of 1.6 million BPS!

Software costs are low, too. CompuStar's disk operating system is the industry standard CP/M*. With an impressive array of application software already available and several communication packages offered, the CompuStar can tackle even your most difficult programming tasks.

Compare for yourself. Of all the microcomputer-based multiuser systems available today, we know of only one which offers exactly what you need and should expect. Exceptional value and upward growth capability. The CompuStar™. A true price and performance leader!



2300 Broad River Rd. Columbia. SC 2921 (803) 798-9100 TWX: 810-666-2115

COMPUSAR

Listing	1g 1 continued:		1555	×
1238	1=1+1	THROUGH	1569	NEXT Y
1235 1240	0 14	IDIGIT ENTRY HAS BEEN SEEN	1578	BC10=
1245		NOTIONAL TIPLE DIGIT INSTRUCTION	1500	(V)
1255	T5=0		1596	- E
1265	7		1600	
1278	B	PROCESS ALL OTHER ONE BYTE INSTRUCTIONS	1685	SIHI THE
1280	· U -		1615	
1290	**PROCESS	TWO BYTE INSTRUCTIONS**	1626	AHKIHBU
1388	: IF M(B1)>207 THEN 1370 IE (M/D4)/1003 OD (M/D	ICHECK FOR ALPHA LABEL AND END INSTRUCTIONS	1635	
1316	IF (MCBI)/1927 OR (MCBI TS=T5+1 If bi+0/60 TUEN 1000	000 - 1000 TO 1000	1645	
1326	IC DITENSE OFFICE B3=2 GOTO 1435	201 - CO - C	1652	PRINTER IS 9
1338	B3=M(B1+2) MOD 16+2 GOTO 1435	IALPHA LABEL:GET SIZE FROM THIRD BYTE	1669	
1340		CONCL SERVE CET) STORE MACH FEGURS SSECTED	1679	
1358			1686 1682	IF S3 MOD PRINTER IS
1369	#*PROCESS	ò	1683	
1375	15 N.517/248 B3=2	LUMB FURR 610 0	1695	i CLEHNUP
1386			1700	FOR I=1 TO
1395	B3=M(B1) MOD 16 T5=T5+1	PROCESS ALPHA DATA ENTRY INSTRUCTIONS (GET LENGTH FROM FIRST BYTE)	1715 1729	SICI)=0 BCI)=0 NEXT I
1405			1725	FOR I=17 TO 1.
1415	I BAR CODE ROW SETUP CALCULATE THE	SECTION: TAKE DATA FOR THIS ROW AND E HEADER DATA AND OTHER VARIABLES	1735	Z M
4 4 4 2 6 6 2 6 6 2 6 6	! IF (B2<16) AND (B1<82)	! CHECK FOR A COMPLETED ROW (A FILLED BUFFER) THEN 1095	1745 1750 1755	LS=IS IF B3=0 THEN I IF B1 <s2 td="" then<=""></s2>
144 144 0 0	T	IUPDATE PARTIAL FUNCTION COUNTERS	1769	PRINT
1450 1455		16	1765	G010 35
1460 1465 1470	! S1(2)=P5*16+S3 MOD 16	! ENTER PRIVACY VALUE AND SEQUENCE NUMBER	1775 1780 1785	* * * * * *
1475	! FOR I=2 TO B2	CALCULATE CHECKSUM (A CUMULATIVE SUM MOD 256)	1798	
0 6 4 4 6 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	C5=C5+S1(I) MOD 256 IF C5>=256 THEN C5=C5 MOD NEXT I	D 256+1	1800 1805 1810	* * * * * * * * * * * * * * * * * * * *
1500 1505	\$1(1)		1815 1826	HP41C INSTRUCT INSTRUCT
1516 1526 1526	CONVERSION SECTION: O BIT PATTERN REPR	CONVERT THE CURRENT ROW OR SEGMENT INTO A PRESENTING THE BAR CODE.	1000 1000 1000 1000 1000 1000	FOR THOSE INTO INSTRUCTION TO THE PROPERTY OF
1536	· (0 III	CONVERT DECIMAL DATA TO A BINARY PATTERN	0000	ABORTED IF TO THE ERR
1548 1548 1545 1568	X=S1(I) FOR Y=2+I*8 TO 3+(I-1)*8 B(Y)=X MOD 2	: STEP -1	1850 1850	· -

```
UPDATE FIRST INSTR. COUNTER TO CURRENT INSTR. CHECK FOR THE START OF A NEW INSTRUCTION
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              (GO BRCK TO PROMPT IF BAR CODE GENERATION HAS)
I BEEN COMPLETED
                                                                                                                                                           IS THE BAR CODE GENERATION AND OUTPUT SECTION USED BY***
BAR CODE GENERATION ON A DIABLO 1650 DAISY WHEEL*****
ER WITH A TITAN 10 96-CHARACTER METALLIC DAISY WHEEL****
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 THIS ROUTINE INTERPRETS
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                P SECTION: THIS SECTION RESETS VARIABLES TO PREPARE FOR ENERHION OF THE NEXT ROW OF BAR CODE.
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       UCTIONS ENTERED IN THE TEXT BRRBY BND LOBDS THE MACHINE THE // ARRBY. THE INTERPRETER IS TRBLE DRIVEN EXCEPT INSTRUCTIONS MHOSE OPERRINDS CHRNGE THE LENGTH OF THE OX (GTO-S), LBL'S OR SEO'S), DIGIT ENTRY INSTRUCTIONS, CTIONS INVOLVING REPROBERIOR TEXT, THE PROCESS MAY BE FAN ERROR IS ENCOUNTERED BY TYPING YBORT' IN RESPONSE
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     * * * *
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  ROW BUFFER
POINT, THE BRRRY 'B' HOLDS A SERIES OF 1'S AND 0'S
WIING A SINGLE ROW OF 41C PROGRAM BAR CODE, INCLUDING
RI AND STOP BITS. OTHER DATA WILL BE FOUND IN THE
                                                                                                                                                                                                                                                                                                                                                                                                                                  IGENERATE BAR PATTERN FROM BIT PATTERN
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    ZERO OUT THE BIT PATTERN ARRAY
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  CODE
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   ****
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   RESET S1 BUFFER POINTER TO 3
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  ZERO OUT THE 16 BYTE BAR
                                                                                                                                                                                                                                                                                                                                   SET PRINTER TO DIABLO LU
                                                               RT AND STOP BITS. OTHER DATA WILL BE FO.
ES B2,S3,L5 AND IS AS EXPLAINED ABOVE.
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   'RUN' ROUTINE * *
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   END OF DIABLO OUTPUT SECTION
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    RESET PRINTER TO CRT
                                                                                                                                                                                                                                                                                                                                                                                                                                                              3X,3A,10A,915A,2A";H1$,H4$,B$,H2$
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 RUCTION INTERPRETATION ROUTINE:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  * * * * * COMPILE' ROUTINE * *
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 DE GENERATION COMPLETED"
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    :0 THEN PRINT CHR$(12)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                END OF
                                                                                                                                                                                                                                                                                                                                                                  3X,20A";T1$
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   *
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  OR MESSAGE.
                                                                                                                                                                                                                                                                                                      -1,15,15)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     _5=L5+1
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  1895
```

HADD START AND STOP BITS

Pump Up Your TRS-80 with the ES/F Mass Storage System



Actual Thickness V

THESE FACTS SPEAK FOR THEMSELVES!

	CASSETTE	ES/F	MINI-DISK
SPEED (Seconds to load "Blackjad	56 ck'')	6 (5' wafer)	6½
CAPACITY (thousands of bytes)	38 (C-20)	64 (75' wafer)	59 (TRSDOS)
RELIABILITY (Designed for digital data?)	NO	YES	YES
SYSTEM COST (First unit plus interface)	\$60)	\$250	\$800
MEDIA COST (in quantities of ten)	\$3.10 cassette	\$3.00 wafer	\$3.20 disk

Let's face it. Cassette players were not designed to store digital data and programs. That's why we designed a digital storage system using a continuous tape loop: the Exatron Stringy/Floppy (ES/F) and the Wafer. There's no expensive interface to buy-the ES/F comes ready to pump up your TRS-80.*

Once your TRS-80* is pumped up by our ES/F...you won't want to deflate it. We're so sure, that we offer an unconditional 30-day money-back guarantee and a one-year limited warranty. Over 2,000 TRS-80* owners have met the wafer . . . why don't you?



```
ISET INDIRECTION FLAG AND EXTRACT NUMERIC OPERAND
                                                                                                                                                                                                       INTERPRETING SECTION: THIS SECTION TAKES THE DECODED TEXT HELD IN T$,11$ AND T2$, INTERPRETS THE INSTRUCTION AND ENTERS THE MACHINE CODE INTO THE ARRAY "M" AT THE POSITION GIVEN BY THE POINTER "MI". AN ERROR CAUSES A MESSAGE TO BE PRINTED WHICH REQUESTS A CORRECTION.
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      Listing 1 continued on page 164
                                                                                                                                                                                                                                                                                      IF F2<>1 THEN 2395 ICHECK FOR A TEXT ENTRY INSTRUCTION MCM1>=240+L-2 IE F1=1 THEN MCM1>=MCM1>=MCM1>=MCM1>=MCM1>+1 IADD COUNTER FOR EXTRA BYTE IF APPEND INST.
                                                                                                                                                                                                                                                                                                                                                                                                                                                                      ICHECK FOR VALID CHARACTERS AND ADD TO INST.
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          2520 JF (L1=LEN(T#)) AND (L2=0) THEN 33351CHECK FOR ERRORS IN MANTISSR 2525 JF (1=L1) OR (1=L2) THEN 2555
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               CODE ARRAY
                                                                                                                                                                                                                                                                                                                                                                                                       BYTE
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   ICHECK FOR DIGIT ENTRY INSTRUCTION
                                                                                                                                                                                                                                                                                                                                                                                                         SNB
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    ENTER VALID CHARACTER
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              OR I=1 TO L1
IF T$[1,1]<>"." THEN 2495'CHECK FOR THE DECIMAL POINT
                                                                                                                                                                                                                                                                                                                                                                                                         IIF ALPHA APPEND, PUT 127 IN
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      IF ERROR EXISTS
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             IF T$[1,1]<>"-" THEN 2430 (CHECK FOR MINUS SIGN
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 IF (T$[1,1]<>"+") AND (T$[1,1]<>"-") THEN 2435
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               IF (T$[1,1]<"0") OR (T$[1,1]>"9") THEN 2490
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  ILOOK FOR EXPONENT
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      PRINT "INVALID CHARACTER IN LABEL OR TEXT"
                                                                                                              2220 IF LEN(T1#)<=2 THEN 2275
2225 PRINT "ERROR IN NUMERIC OPERAND IN LINE #
12$=11$[1,P2-1]
IF T2$="IND" THEN 2210
PRINT "OPERAND ERROR IN LINE # ";J
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        Z=FNS(X, Y, (T*[I, I]), C*(*))
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    IF L1=0 THEN L1=LEN(T$)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             M(M1)=NUM(T$[I,1])-32
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   IF L2<>0 THEN L1=L2-1
                                                                                              2215 T1#=TRIM#(T1#[P2+1])
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        IF Z<>0 THEN 2370
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      IF F6<>1 THEN 2669
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               IF F9=1 THEN 2538
                                                                                                                                                                                                                                                                                                                                                                                                            IF F1<>1 THEN 2335
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     2435 L1=POS(T#," ")
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   L2=P0S(T$,"E")
                                                                                                                                                                                                                                                                                                                                                                                                                                                                         FOR I=2 TO L-1
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 FOR I=1 TO L1
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        GOSUB 3400
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        GOTO 1930
M(M1)=C2(Z)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                G0T0 2510
                                                GOSUB 3488
                                                                                                                                               GOSUB 3400
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 M(M1)=26
                                                                  GOTO 1930
                                                                                                                                                              GOTO 1930
                                                                                                                                                                                                                                                                                                                                                                                                                            M(M1)=127
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      G010 3335
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      MCM1)=28
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         M1=M1+1
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      M1=M1+1
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  M1=M1+1
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                M1=M1+1
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     2430 T$=T$[2]
                                                                                                                                                                                                                                                                                                                                            M1=M1+1
                                                                                                                                                                                                                                                                                                                                                                                                                                           M1=M1+1
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              NEXT I
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        NEXT I
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   F9=1
                                                                                                                                                                                                                                                                                                                                                                           V±59
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      B=64
                                                                                  F5=1
                                                                                                                                                                                                                                                                                                                                                             ×=1
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                                                                                                                                                                                                                                                                                                            18DD A NULL INSTRUCTION BETWEEN ADJACENT DIGIT
ENTRY INSTRUCTIONS
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                IGET SECOND OPERAND AND CHECK FOR INDIRECTION
                                                                                                                                           SCANNER SECTION: THIS SECTION SCANS THE INSTRUCTION AND SENDS
THE DECODED TEXT TO THE INTERPRETING SECTION. IT ALSO SETS
SEVERAL FLAGS (FIL-FE) FOR THE FOLLOWING CONDITIONS, RESPECTIVELY:
ALPHA APPEND INSTRUCTION, ANY TEXT INSTRUCTION, ANY ONE WORD
INSTRUCTION, ANY ALPHA OPERAND, ANY INDIRECT OPERAND, AND ANY
                                                              LOOP THROUGH THE INSTRUCTION POINTER BRRRY (AND LOOK FOR A VALID POINTER
                                                                                                 THEN EXTRACT THE INSTRUCTION FROM THE TEXT
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   ONLY ONE WORD LONG
                                                                                                                 SAVE CURRENT MACHINE CODE ARRAY POINTER
                                                                                                                                                                                                                                                                                                                                                                                                                                                           ICHECK FOR ALPHA APPEND TEXT INSTRUCTION
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     INOT A DIGIT ENTRY INSTR.; CONTINUE SCAN
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 ILOOK FOR AN OPERAND OF THE INSTRUCTION ISET FLAG AND RETURN IF ONLY ONE WORD LO
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           FIND END OF TEXT AND CHECK FOR ERRORS
                                                                                                                                                                                                                                                               INITIALIZE FLAGS AND TEXT VARIABLES
                                                                                                                                                                                                                                                                                                                                                                                                            I CHECK FOR A TEXT ENTRY INSTRUCTION
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         PRINT "ERROR IN ALPHA REGISTER ENTRY INSTRUCTION AT LINE # ";J
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        ICHECK FOR A DIGIT ENTRY INSTRUCTION
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  IGET FIRST OPERAND AND CHECK FOR
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                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     IF ((T1$="+") OR (T1$="-") RND (LEN(T$)>1) THEN 2085
IF (T1$=" ") OR (T1$="E") OR (T1$=".") THEN 2085
                                                                                                                                                                                                                                                                                                                                                                                                                        IF (T$[1,1]<>"") AND (T$[1,2]<>"A") THEN 2055
IF T$[1,2]<>"A" THEN 2000
!CHECK FOR ALPHA APPER
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 (T1$CL,LJ<>"'") THEN 2175
!CHECK FOR LENGTH OF |
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    SET DIGIT ENTRY FLAG
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          PRINT "ALPHA STRING TOO LONG IN LINE # ";J
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       SET TEXT FLAG
                                                                                                                                                                                                                                                                                                                                                                                            IF (T$="END") OR (T$=".END.") THEN 3355
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      T1*=T*[1,1]
IF (T1*>="0") AND (T1*<="9") THEN 2085
                                                                                                                                                                                                                               DIGIT ENTRY INSTRUCTION.
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        IF T$[L, L]=""" THEN 2040
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   8
                                                              FOR J=1 TO 2240
IF P(J)(0 THEN 3335
                                                                                                                                                                                                                                                                                                                                                               F1=F2=F3=F4=F5=F6=0
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 T1#=TRIM#(T#[P1+1])
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                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 IF P1<>0 THEN 2130
                                                                                                                                                                                                                                                                                                               IF F6<>1 THEN 1955
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          IF L<18 THEN 2020
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 P2=P0S(T1$," ")
IF P2=0 THEN 2220
                                                                                                 T$=FNI$(A$,P(J))
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                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  T1$=T1$[2,L-1]
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  T$=T$[1,P1-1]
                                                                                                                                                                                                                                                                T1$=T2$=" "
      Listing 1 continued:
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                                                                                                                    E4=M1
                                                                                                                                                                                                                                                                                                                                                                             $1=$S
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CENTRONICS 737 (RADIO SHACK LINE PRINTER IV)

Word Processing Print Quality



. 18 x 9 dot matrix; suitable for word processing • Underlining • proportional spacing • right margin justification • serif typeface • 50/80 CPS • 9½" Pin Feed/Friction feed • Reverse Platen • 80/132 columns

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The Small Business Printer: Letter Quality & Speed.



• 10 x 9 dot matrix • Letter quality print; Lower case descenders . High speed; 165 CPS, Bidirectional, Logic seeking . Wide carriage, Adjustable tractors to 16" . Vertical forms control . Variable line spacing • User programmable character set • Dot Resolution Graphics

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\$2195

EPSON MX80

Low-Priced Professional Print Quality



• 9 x 9 dot matrix • Lower case descenders • 80 CPS • Bidirectional, Logic seeking • 40, 66, 80, 132 columns per line • 64 special graphic characters: TRS-80 Compatible • Forms handling . Multi-pass printing . Adjustable tractors

EPSON MX80 (List \$645)

\$Call

ANACOM

Low Cost, High Speed, Wide Carriage

• 9 x 9 dot matrix • Lower case descenders • Wide carriage • Adjustable tractors to 16" • 150 CPS, Bidirectional, Logic Seeking

EPSON TX80

• 5 x 7 dot matrix • 125 CPS • Double wide characters • Dot resolution graphics & 64 special graphic characters • 6/8 line per inch • Vertical forms control • Adustable tractors • Heavy Duty -Highly reliable

EPSON TX80 (List \$799)

\$Call

\$ Call

OKIDATA MICROLINE SERIES

TRS-80 Graphics Compatibility



• 9 x 7 dot matrix • 80 CPS • 80, 132 columns - 64 shapes for charts, graphs & diagrams . Double wide characters . 6/8 lines per inch • Up to 3 part copy • Friction & pin feed • 200 M character head warranty

OKIDATA MICROLINE 80 (List \$800)

\$599

THE SMALL COMPUTER PRINTER by Brent Weston

This illustrated booklet provides the information you need about today's small printers. There are many printers available, each with different features and capabilities. The Small Computer Printer will guide you in determining which printer best fits your applications. A complete feature comparison chart is included representing over 40 small printers. A printer is a big investment learn all about them before you buy one.



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• Graphics • Tractors/Friction Feed • 2K Input Buffer • RS-232 Serial, Centronics® Parallel, IEEE-488, 20 ma • TRS-80 Cable option • 100 CPS • Fast form feed • User programmable character set • 64, 72, 80, 96, 120, 132 Columns/line • Expanded characters · Automatic skip-over-perforation • Horizontal & Vertical tabs • Programmable vertical line spacing . Intel 8085 Microprocessor - over 40 software commands

BASE 2 800B (List \$699)

\$649

PRINTERS

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OKIDATA MICROLINE 82 & 83	Call
NEC 5530-5 letter quality, RO, parallel, tractors (List \$2970)	2599
PAPER TIGER IDS 440 w/graphics and 2K buffer (List \$1094) s	
PAPER TIGER IDS 460 w/graphics, 9 x 9 dot matrix (List \$1295) :	Call
QUME 5/45 typewriter quality (List \$2905)	2559

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INTERN AGE EGGII MENT	
APPLE II - BASE 2 parallel graphics interface board & cable \$ APPLE II - EPSON TX80	160
	440
parallel graphics interface board & cable	110
SSM AIO BOARD Apple Serial/parallel interface (List \$225)\$	175
MICROTRONICS Atari parallel interface	69
ATARI 850 Interface module, serial/parallel	199
TRS-80 CABLES to keyboard or Exp. interface	

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ISTRUC 2530 2600 T≄[1,	CTION ERROR IN LINE # ";J !ENTER EXPONENT IF IT EXISTS !CHECK FOR NEGATIVE EXPONENT !I]="+"> THEN T\$=T\$[2] !ADD EXPONENT TO MACHINE CODE ARRAY	X=1 Y=59 M(M1)=29 IF T="XEQ" THEN M(M1)=3 L=LEN(T1\$) M(M1+1)=240+L FOR I=1 TO L FOR I=1 TO L S=FNS(X,Y,(T1\$E(I,II),C\$ IF Z<0 THEN 2935 PRINT "INVALID CHRRHCI GOSUB 3400 GOTO 1930 M(M1+1+1)=C2(Z) NEXT I M1=M1+L+2 GOTO 3335 GOTO 3335 IF (V>14) OR (T\$="XEQ") M(M1)=177+V	IBDD LENGTH TO SECOND BYTE (HI ORDER BITS= F HEX) (**) (C+) (C+
) OR (T\$[], 11)-32 55	CT*(I,II)>"9") THEN 2530 2 !OTHER INSTRUCTIONS: CALCULATE VALUE OF OPERANDS !ONE WORD FLAG !ALPHA OPERAND FLAG	M(M1+1)=0 M1=M1+2 GGOTO 3335 i M(M1)=208 IF T\$="XE0" THEN P M(M1+1)=0 M(M1+3)=V MI=M1+3 GGTO 3335	ILONG FORM (3 BYT ISECOND BYTE AGRI ITHIRD BYTE CONTR
IF L<=2 THEN 2705 INUMERIC OPERRND IF L<=2 THEN 2705 INUMERIC OPERRND TOO LONG IN LINE # ". GOSUB 3400 IF L<= THEN 2740 ICHECK FOR DOUBLE IF L<2 THEN 2740 ICHECK FOR DOUBLE IF L<2 THEN 2740 ICHECK FOR DOUBLE IF (V)=0) AND (V<=99) THEN 2805 IF (V)=0) AND (V<=99) THEN 2805 IF LOORRECT NUMERIC OPERAND IN LINE # GOSUB 3400 ICHE # GOSUB 3400 ICHE # ICHE B 3400 ICHE # ICHE B 3400 ICHE B 3400	INUMERIC OPERAND SEEN LONG IN LINE # ";J ICHECK FOR DOUBLE DIGIT OPERAND 2805 PERAND IN LINE # ";J	3020 IF T#<>"STO" THEN 3080 3025 ! 3030 IF V>15 THEN 3055 3035 M(M1)=48+V 3040 M1=M1+1 3050 M(M1)=145 3060 M(M1+1)=V 3065 M(M1+1)=V 3065 M1=M1+2 3065 M1=M1+2	ICHECK FOR STORE INSTRUCTION ISHORT FORM (ONE BYTE) STORE INSTRUCTION ILONG FORM (2 BYTE) STORE INSTRUCTION
G010 1930 V=0 V=0 FOR I=1 TO 26 IF T1#=S1#(1) THEN V=1+101 NEXT IF V<>0 THEN 2805 V=NUM(T1#)-48 IF (V>=0) RND (V<=9) THEN 29 FRINT "INCORRECT STACK OR S G0SUB 3400	ICALCULATE STACK REGISTER OR LOCAL LABEL VALUE 31 ICALCULATE SINGLE DIGIT OPERAND VALUE 2805 SINGLE DIGIT OPERAND IN LINE # ";J	3075 3075 3080 1 T\$	ICHECK FOR RECALL INSTRUCTION ISHORT FORM (1 BYTE) RECALL INSTRUCTION ILONG FORM (2 BYTE) RECALL INSTRUCTION
F5=1 THEN V=V+128 11 (T*<>"GTO") GND (T*<>"XE F5<>1 THEN 2860 11	I CHECK FOR SPECIAL COMMANDS S IADD INDIRECTION PIT (HIGH ORDER BIT) TO OPERAND (T\$<>"XEQ") THEN 30201CHECK FOR 'GTO' OR 'XEQ' COMMAND !CHECK FOR 'GTO IND' OR 'XEQ IND'	IF T*<>"LEN 3265 ! ! ! ! F 4<>! THEN 3210 X=1. Y=59 M(M1)=192 M(M1)=192 ! ENG(T11)	JALPHA LABEL INSTRUCTION
IF F4()1 THEN 2955	ISET HIGH BIT FOR *XEG IND* ISTART WITH *GTO*XEG ALPHA LABEL* COMMAND	MCM1422=241+L MCM1432=241+L MCM1+32=0 M1=M1+2 GOTO 2900	THEY ASSIGNMENT BYTE (SET TO ZERO FOR BAR CODE) HADD CHARACTER DATA TO MACHINE CODE ARRAY Listing 1 continued on page 166

dBASE" II vs. the Bilge Pumps.

by Hal Pawluk

We all know that bilge pumps suck.

And by now, we've found out—the hard way—that a lot of software seems to work the

So I got pretty excited when I ran across dBASE II, an assembly-language relational Database Management System for CP/M. It works! And even a rank beginner like myself got it up and running the first time I sat down with it.

If you're looking for software to deal with your data, too, here are some tips that will help:



dBASE II vs. everything else.

dBASE II really impressed me.

Written in assembly language (with no

need for a host language), it handles up to 65,000 records (up to 32 fields and 1000 bytes each), stores numeric data as packed strings so there are no roundoff errors, has a superfast multiple-key sort, and supports ISAM based on B* trees.

You can use it interactively with English-like commands (DISPLAY 10 PROD-UCTS), or program it

(so when you've set up the formats, your secretary can do the work). Its report generator and userdefinable full screen operations mean that you can even use your existing forms.

And if all this makes your mouth water, but you've already got all your data on a disk, that's okay: dBASE II reads your ASCII files and adds the data to its own database.

Right now, I'm using dBASE II with my word processor for budgeting, scheduling and preparing reports for my clients.

Next come job costing, time billing and

accounting.

Tip #1: Database Management vs. File Handling:

Any list or collection of data is, loosely, a data base, but most of those "data base management" articles in the buzzbooks are really about file handling programs for specific applications. A real Database Management System gives you data and program independence (no reprogramming when data changes), eliminates data duplication and makes it easy to turn data into information.

Tip #2: Assembly Language vs. BASIC:

This one's easy: if you're setting up a DBMS, you're going to be doing a lot of sorting, and Basic sorts are s-l-o-w. Run a benchmark on a Basic system like S*-IV against a relational DBMS like dBASE II and you'll see what I mean. (But watch it: I've also seen one extremely slow assembly-language file management system.)

Tip #3: Relational vs. Hierarchal & Network DBMS.

CODASYL-like hierarchal and network systems, around since the 1960's, are being phased out on the big machines so why get stuck with an old-fashioned system for your micro? A relational DBMS like dBASE II eliminates the predefined sets, pointers and complex data structures of a CODASYL-type DBMS. And you don't need to be a programmer to use it.

An Unheard-of Money-Back Guarantee.

dBASE II is the first software I've seen with a full money-back guarantee.

To check it out, just send \$700 (plus tax in California) to Ashton-Tate, 3600 Wilshire Blvd., Suite 1510, Los Angeles, CA 90010. (213) 666-4409. Test dBASE II doing your jobs on your computer for 30 days. If, for some strange reason, you don't want to keep it, send it back and they'll refund your money.

No questions asked.

They know you don't need your bilge

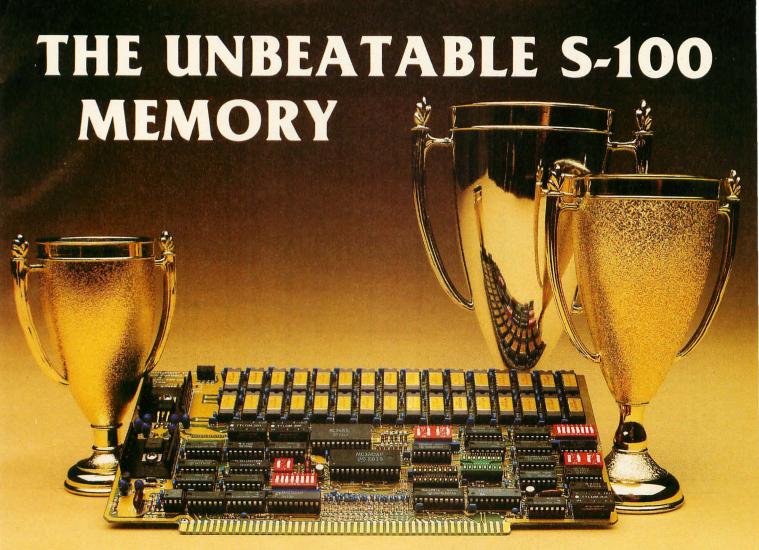


© Ashton-Tate 1980

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THIS ROUTINE LISTS OUT THE COMMANDS AVAILABLE IN TH
THE SYNTAX OF THE COMMANDS AND OF INSTRUCTION ENTRY
                                          * * * PROGRAM COMMANDS LIST ROUTINE
                                                                                                                                                                                                                                                                                                                                                   (BLANKS ARE USED AS DELIMITERS)"
                                                                                                         "COMMRNDS AVAILABLE IN THIS PROGRAM:"
                                                                                                                                                                                                                                                                                                                  "SYNTRX FOR INSTRUCTION ENTRY:"
                                                                                                                                                                                                                                                                                                                                        n <41C INSTRUCTION>"
                                                                                                                                                                                                                                                                                                                              A) INSTRUCTION FORMAT:
                                                                                                                                                                                                                                                                                                                                                                PRINT " B)SPECIAL SYMBOLS:"
                                                                                                                                                                                                                                                                                                                                                                                                                                80
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             IF I>=V1 THEN P(I)=-1
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           IF P(1)=-1 THEN 3715
                                                                                                                                                                                                                                                                                                                                                                                                                     3630 PRINT " C)TEXT FORMAT:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 FOR I=V2+1 TO V1-1
                                                                                                                                                                                                                                                 RUNPRIVATE
                                                                                                                                             DELETE nn
                                                                                                                                                                                                                                                                      SCRATCH
                                                                                                                                                                                                                                                           SAVEPROG
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      FOR I=1 TO 2240
                                                                                                                                                                                                                           RENUMBER
                                                                                                                                  COMPILE
                                                                                                                                                                   GETPROG
                                                                                                                                                                               GETTEXT
                                                                                                                                                                                                     NUMBER
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   K1(I)=P(I)
                                                                                                                                                                                           LIST
                                                                                                                                                         EXIT
                                            *
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  G010 358
                                                                                                                                                                                                                                                                                                                                                                                                                                            3649 GOTO 358
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             NEXT I
                                                                                                                                                                                                                                                                                                                                                                                                                                3635 PRINT "
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    K=V1-
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                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   720
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    END INSTRUCTION: UNCOMPILED POINTER
                                                                                                                                                                                                                                                                STORE INSTRUCTION TYPE IN MACHINE CODE ARRAY
                                                                                                                                                             ALL OTHER COMMANDS ARE TABLE DRIVEN
                                                                                                                                                                                                                                                                                                                                                                                                                                                           ***END OF THE INSTRUCTION DECODE LOOP***
                                                                                                                                                                                                                                                                                      ICHECK FOR CORRECT ONE WORD INSTRUCTION
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                RESET MACHINE CODE ARRAY TO OLD VALUE
                                                                                                                                                                                                                                                                                                                                                        ICHECK FOR TWO BYTE INSTRUCTION FIRST CHECK FOR COMPLETE INSTRUCTION
```

M M M M

IN THIS PROGRAM AND



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4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4
4105 41105 41106 4115 415 4
THIS ROUTINE
4135 ! 4140 IHPUT "GIVE THE NAME 4145 F8=1
4195 READ #1; MC*) 4200 ASSIGN #1 TO * 4205 GOTO 350 4210 * * * * * *
4240 : * * * * * * * * 4245 : 4250 E1=ERRN
GOTO 350 IF E1<>64 THEN
4290 IF ELANSS THEN 4380 4295 PRINT "TAPE IS WRITE 4300 GOTO 350 4305 IF ELANSS THEN 4320 4330 DEINT "IMPONDED FILE
GOTO 350 IF E1<>54 THEN 4
Col.
INFO! "DU 700 WI IF (T2\$="Y") OR GOTO 350
4350 IF E2=3850 THEN 3855 4355 IF E2=4060 THEN 4065 4360 PRINT "ERROR # ";E1; 4365 G0TO 350
00000
4890 1000 1000 1000 1000 1000 1000 1000 1
·

```
GET THE NUMBER OF BYTES IN THE MACHINE CODE ARRAY
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 ISAVE ERROR NUMBER WHERE ERROR OCCURED
                                                                                                                          END OF 'SAVEPROG' ROUTINE * * * * * * * * *
                                                                                                                                                                                                                                                     * "GETPROG' ROUTINE * * * * * * * * * * * * *
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               RROR CONDITION HANDLING ROUTINE * * * * * * *
                                                                                                                                                                                                                                                                                                TRIEVES THE COMPILED PROGRAM FROM CASSETTE TAPE
ISAVE THE NUMBER OF BYTES IN THE PROGRAM
                                                                                                                                                                                                                                                                                                                                                  IE OF THE FILE TO BE READ",TI$

1SET THE COMPILED PROGRAM PRESENT FLAG
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                               E NAME (SHOULD BE SIX CHARACTERS OR LESS)"
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 DOM ON TAPE. PLEASE USE ANOTHER TAPE"
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         END OF 'SAVEPROG' ROUTINE * *
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        PLEASE INSERT TAPE"
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                     * * END OF ERROR ROUTINE * *
                                                                                                                                                                                                                                                                                                                                                                                                                           ICHECK FOR FILE ERRORS
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      LE NAME"
[E2<>4060) THEN 4345
TO USE THE OLD FILE? ",T2$
$4="YES") THEN 4350
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      E PROTECTED. PLEASE FIX"
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            ECTED OR OF WRONG TYPE'
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                              15
15
1 "SEEN RT LINE # "; E2
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          APE DRIVE.
                                                                                                                                                                                                                                                                                                                                                                                                                                                       " QN
```

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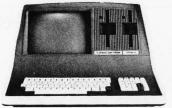
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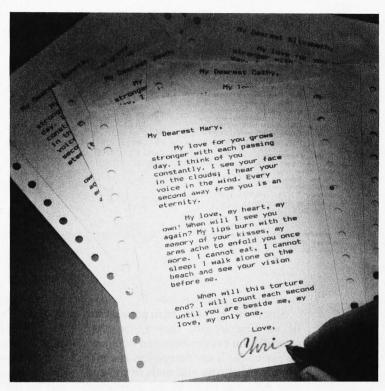
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```
NUMBER FORMAT FUNCTION: THIS FUNCTION CONVERTS A NUMBER INTO A CHARACTER STRING. IT IS USED ONLY IN THE DIABLO PRINTOUT SECTION.
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                ARRAY
0 IF
                                                                                  IGET TEXT UP TO THE SEMICOLON DELIMITER
                                                                                                                                                                                                                                                                                                                                                                                 1) AN INTEGER NUMBER TO BE CONVERTED INTO A CHARACTER STRING
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  CREATES A STRING CONTAINING THE ROW NUMBER AND BEGINNING AND ENDING FUNCTION NUMBERS. IT IS USED ONLY IN THE DIABLO
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                NARY SEARCH FUNCTION: FUNCTION SEARCHES PASSED CHARACTER FOR PASSED KEY AND RETURNS INDEX OF KEY FOUND IN ARRAY OR NO KEY WAS FOUND. PARAMETERS REQUIRED ARE:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        ROW AND INSTRUCTION PRINTOUT FUNCTION: THIS FUNCTION
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        CONVERT ONE DIGIT NUMBERS
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       !CONVERT TWO DIGIT NUMBERS
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        N#=CHR#(I+48)&CHR#(J+48)&CHR#(K+48)&CHR#(N MOD 10+48)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    PRINTOUT SECTION. PARAMETERS NEEDED ARE:
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          N$=CHR$(I+48)&CHR$(J+48)&CHR$(N MOD 10+48)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        DEF FNP$(INTEGER R, INTEGER I, INTEGER F)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          N$=CHR$(N DIV 10+48)&CHR$(N MOD 10+48)
                                                                                                                                                                                                                             *
                                                                                                                                                                                                                                *
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                 R#="ROW "%FNF#(R)%" ("%FNF#(I)
IF I=F THEN 4995
R#=R#%" - "%FNF#(F)
R#=R#%")"
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      2) FIRST INSTRUCTION NUMBER
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         3)LAST INSTRUCTION NUMBER
FOR J=1 TO 50
IF A*[I+J;1]="!" THEN 4745
```



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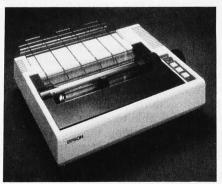
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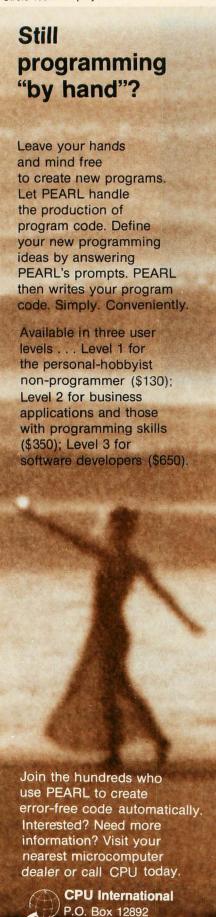
For starters, it features the world's first *dispos*able print head—after it's printed between 50 and 100-million characters, just throw it away. A new one costs less than \$30 and you can change it yourself with one hand. Plus, the MX-80 prints bidirectionally and 80 CPS with a logical seeking function to minimize print head travel time and maximize throughput. Finally—and this is the

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```
Listing 1 continued:
```

```
1) INDEX OF FIRST POSITION (INTEGER)
5045
         2)INDEX OF LAST POSITION (INTEGER)
3)KEY TO BE FOUND (STRING)
5050 !
5055
         4)STRING ARRAY IN WHICH THE SEARCH IS MADE
5969 1
5965
5070 DEF FNS(INTEGER I.J.Q$.A$(*))
5075
     INTEGER F, L, M
5080
      F=I
5085
5090
      M=(F+L) DIV 2
                                     !FIND CENTER OF ARRAY
5095
      IF Q$=A$(M) THEN RETURN M
                                    !IF KEY HAS BEEN FOUND, RETURN INDEX
5100
      IF Q$>A$(M) THEN F=M+1
      IF Q$<A$(M) THEN L=M-1
5105
                                    !CONTINUE SEARCH THROUGH APPROPRIATE HALF
5110
      IF FK=L THEN 5090
                                    !RETURN Ø IF SEARCH FAILS
5115
      M=0
      RETURN M
5120
5125 FNEND
5130
5135 ! *
           ***** END OF BAR CODE GENERATION PROGRAM *******
5140 ! **
```

Text continued from page 150:

Since the HP-41C instructions are of varying length, they quite often straddle the border between two rows of bar code. If an instruction starts in the previous row and ends in the current row, the bytes of the instruction contained in the current row are the leading partial-function bytes. Alternately, if an instruction starts in the current row and ends in the next row, the bytes contained in the current row are the trailing partial-function bytes. The third byte of a bar-code row contains, in the 4 high-order bits, the number of leading partial-function bytes, and, in the 4 low-order bits, the number of trailing partial-function bytes.

A Bar-Code Generating Program

The program given in listing 1, which runs on a Hewlett-Packard HP-9845 minicomputer, allows the user to enter numbered HP-41C instructions and will insert the instructions into a text string for later use. Each instruction is associated with a value between 1 and 2240, which determines the order of execution of the HP-41C instructions. The number 2240 is given as a maximum since that is the largest number of bytes available to the user in program memory.

If the HP-41C program is extremely long, a renumbering command allows the user to create gaps in his numbering scheme to allow for later insertion of instructions. Using this program, the user is able to insert, delete, and replace instructions; the user can save the program in a file for later use.

In response to the prompt symbol, the user may give other single-word commands to compile and generate bar code for the HP-41C programs, save and retrieve the compiled HP-41C machine language, and list or delete the entire program. The syntax and action of each command are given in table 2 and will be printed out by the program if a "??" is typed in response to the prompt symbol.

The basic structure of the program is a main routine that generates the prompt symbol and decodes the input. A series of other routines perform the command functions and are called by a jump table in the main routine. The input to the main routine is decoded only to the extent of determining whether a command or an instruction has been given, and if an instruction has been decoded, the instruction number is calculated. The instruction is then appended to a text string, and a pointer to that instruction is entered into a pointer array at the position given by the instruction number. Consequently, the other routines will be able to retrieve the program by a linear inspection of the pointer array.

Replacement, deletion, and renumbering of instructions only involve manipulation of the pointer array, while insertion requires that the instruction number (an integer) must fall between two existing instruction numbers. The syntax of the HP-41C instructions recognized by this program follows that of the HP-82143A thermal printer and of the program listings distributed by the HP User's Library, with a few exceptions dictated by the difference between ASCII and the HP-41C character set. For example, characters representing the Greek letter Σ , the angle sign, and the # sign are represented by the

Text continued on page 178

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Bar-Code Generator Commands

Compiles the current program and loads the compiled code into the array M. COMPILE

Deletes the instruction given by n from the current program. DELETE n

Halts execution of the bar-code generator or of the line-number generator. **EXIT**

GETPROG Retrieves compiled code from a file on cassette tape. (The routine prompts for a

file name.)

Retrieves program instructions from a file on cassette tape. (The routine prompts **GETTEXT**

for a file name.)

LIST Lists the entire current program.

Automatically generates instruction numbers for HP-41C program entry. The start-NUMBER

ing number and size of the increment are requested by the routine. This routine is

halted by typing "EXIT"

Renumbers the current program instructions. (The routine prompts for the old RENUMBER

starting number, new starting number and size of the increment.)

Generates the bar code from the compiled code. (It may not be run unless com-RUN

piled code has been generated.)

RUNPRIVATE Generates bar code for a private program.

SAVEPROG Stores compiled code for the current program on cassette tape. (The routine

prompts for a file name.)

SAVETEXT Stores instructions of the current program on cassette tape. (The routine prompts

for a file name.)

SCRATCH Erases the current program.

Displays a list of available commands and syntax rules.

Table 2: A table of commands for the bar-code generating program given in listing 1.

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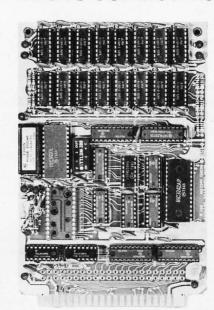
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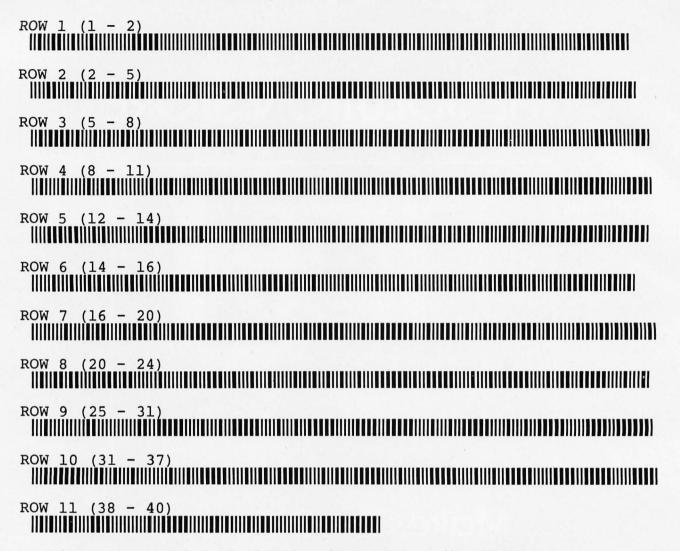


Figure 2: A demonstration program for the HP-41C. This bar-code program was created by an HP-9845 minicomputer connected to a Diablo 1650 printer using a Titan 10 metallic daisy-wheel. The program requires twenty registers within the HP-41C.

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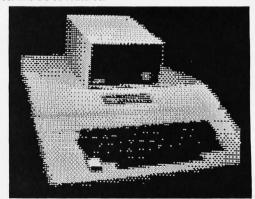
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ASCII characters &, @, and #, respectively. Also, single quotes instead of double quotes are used for text and alphanumeric labels, and an alphanumeric append instruction is indicated by the character A preceding the single quotes and character string. The most important routines called by a command are the compile routine, which decodes the current program held in the text string, and the run routine, which takes the compiled machine code and generates the bit pattern representing the required bar code.

The program listed here was developed on an HP-9845A minicomputer and contains the functions and output statements required to generate bar code on the Diablo 1650 daisy-wheel printer. This is the system used by the HP User's Library to produce bar code at request for any program written either for the HP-41C or for the HP-67 and HP-97 series. The Diablo 1650 printer is used with a 96-character Titan 10 metallic daisy wheel and a Hytype II multistrike film ribbon.

The bars are printed by using the vertical bar (about 160 mils tall and 9 mils wide) on the Titan 10 with a horizontal increment of 1/120 inch. The narrow bars are two characters wide, the wide bars are four characters wide, and the spaces are three blanks wide. Three blanks are used instead of two because the ink generally spreads a slight amount, causing the spaces to shrink and the bars to grow larger. The paper used is the standard one-ply, 81/2 by 11 inch, white computer paper. Figure 2 contains the bar code generated by the User Library's Diablo 1650 for a short demonstration program written for the HP-41C.

The subroutine at line 1605 prints a row of bar code and clears certain variables in preparation for the next row of bar code; this routine must be changed by the user if a different computer/printer combination is used. Copies of the resulting bar code may be made by an office copier if careful attention is paid to contrast, sharpness, and bar width. Many of the less expensive copy machines shrink the size of the bars, thus expanding the size of the spaces and rendering the bar code unreadable. Most of the commercial printing houses have the better copiers needed for this purpose. If many copies are needed, offset printing may also be used as a more expensive but very reliable method for bar-code reproduction.

For further assistance in generating bar code, you can obtain the Hewlett-Packard Creating Your Own HP-41C Bar Code manual (part number 82153-90019), which contains a listing of the program given here and a discussion of bar codes and barcode generation. ■

Editor's Note:

The Hewlett-Packard bar-code format is partially compatible with the PAPER-BYTE® format designed by BYTE Publications Inc in 1977. Fortunately, the compatibility is in the most important place, the representation of 1 and 0 bits within a line of bar code. Although Hewlett-Packard uses different header and trailer bytes to frame the actual bytes of data, the encoding scheme used to encode both the data and the header and trailer bytes is the same in both Hewlett-Packard bar codes and PAPERBYTE® . Hewlett-Packard's decision in this direction strenghtens the authority of the PAPERBYTE® format, and we feel that this is an important step toward the standardization of machinereadable bar code....GW



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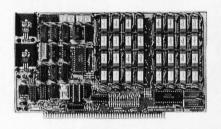
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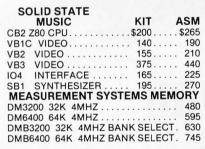


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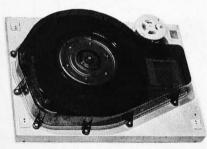
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Systems Notes

Numerical Analysis for the TRS-80 Pocket Computer

Mike Salem, 26A Delancey St, London NW1 7NH, England

Complicated programs can often be easily modified to fit into the new pocket computers. I've taken three programs from the December 1979 issue of BYTE and modified them to run on the Radio Shack TRS-80 Pocket Computer (sold as the Sharp PC-1211 outside of the United States). The Pocket Computer has a 24-character LCD (liquid-crystal display), twenty-six fixed variables, and 1424 bytes of programmable memory.

One of the programs I modified was the discrete-Fourier-transform program that appeared in "Frequency Analysis of Data Using a Microcomputer" by F R Ruckdeschel (December 1979 BYTE, page 10). I also combined two programs that compute the time-domain

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VISA • Master Charge • COD • Company Check TRS-80 is a trademark of Radio Shack, Inc. CP/M is a trademark of Digital Research response of a system with a given transfer function into a single program ("Noniterative Digital Solution of Linear Transfer Functions" by Brian Finlay, December 1979 BYTE, page 144). The modified programs have all of the features of the originals, with the obvious omissions of printing and plotting.

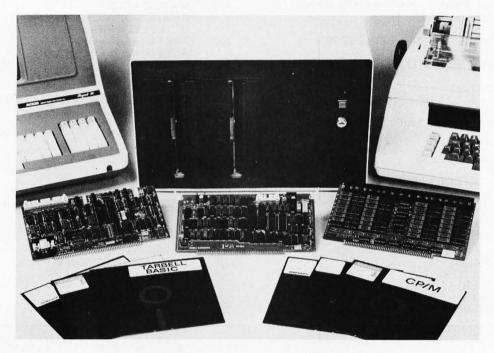
Incidentally, it is important to note that the TRS-80 Pocket Computer, in common with many machines, allows BASIC lines to contain multiple statements (saving 3 bytes of programmable memory for each line number omitted). Although this feature is useful in itself, the TRS-80 Pocket Computer also has an IF statement that can control all of the remaining statements in the

Listing 1: A discrete-Fourier-transform program for the TRS-80 Pocket Computer. This program was modified from "Frequency Analysis of Data Using a Microcomputer" by F R Ruckdeschel (December 1979 BYTE, page 10). Statements entered on the same line are separated here for clarity.

```
10
       :REM BYTE DEC 79
 11
       :RADIAN
190
       :INPUT "1ST X? ";Z,"LAST X? ";Y,"#OF POINTS? ";N
250
       \cdot T = 1
       :INPUT "I/P SCALE FACTOR? ";I
       :IF I < 1 GOTO 250
290
       :D = (Y - Z)/(N - 1)
       :Q=0
       :V = \pi/DI
       : U = V/(N-1)
       :FOR I = 1 TO N
       :PAUSE "NEXT # = ";I
       :BEEP 1
       :INPUT "NEXT F(T) VALUE? ";O
       :A(I+26)=O
       :NEXT I
370
       \cdot B = 0
       :FOR I = 27 TO N + 26
       :IF B > A(I) LET B = A(I)
410
       :NEXT I
             FOR I = 27 TO N + 26
420
             A(I) = A(I) - B
            NEXT I
       :B = ABS B
       :FOR I = 27 TO N + 26
       :IF T < A(I) LET T = A(I)
       :NEXT I
510
       :FOR I = 1 TO N
:W = (I - 1)*U
710
       :C = 0
       :P=0
            FOR M = 1 TO N
            X = Z + (M-1)^*D
            G = WX
770
            C = C + A(M + 26)*COSG
            P=P+A(M+26)*SIN G
            NEXT M
800
       :F = \sqrt{(PP + CC)^*D}
       :IF I=1 LET C=C-NB
                F = D^*ABSC
810
       :PRINT U*(I-1);"RAD/S"
       :PRINT "AMPL. =";F
:IF C <> 0 LET O = ATN(P/C)*180/\pi
815
                     PRINT "PHASE = ";O
820
       :NEXT I
       :BEEP 3
       :PRINT "END OF RUN"
```

:END

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Systems Notes

same line. Since this makes listings a bit difficult to read, I prepared listings 1 and 2 with a separate statement on each line.

Listing 2: A program for the TRS-80 Pocket Computer that computes the time-domain response of a system with a given transfer function. The program shown was combined and modified from two programs contained in "Noniterative Digital Solution of Linear Transfer Functions" by Bryan Finlay (December 1979 BYTE, page 144).

```
:REM "TF: TRANSFER FCN - BYTE DEC 79"
 10
 70
       :RADIAN
       :INPUT "CONST.? ":K."#TERMS NUM.? ";E,"#TERMS
       DEN.?";L
       :IF E = 0 GOTO 240
150
160
            FOR G = 27 TO E + 26
            O = 10 + G
            INPUT "RL, NUM.?"; A(G), "IM, NUM.?"; A(O)
            NEXT G
240
       :IF L = 0 GOTO 330
            FOR H = 47 TO L + 46
250
            O = 10 + H
            INPUT "RL, DEN.?"; A(H), "IM, DEN.?"; A(O)
            NEXT H
330
       :FOR G=1 TO L
       :O = 66 + G
       :O = 76 + G
       :A(O) = 1
       :A(Q) = 0
       :IF E = 0 GOTO 450
370
            FOR H=1 TO E
            D = A(26 + H) - A(46 + G)
C = A(36 + H) - A(56 + G)
380
            M = \sqrt{(DD + CC)}
```

```
N = ATN(C/D)
            IF D<0 LET N=N-\pi
410
            A(O) = MA(O)
             A(Q) = N + A(Q)
            NEXT H
450
       :FOR R = 1 TO L
       :IF R = G GOTO 501
465
       :D = \bar{A}(46 + R) - \bar{A}(46 + G)
       :C = A(56 + R) - A(56 + G)
       :M = \sqrt{(DD + CC)}
470
       :N = ATN(C/D)
       :IF D<0 LET N = N - \pi
500
       :A(66+G) = A(66+G)/M
        :A(76+G)=A(76+G)-N
501
        :NEXT R
       :NEXT G
520
       :INPUT "T(0)? ";O,"DT? ";S,"# STEPS? ";N
       :T = O + NS
620
        :U = -S
        :FOR Q=1 TO N
        :U=U+S
        :V = 0
       :W = 0
        : H = 1 + INT((U - O)/S)
650
             FOR G=1 TO L
             X = A(66 + G) * EXP(-UA(46 + G))
             Y = A(76 + G) - UA(56 + G)
             V = V + X*COS Y
             W = W + X*SIN Y
             NEXT G
        :Z = K^* \sqrt{(VV + WW)^*SGN V}
710
        :BEEP 1
       :PRINT "TIME = ";U
:PRINT "RESP. = ";Z
730
       :NEXT Q
```

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Languages Forum

A Bug in BASIC

W D Maurer, Dept of Electrical Engineering and Computer Science, The George Washington University, Washington DC 20052

The purpose of this article is to describe and analyze a particular bug that is common to a number of BASIC systems for microcomputers. Specifically, of fifteen microcomputers surveyed, four of them had this particular bug in their BASIC, nine of them did not, and the remaining two had the bug in one version of their BASIC but not in the other. The bug is illustrated by a simple BASIC program that runs properly on the systems that do not have the bug and encounters a run-time error on systems that do have it. By comparing the program inputs that cause erroneous behavior with those that do not, the cause of the bug is traced, and two possible corrections are suggested. One of these is quite elegant and results in almost no change in running time or space requirements. It is, of course, rather common for programmers to accuse either the hardware or the system software of being at fault when their programs have bugs. The analysis here may serve as an example of a valid isolation technique of a bug's source in system software.

The program illustrating the bug is shown in listing 1. It accepts some numbers from the keyboard, checks for the presence of the number 0, and checks for duplications. Sample inputs and outputs are shown in listing 2. Of the six test cases in listing 2 on page 190, only Test IV and Test VI cause problems; both correct and erroneous behavior are shown. Table 1 gives the names of the microcomputer systems and their respective behavior.

There are no easy explanations for the presence of this bug. As should be evident from table 1 on page 194, many of the lowest-priced systems are free from the bug,

Listing 1: A BASIC program that sometimes causes a NEXT WITHOUT FOR error.

20 PRINT "HOW MANY NUMBERS?" 30 INPUT N PRINT "INPUT ";N;" NUMBERS" 50 FOR C = 1 TO N 60 INPUT T(C) NEXT C 70 FOR C=1 TO N 80 IF T(C) = 0 THEN 130 90 NEXT C 100 PRINT "ZERO IS NOT PRESENT" 110 GOTO 140 PRINT "ZERO IS PRESENT" 120 130 140 FOR R = 1 TO N - 1150 FOR C = R + 1 TO N 160 IF T(R) = T(C) THEN 210 NEXT C 170 180 NEXT R PRINT "NO DUPLICATIONS" GOTO 220 190 200 PRINT "T(";R;") = T(";C;")" 210

as are many of the highest-priced systems. A large proportion of the BASIC systems surveyed, with and without the bug, were produced by a single software supplier; other systems, with and without the bug, were not. We draw no general conclusions about the general relative suitability of the various systems; many of the systems that exhibit the bug have numerous advantages when compared to systems that do not have it.

As we shall see, there are various ways to circumvent the bug. That is, we can rewrite the program so that it still does the same thing as before, without encountering the bug, and we can also do this in a variety of ways. This, however, does not change the fact that there is a bug. We have the incontrovertible evidence of a simple program that clearly ought to run, that does run on nine microcomputer systems, and does not run on another four systems.

The bug has to do with FOR...NEXT loops in which there are abnormal exits. Many programmers are still under the erroneous impression that this is illegal—that you are not supposed to jump out of a FOR loop. On the contrary, it is illegal to jump *into* such a loop. Abnormal exits from loops are absolutely necessary in programming for such tasks as searching (as illustrated here), error exits, and, in general, the treatment of special cases.

Let us now analyze the bug. It is clear from listing 2 that the problem arises at statement 180. The error message, NEXT WITHOUT FOR ERROR IN 180, means that there is a NEXT statement (180 NEXT R) that does not have a corresponding FOR statement. But this is clearly false; there is a corresponding FOR statement (140 FOR R=1 TO N-1).

Is the problem the expression N-1 in statement 140? If statement 140 is changed to 140 Z=N-1 and 145 FOR R=1 TO Z, the bug is still there. So this is not the problem.

Can we ever get to statement 180 without encountering the bug? If we look at Test I, we see the message NO DUPLICATIONS. Clearly this was printed at statement 190, and there are no jumps to 190 in the program, so the only way to get to 190 is through 180. Thus, in Test I, the computer got through statement 180 with no problems.

How did we get to statement 180? There are no jumps to 180 in the program either; so we must have gotten there from 170 NEXT C. Could this have caused the problem? Since the problem is that the system thought it was not in a loop when it got to statement 180, we now consider the possibility that the system thought it was coming out of an *outermost* loop at 170 NEXT C.

Could the system have thought it was coming out of one of the earlier loops? The FOR statement corresponding to 170 NEXT C is 150 FOR C=R+1 TO N. But there are two earlier FOR loops that use C, one starting at 50 and the other starting at 80. Could this be the source of the confusion?

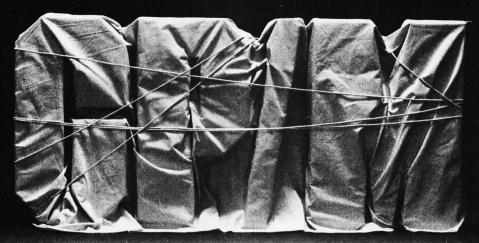
If so, it was probably the loop starting at 80 that caused the problem. The loop starting at 50 is completely self-contained, but the loop starting at 80 has an abnormal exit: 90 IF T(C) = 0 THEN 130. Here is our hypothe-

220

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Listing 2: Test runs of the program in listing 1. Test IV and Test VI can each return two sets of behavior, one for versions of BASIC that correctly execute the program and one for versions of BASIC that do not.

that correctly execute the program and	, , , , , , , , , , , , , , , , , , , ,		
JRUN HOW MANY NUMBERS? ?5		JRUN HOW MANY NUMBERS? ?5	
INPUT 5 NUMBERS ?6 ?7 ?8 ?10 ?12 ZERO IS NOT PRESENT NO DUPLICATIONS	Test I	INPUT 5 NUMBERS ?2 ?6 ?8 ?2 ?0 ZERO IS PRESENT T(1) = T(4)	Test V
JRUN HOW MANY NUMBERS?		> RUN HOW MANY NUMBERS? ?5	
?5 INPUT 5 NUMBERS ?4 ?7 ?2 ?4 ?10 ZERO IS NOT PRESENT T(1) = T(4)	Test II	INPUT 5 NUMBERS ?7 ?0 ?24 ?1 ?24 ZERO IS PRESENT T(3) = T(5)	Test VI (correct)
JRUN HOW MANY NUMBERS? ?5 INPUT 5 NUMBERS ?3 ?7 ?9 ?23 ?9 ZERO IS NOT PRESENT T(3) = T(5)	Test III	JRUN HOW MANY NUMBERS? ?5 INPUT 5 NUMBERS ?4 ?0 ?7 ?12 ?6 ZERO IS PRESENT ?NEXT WITHOUT FOR ERROR IN 180	Test IV (erroneous)
> RUN HOW MANY NUMBERS? ?5 INPUT 5 NUMBERS ?4 ?0 ?7 ?12 ?6 ZERO IS PRESENT NO DUPLICATIONS	Test IV (correct)	JRUN HOW MANY NUMBERS? ?5 INPUT 5 NUMBERS ?7 ?0 ?24 ?1 ?24 ZERO IS PRESENT ?NEXT WITHOUT FOR ERROR IN 180	Test VI (erroneous)

sis: when this abnormal exit was taken, the system did not realize that it was not in a loop any more. Then, when it came to 170, it thought that it was finally coming out of the loop that started at 80. Since this loop was an outermost loop, the system thought that it was no longer in any loops at all. Under these conditions (if they existed), a NEXT statement, such as the one at 180, would truly be an error.

This hypothesis is certainly plausible, but it has to be checked. Specifically, does it account for the fact that Tests I and III worked, while Tests IV and VI did not? In Tests I and III, we print ZERO IS NOT PRESENT. This was done at 110, and it is not too hard to see that in this case the abnormal exit is not taken; we never jump from 90 to 130. In Tests IV and VI, we print ZERO IS PRESENT, and under those conditions we do jump from 90 to 130. This behavior is consistent with our hypothesis.

Why did Test V work? The message T(1)=T(4) is printed by Test V. Looking at statement 210, we can see

that we must have had R=1. Looking at statement 140, we can see that we must have been in the *first* iteration of that loop (since R=1) and that we made an abnormal exit from 160 to 210. Thus 180 was never executed. Again this behavior is consistent with our hypothesis.

What happens if we change C to D in the earlier loop? If we go back to statements 80, 90, and 100, and change C to D throughout these statements, the bug disappears. If we change C to D throughout the loop at statements 50, 60, and 70 (and leave 80, 90, and 100 without change), the bug does not disappear. This tells us two things. First, the bug has nothing to do with the loop at 50, 60, and 70 (again consistent with our hypothesis). Second, the bug definitely does have something to do with variable names. The confusion is between FOR C at 80 and FOR C at 150, and the confusion goes away if one of these is changed to FOR D and if other corresponding changes are made.

What happens if we change the earlier loop so that

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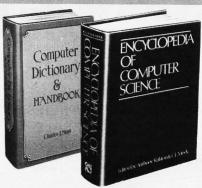
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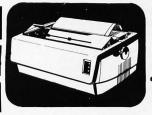
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Table 1: A list of computer systems' running versions of BASIC that do and do not run correctly due to a bug in their handling of the FOR...NEXT loop. The systems listed here were tested on November 12 and 13, 1980.

there is no FOR statement? This can be done by simply changing 80 to C=1 and then replacing 100 by two statements: 100 C = C + 1 and 105 IF C < = N THEN 90. If this is done, even though the same variable name C is still used in two places, the bug disappears. This is further evidence for our hypothesis, because now there is no confusion about which FOR statement corresponds to the NEXT statement where the bug appears.

The above changes illustrate ways of working around the bug. If you have a FOR loop with an abnormal exit, you will never find the bug if that particular FOR loop has a uniquely named loop-index variable. That is, if it ends with NEXT α , then nowhere else in the program should there be a statement NEXT α with the same α .

Now let us dig a little deeper. At statement 90, the exit goes to 130, while the loop involves only statements 80, 90, and 100. Why can't some of our BASIC systems tell that the exit at 90 is an abnormal exit? Presumably because they have no information whatsoever as to where loops start and end. Why would this be the case? There is a plausible explanation having to do with the relationship between the BASIC interpreter and its

Many of the BASIC systems that exhibit the bug have a very close coupling between running and editing a BASIC program. The two activities, in fact, can be carried on alternately with very little internal data processing to accompany the switch-over from running to editing or from editing to running. Simple editing, however, may produce far-reaching changes in the loop structure of a program. Adding or deleting a single FOR or NEXT statement can cause the pairing of other FOR and NEXT statements to be changed, even though they are widely separated from the added or deleted statement. Therefore, the decision must have been made not to keep FOR...NEXT pairing information at run time, with the hope that it would never really be needed. As we can see, Murphy's law is applied in this case with a vengeance.

Let us now examine the bug technically in terms of stacking considerations. This will also suggest methods of fixing the bug.



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Languages Forum.

At the start of a FOR loop, certain information is stacked; upon normal exit from that loop, it is unstacked. Upon *abnormal* exit from a loop, the information is also supposed to be unstacked, but in most cases it does not matter whether the information is unstacked or not. In this case, however, it appears to matter. The sequence of events is as follows:

- •At statement 50, we enter a loop, and C is stacked. Clearly, the loop-index-variable name must be stacked, along with other information that we shall ignore for the moment.
- •At statement 70, we make a normal loop exit, and C is unstacked, leaving the stack empty.
- •At statement 80, we enter another loop, and C is stacked again.
- At statement 90, if we make the abnormal exit from this loop, C is supposed to be unstacked; but let us assume for the moment that it is not.
- •At statement 140, we enter another loop, and R is stacked.
- At statement 150, we enter a third loop, and C is again stacked. Note that we are now in two loops, although the system thinks that we are in three.
- At statement 170, we exit from a loop, and C is unstacked. But C is on the stack twice. Which version of C is unstacked? It must be the one at the *bottom* of the stack, because, according to our analysis, when we get to statement 180, the stack is empty. Then we try to unstack an entry, and, since it is empty, we signal an error.

This gives a clue to fixing the bug in an imaginative way. Of course, one way of fixing the bug is to simply keep the relevant FOR...NEXT pairing operation around at run time. But a simple change in the handling of NEXT statements would also fix the bug in this case. We must search the stack for the right information to unstack, and the trick is to search the stack *downwards* from the top, rather than upwards from the bottom. If we had done this, we would have unstacked the right version of C, and the bug would not have occurred.

Are there any other ill effects from leaving extra information on the stack that should be unstacked, as is done by those systems that have the bug? At the end of the execution of the program, the stack will not be empty. Since this could also happen if there were a FOR statement in the program without a corresponding NEXT, this indication might be given (erroneously) at the end of the run. (The Data General D2 microcomputer system appears to exhibit this behavior.) Another possible unwanted effect is unlimited stack growth, causing stack overflow. If an abnormal exit causing extraneous stack information is inside an outer loop, then unwanted stack information can continue to pile up—eventually resulting in overflow. This situation is more serious on a Z80-based system than on a 6502-based system, since the stack on the 6502 is confined to hexadecimal addresses 0100 thru 01FF, and it wraps around when it overflows.

In conclusion, let it be carefully noted—as is necessary in this fast-changing field—that all the information in this article is as of November 12 and 13, 1980. ■



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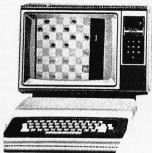
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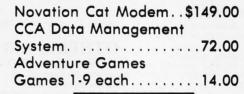
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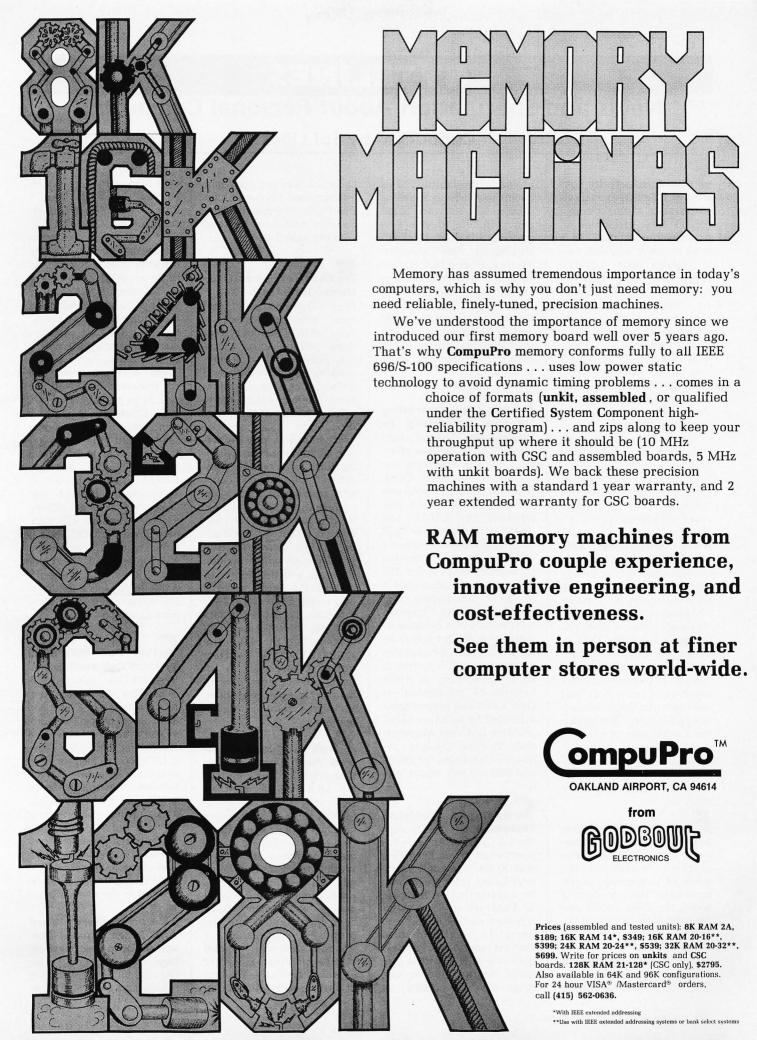
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from





BYTELINES

News and Speculation About Personal Computing

Conducted by Sol Libes

uperconductivity At Room Temperatures Reported: A breakthrough for the next generation of supercomputers may have been made. It was previously thought that superfast computers, using Josephson junctions, would require supercooling to a temperature near absolute zero. Now, Fred W Vahldiek of the Wright-Patterson Air Force Base, Dayton, Ohio reports that he has achieved superconductivity at room temperatures. Vahldiek has developed titanium borite crystals with zero resistance.

Further research will be required to determine whether or not this could lead to the development of computers with picosecond machine cycles and 100% power efficiency.

BM Announces 370-On-A-Chip: IBM has disclosed what many already suspected: it has implemented the circuitry of a model 370 processor on a single integrated circuit. IBM has created a 370 model 138 processor that utilizes 5000 circuits and Schottky-clamped bipolar TTL (transistor-transistor logic) technology that can execute 2000 instructions per second. The device has a cycle time of only 100 ns and consumes 2.3 watts. It is part of a research project, and no specific plans for a product have been announced.

Fight For 16-Bit Microprocessor Market: It appears that the 16-bit microprocessor market is the scene of a three-way battle between the Motorola 68000, the Zilog Z8000, and the Intel 8086. Although the 68000 is ranked first in performance and the 8086 is ranked last, the volume of sales is greater for the 8086. Intel has a two-year lead in

product availability. This means that there is already a substantial software base and peripheral device support. Furthermore, Intel has introduced 8086 enhancements such as a 10 MHz version, an arithmetic co-processor, and a new 32-bit microprocessor, the iAPX-432, that may undercut the 68000 and Z8000. Intel expects to start shipping samples of the iAPX-432 in two or three months.

UNIX-Like Operating Systems Increasing in Popularity: Several software suppliers are now offering UNIX-like operating systems that may rival CP/M. The first UNIX-like software package, called TYNIX, was released for LSI-11 and Heath H-11 systems in 1978 by the Boston Children's Museum. In 1979, Yourdon announced OMNIX for Z80 computers and advertised it as CP/M compatible and similar to UNIX. Yourdon then withdrew it because of software bugs, but it may be released again. Whitesmiths released its IDRIS system in early 1980. Also in 1980, ElectroLabs introduced its OS-1 UNIX-like system (now marketed by Software Labs), and late last year Microsoft and Morrow Designs announced packages for Z8000 and Z80 systems, respectively.

Copyright Decision Overturned: In Chicago, the US Court of Appeals has overturned an earlier ruling that ROM- (read-only memory) based software cannot be copyrighted. In the case of Datacash vs JS & A (as reported earlier in this column), the court had ruled that the marketing of a chess game by JS & A with a program identical to the one originally developed by

Datacash was not copyright infringement because under the 1909 copyright law the program could not be read with the naked eye.

tlons Released: Xerox, Digital Equipment, and Intel have published specifications for the Ethernet system developed by Xerox. Ethernet provides a local networking system for word and data processing applications. Xerox has already released some Ethernet products.

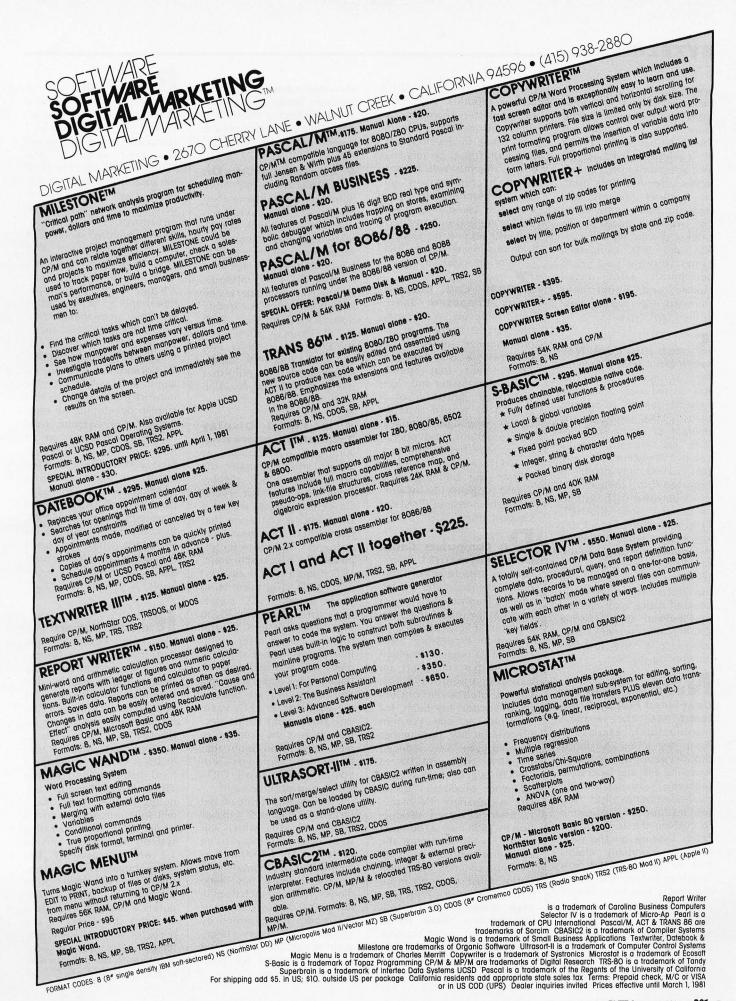
Ethernet is a passive system and does not use switching logic or a central computer. Rather, coaxial cable and communications transceivers attach each machine to the network; each machine is assigned a 48-bit address. Data is transferred in serial groups which include the data and the addresses of both the sender and the addressee. Each transceiver monitors the cable for data with its address. It is expected that the IEEE (Institute of Electrical and Electronics Engineers) will integrate the Ethernet specifications into the networking standard currently in development.

da Language Finalized And The Rush is On: Ada, the language that the DOD (Department of Defense) expects to eventually replace all other languages, has been finalized, according to Jean Ichbiah, president of Apsys, Washington DC. Over nine hundred revision proposals were submitted, and several major improvements have been incorporated into the proposed Ada language standard that was released in 1979. The most significant improvement is the addition of tasking. The Ada Reference Manual may be obtained from the DOD's DARPA office, 1400 Wilson Blvd, Arlington VA 22209.

At least twenty-five companies and universities are reported to be in the process of developing compilers for the Ada language. A few universities have already had their Ada compilers running. However, the first commercial release has yet to occur. Intel claims that its new 32-bit microprocessor, due for release shortly, will use Ada as its primary language. WD (Western Digital) is rumored to be working on a single-board Ada computer that is similar to its Pascal MicroEngine. WD has purchased a 20% interest in Telesoftware Inc of San Diego, which is developing an Ada compiler. (Dr Kenneth Bowles of UCSD Pascal fame owns an additional 40% interest in the company.) Reportedly, Telesoftware already has a preliminary version of its Ada compiler running.

P/M For 8086/8088 Systems Released: Digital Research has released CP/ M-86. This operating system is designed for 8086- and 8088-based systems and provides the same facilities and file format as CP/M, release 2. CP/M-86 can also function as a slave node in a CP/NET network. As with 8080-based versions of CP/M, the logicand hardware-dependent portions of CP/M-86 are modularized for ease of customization. Digital Research also plans to release MP/M and PL/I for 8086/8088-based systems in the near future.

Montgomery Ward And Sears Expand Personal Computer Market-Ing: After test marketing Ohio Scientific computers in selected stores, Montgom-



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ery Ward has decided to expand its personal computer sales into one hundred stores. The stores will sell the OSI Challenger 1P and 4P cassette-based systems with accessories such as disk drives, video monitors, printers, security systems, and software. Sears is now carrying two full pages in its catalog promoting the Atari 400 and 800 computers, games software packs, and peripherals. Other retail chains and department stores are expected to follow in their footsteps.

System Puts Local Network On Cable TV: Sytek Inc, Sunnyvale, California has introduced a packetnetwork system to support up to 24,000 terminals and operate at up to 9600 bps (bits per second) over a cable TV system. This system, called "LocalNet," is expected to fill the gap that exists between such systems as Ethernet and ARPANET. Ethernet is limited to a 1 to 2 km distance while ARPANET is committed to long-distance distributed processing. LocalNet can cover up to 50 km distances on a single coaxial cable and can be piggybacked onto existing CATV cable systems, thus providing a very low-cost networking system.

EC Claims Cure For **Dual-Sided Floppy Prob**lems: NEC, the Japanese manufacturer, claims to have developed a floppy disk system which eliminates the disk and head wear problems associated with dual-sided floppy disks. NEC uses an "air" shock absorber to cushion the force of the heads landing on the disk, and the company claims that its new FD1160 Soft Touch drive provides twice the media and head life of competitive drives.

Standard For 32-Bit Bus: The IEEE has formed a committee to draft a backplane bus standard, designated as P896, for 32-bit microcomputers. According to committee chairman Andrew Wilson, P896 is already well along in development, and a draft may be released soon. The bus will support 32-bit microprocessors under development by Intel and other companies. It will be processor-independent and will support up to sixty-four bus masters and clock rates of up to 20 MHz.

48000 Call Conventions Proposed: Microsoft, Bellevue, Washington (the largest supplier of microcomputer software) has proposed a standard for Z8000 calls that specify parameter-passing and register usage. Adoption of a standard would enable Z8000 languages, application programs, and operating systems to be more easily interfaced, and would facilitate the building of a Z8000 program library similar to the present CP/M User Group Library.

Do Computers Cause **Unemployment?** Calvin C Gotlieb, a professor of Computer Science at the University of Toronto, delivered a paper at the recent IFIPS (International Federation of Information Processing Societies, Inc) Congress-80 which claimed that computers are causing unemployment. Gotlieb cited dozens of studies to support his claim; for example, at one Western Electric facility, the number of employees was reduced by 50% (from 39,200 to 19,000) over a six-year period, while production doubled. A Japanese TV manufacturer increased production by 25% over a four-year period, while reducing the number of workers by 50%. Gotlieb contends that computers must be used more wisely, and cited a West German study that stated: "(C)omputers make things more formal, more routine, more bureaucratic and inevitably lead to less humane treatment of people." He also cited a law on the West German books that complains: "(O)nce a decision is made by a computer, no one is permitted to challenge it."

Amateur Robotics On The Rise: More and more hobbyists are building their own robots. The evidence is the fact that there are already several companies supplying robot parts to hobbyists and two magazines catering to their interests. Hobbyists seeking parts and kits should write to: Hobby Robotics Company, POB 997, Liburn GA 30247, and the Robot Mart, 19 W 34th St, New York NY 10001. Robot Mart also publishes the Hobby Robot Newslet-

Flat-Panel Display Technology Improving: Although CRTs (cathode-ray tubes) still dominate the computer-terminal display field, it appears that several flat-screen systems will soon be ready to challenge that dominance. The new technologies include electrophoretic, electrochromic, LCD (liquid-crystal display) and LED (light-emitting diode) systems. LCD panels are already available in 1-and 2-line versions. Several firms will soon offer multiline panels. Dot-matrix displays are also under development by several firms, and prototypes are becoming available in LED, vacuum fluorescent, and electroluminescent technologies. There is no doubt that flat-screen terminals will compete with small CRTs within two or three years.

One manufacturer of flat screens is Optotek Ltd, of Ottawa, Canada, which will soon offer a display using LEDs that are 1/8000 inch in diameter. Each square inch of the display has 4000 diodes. A 3-by 4-inch display has 49,000 diodes. Control of the diodes is performed by special VLSI (very large-scale integration) integrated circuits provided for each square-inch block.

Random Bits: As of January 1, 1981, Radio Shack has stopped production of the TRS-80 Model I computer, in anticipation of increased sales of the TRS-80 Model III....The IEEE has established a committee to develop a standard for benchmark programs for microprocessor users.... Several hundred workers at the Minneapolis Star and Tribune newspaper recently went on strike to protest, among other things, the newspaper's experimental electronic newspaper project with CompuServe Inc.... Japan's NTT (Nippon Telegraph and Telephone Public Corporation) will soon inaugurate a public facimile network that may be the first step in developing an electronic mail system....Intel has released prices on its new 2764 64-K-bit (16K by 8 bits) 250ns EPROM: \$163 each in lots of one hundred....Seventy to eighty percent of all TRS-80 Model II systems are running CP/M; this statement is based on the fact that Lifeboat Associates has already sold 4000 copies of CP/M for the Model II.

Random Rumors: Apple Computer Company may be setting up its own floppydisk manufacturing operation to make double-sided double-density drives for its new Apple III Computer. Introduction of the drive is expected by mid-year.... Sources say that Radio Shack is close to releasing a hard-disk drive for the TRS-80 Model II and III computers. Further, Radio Shack will soon release version 1.3 of its DOS (disk-operating system) to replace version 1.2 which, reportedly, has many bugs. Unfortunately, the two versions will not be compatible....Altos Computers is said to have switched from the Z8000 to the 8086 for its new 16-bit system. This decision was probably due to the introduction of the CP/M-86 from Digital

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The VP-3301 offers you a choice of two software-selectable display formats: Either 40 characters by 24 lines. Or 20 characters by 12 lines.

The terminal's resident character set consists of 52 upper and lower case alphabetics, 10 numerals, 32 punctuation/math symbols, and 31 control characters.

You can also define a total of 128 of your own characters. Including: Greek letters and other foreign alphabets, graphic symbols, large graphics building blocks, playing card suits, unique character fonts, and "little green men."

The keyboard section features flexible-membrane key switches with contact life rated at greater than five million operations. A finger positioning overlay and positive keypress action give good operator "feel".

An on-board sound generator and speaker provides aural feedback for key presses and may also be activated with escape sequences to provide an audio output.

The sealed keyboard surface is spill proof and dust proof. This combined with high noise immunity CMOS circuitry makes the VP-3301 ideal for hostile environments.

Output is industry standard asynchronous RS232C or 20 mA current loop with six switch selectable baud rates and 8 selectable data formats.

The terminal can be connected directly to a 525 line color or monochrome monitor. Or to a standard TV set using an Rf modulator.

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Research....North Star Computers might be developing a single-board 8088-based system that will work with a hard disk and support CP/MWhitesmiths Ltd is rumored to be about to release an 8088/8086 version of its C compiler....A California firm may be readying an under \$300 OEM (original equipment manufacturer) daisywheel printer that would be set for introduction by the end of the year.

Predictions, Predictions...In my December 1979 column I made eleven predictions for 1980. Several readers asked me to grade myself on how well I did, so here goes:

- 1. The first Japanese personal computer system will become available in this country. Score a "correct." In fact, several have been introduced and reported on in this column. Look for many more in 1981.
- 2. Competitive pressures on small manufacturers will increase. This will cause several mergers, consolidations or acquisitions. Score a "correct" on this one too. So many failures, mergers and acquisitions occurred that they are too numerous to be mentioned. More will be forthcoming in 1981.
- 3. A sizable number of audio and office equipment retailers will enter the computer retailing business. This will create pressures on conventional computer stores. We may even see the appearance of stores that sell only software, much like audio record stores. Score a "maybe." Although some steps have been taken in this direction (eg: Bell & Howell and several other audio/ visual and office equipment suppliers), the real first step has yet to be taken...possible developments this year or
- 4. 16-bit microcomputer systems will be common-place. Score a "maybe" on this one too. Although several 16-bit systems were introduced, lack of 16-bit

parts and software limited their adoption. We should see a significant increase in their acceptance in 1981 with the availability of CP/M, MP/M, UNIX and other powerful operating systems.

- 5. IBM, DEC, Data General, H-P and other minicomputer makers will introduce low-cost microcomputer systems. Score a "partial" on this one, as H-P (Hewlett-Packard) introduced the HP-85 and IBM showed its S-100 product in Europe but withheld it from the US market. These companies may jump in this year or next.
- 6. Several personal computer manufacturers will introduce second-generation machines with significant increases in power. Score a "no." Although Apple, Tandy and Commodore all introduced new machines, none were significantly different from their previous units. I look to 1981 for the introduction of a machine with significantly new performance versus price mark. 7. The emphasis will shift from hardware to software. BASIC will continue as the dominant language. Score another "correct." This year should see continued improvements in disk operating systems and applications packages.
- 8. Business application software for microcomputer systems will finally come of age and provide the needed performance that suppliers have been promising but not delivering during the past two years. Score a "correct."
- 9. The first low-cost microcomputer-based robot kit will be introduced. Score an "incorrect." Although a robotic arm kit was introduced, its price was beyond the means of most personal computerists. Maybe this prediction will come true in 1981.
- 10. Typewriters will have built-in intelligence, and use microprocessors, built-in microdisks, and word processing features. The dumb typewriter will soon be a thing of the past. Score an

"incorrect." Although Smith-Corona and Triumph-Adler introduced electronic type-writers, their intelligence is still on a primitive level. I am now projecting 1982 or 1983 on this development.

11. Personal computer time-sharing systems will proliferate. Score a definite "correct" on this one.

All in all, I would rate my prediction ability as "fair": about sixty points out of a possible one hundred. Where I guessed wrong I was just ahead of the industry.

Predictions For The Future: Not allowing my previous performance to deter me, I will venture forth with some more predictions:

- 1. The S-100 will become the *de facto* standard for bus interfacing. There are already thirty-two manufacturers of S-100 systems, and I expect this number to increase to over forty in 1981 (and to include IBM). This trend should continue into the mid-1980s, when we may see the development of a new interface bus to accommodate new hardware and architectures.
- 2. Hardware will become more sophisticated and less expensive. This is not a difficult prediction to make, since Moore's law states that "the number of components per integrated circuit roughly doubles every year." Thus, personal computer systems will acquire the characteristics of their larger, more expensive predecessors. In other words, within three to five years we can expect personal computers with the characteristics of large IBM 370s. The likelihood is that by the mid-1980s we will see a single package device containing processor, floating-point arithmetic, main memory and read-only memory with the complete operating system and a compiler or interpreter.
- 3. The man-machine interface will improve to accommodate the many users who

have little or no knowledge of computers. I therefore look for voice input/output to become commonplace by the end of the decade. Although voice input may be limited to short commands, output should be of a high quality with a large vocabulary.

- 4. Cheap mass storage will finally arrive via video cassette and optical disk memories. We will be able to store 100,000 pages of printed text on a single optical (video) disk...expect to see the Encyclopaedia Britannica on a single optical disk, with sophisticated cross-referencing software. Furthermore, expect optical disks that may be used with personal computers to provide high-quality video images for games, educational use, etc.
- 5. Higher-quality displays using either liquid crystal or semiconductor technology will replace CRTs (cathoderay tubes).
- 6. Personal computers will include self-testing capabilities and redundant circuits to improve reliability.
- 7. Expect BASIC to continue as the dominant language. Assembler and Pascal will still be the most popular languages for systems-level programming, and C will increase in popularity. Natural programming languages and automatic programming still appear to be many years away. The number of menudriven systems for the naive user will increase.
- 8. Operating systems such as UNIX, CP/M, MP/M and more sophisticated systems will increase in popularity, and many manufacturers will design special hardware to support these operating systems.

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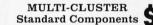
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Product Review

The Newest Sargon—2.5

John Martellaro 2929 Los Amigos Ct Apt B Las Cruces NM 88001

Time travel is common now. You've decided to spend the afternoon in Vienna on a sunny spring day in 1770. There is talk that at the Royal Palace the Baron Wolfgang von Kempelen, counselor to the Royal Chamber, will be giving a demonstration of his amazing Automaton Chess Player. You wander over towards the Palace.

The murmur of the crowd grows as the Baron rolls a large wooden cabinet into the courtyard, the result of a solemn promise he made to the Queen 6 months ago to build a chess-playing machine. The Baron smiles graciously and invites anyone to come forward from the

crowd to play the Automaton.

Meanwhile, the noblemen are about ready to accuse the Baron of a hoax. A machine that thinks? Rubbish. Sacrilege. And the spectators are no more convinced. Catcalls from the crowd dare the Baron to open the cabinet—obviously big enough to hold a small man—whereupon von Kempelen opens all the doors only to reveal a complex system of pulleys, gears, and levers, nothing else.

About this time, you decide to come forth from the crowd to play this wondrous machine. Unknown to everyone, you have Sargon 6, no bigger than a matchbook, hidden in your palm. With its aid, you win, but the Automaton plays a superb game. Afterwards, a crowd gathers around you, and the Baron congratulates you on your game. Everyone agrees that the machine played a creditable game of chess, clearly outplayed by a genius. A priest overhearing this remarks that this is proof of the superiority of the human mind. You shrug, put Sargon 6 in your pocket, and wander off into the crowd.

The Baron will go on to amaze the bewildered crowds in Europe and America for many years, and the machine will defeat many chess players. It will take 70 years for the hidden compartment and the hoax to be revealed. But the dream of a chess-playing machine is planted firmly in the minds of men. A dream which would take another

200 years to come true.

Introduction

Sargon 6 isn't available yet, but Sargon 2.5 is. It is a game module and holder slightly larger than a hardback book, but the real guts are no larger than a pocket calculator. This is the MGS (Modular Game System) from Chafitz; as of this writing, it is the strongest chessplaying microcomputer you can buy.

You may already be familiar with the Sargon 1 and

Sargon 2.0 computer programs written by Dan and Kathe Spracklen. These are available on cassette or floppy disk (from Hayden Books) for the Apple II and TRS-80 computers. But now Chafitz is marketing Sargon 2.5 as a plug-in ROM (read-only memory) module that fits into the MGS. Presumably, when Sargon 3 and other versions are available, you can remove the old ROM and plug in the new one. Not only does this protect the firmware, but allows new games (such as checkers and backgammon) to be run on the same system.

The technical specifications of the MGS-Sargon 2.5 combination are many and impressive. The system is rather complete: a benefit of Chafitz's previous experience with its chess machine, Boris. A touchpad

keyboard allows the user to:

- force selection of best move
- use the machine in its hint mode
- set playing level (from 0 to 6)
- set up a given position
- show elapsed time (either player, cumulative, or time per move)
- withdraw a move or moves (up to three moves)

At a Glance_

Name

Chafitz Modular Game System with Sargon 2.5

Manufacturer

Chafitz Inc, 856 Rockville Pike, Rockville MD 20852, (301) 340-0200

Price

\$375

Processor 6502, 8-bit

System-clock frequency 2 MHz

Memory

2 K bytes of program-

mable memory (for internal use only)

Additional features

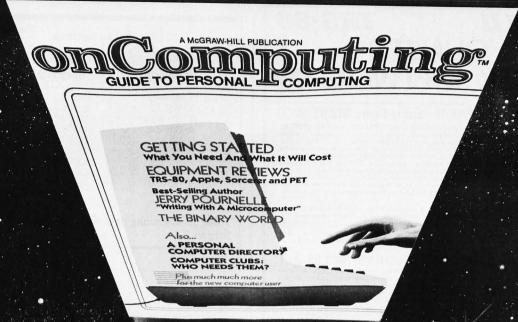
Includes AC adapter, keyboard, chessboard, magnetized chess pieces; Sargon 2.5 is a removable module that can be replaced by other game modules (not yet released)

Software

Sargon 2.5 program, held in 8 K bytes of ROM

Options

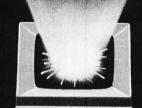
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Sicilian Defense

White	Black	
Martellaro	Sargon 2.5 (level 4)	
1. e2-e4 2. Ng1-f3 3. Bf1-b5 ch 4. Bb5-c4 5. Nb1-c3 6. d2-d4 7. Nf3xd4 8. Bc1-e3 9. Nd4-b5 10. Ra1-b1	c7-c5 d7-d6 Bc8-d7 Nb8-c6 Ng8-f6 c5xd4 Qd8-b6 Qb6xb2?? Ra8-c8 and Black loses his Queen	

Table 1: Beginning of a chess game between the author and Sargon 2.5.

The system is very nicely packaged. The quality of the plastic case and the display is outstanding. In the instruction manual there is a brief rule description of chess and information on the USCF (United States Chess Federation). This is an important and welcome addition. Overall, the instructions are clear and easy to understand. For once, we have complete documentation.

A conversation with Kathe Spracklen revealed that the decision algorithms of Sargon 2.5 are exactly the same as those of Sargon 2.0. The only modification is that the host 6502 microprocessor runs at 2.0 MHz as opposed to the Apple's effective 1.0 MHz, and Sargon 2.5 thinks on its opponent's time. The result of this is that Sargon 2.5 is often ready with a move as soon as the opponent enters his move. The program uses 8 K bytes of ROM and 2 K bytes of programmable memory.

Playing Strength

When chess programs were first written for microcomputers (Microchess 1.0 on the KIM and Sol), we all laughed and proceeded to demolish them. While we had respect for the programs on big computers, microcomputer chess programs had a poor reputation. Times have changed, and now the average player can no longer bully microcomputer-based chess programs. That is not to say that Sargon can't be beaten by a good player. (Some results are given here; see tables 1 and 2.) But now a player must use care and caution, and a single slip can mean disaster.

Sargon 2.5 in experimental form obtained a USCF rating of 1641 in a rated human tournament (the 1979 Paul Masson Championship). This is not bad at all for a machine that plays under tournament time controls and can be held in the palm of your hand. Reportedly, the Spracklens are working on major improvements that will boost its rating (Sargon 3) to 1800 in tournament time. Sargon 2.5 is probably the last microcomputer program that we amateur players will be able to consistently beat.

Playing Results

In a match of five games between Sargon 2.5 and Sargon 2.0 (which runs on my Apple II), the programs split—two wins, two losses each, and a declared draw. Sargon 2.5 started out slowly indeed. I didn't mind too much when I (rated about 1700) and a friend (rated 1850)

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Grunfeld-Ind	ian Defense	18. g2xf3 19. O-O	Qe4-f5
White	Black	Taunting Sargon 2.0 to d	o any harm
Sargon 2.5 (level 4)	Sargon 2.0 (level 3)	19	b7-b5
1. d2-d4 2. c2-c4 3. Nb1-c3 4. c4xd5 5. Nc3xd5	Ng8-f6 g7-g6 d7-d5 Nf6xd5 Qd8xd5	20. Bc4-b3 21. Bc7-g3 22. Rc3-c5 23. Bb3-d5 24. Qd1-c2	Qf5-g5 ch Qg5-f6 a7-a6 Ra8-d8
6. Ng1-f3	Bf8-g7	Threatening, of course, 25. Rc5-c8.	
7. Bc1-f4 This gets the Bishop deve	eloped in preparation for 8. e2-e3.	24 25. Qc2xf5	Qf6-f5 g6xf5
8. e2-e3 9. Qd1-d2	Nb8-c6 Qd5-a5 ch Nc6-b4!	26. Bd5-b7 27. Bb7xa6 28. Rc5-c4	e7-e6 b5-b4 Rd8-a8?
Not a bad move for a \$3	0 program. But it will be fruitless.	Black was in serious trou following clincher.	ble, but there was no reason to allow the
10. Ra1-c1 11. Rc1-c5 12. Bf4xc7	Bc8-f5 Qa5-b6 Nb4-c2 ch	29. Bg3-d6 ch 30. Ba6-b5 ch	Kf8-e8
Sargon 2.0 has been wanting to do this badly. Now, however, it is		The mating web starts	
in vain. 13. Rc5xc2 14. Bf1-b5 ch	Qb6-e6 Ke8-f8	30 31. Rc4-c7 32. Rc7-b7	Ke8-d8 Ra8-a5
15. Bb5-c4	Qe6-e4	Threat: Rb7-b8 mate.	
16. Rc2-c3 17. Qd2-d1	Bf5-g4	32 33. Rb7-d7 ch	Ra5-a8 Kd8-e8
Sargon 2.5 is finding all the right defensive moves and is a pawn and Knight to the good.		34. Rd7-a7 ch 35. Ra7xa8 mate	Ke8-d8
17	Bg4xf3		

Table 2: Record of a complete chess game between Sargon 2.5 (running on the Chafitz Modular Game System) and Sargon 2.0 (running on an Apple II computer).

Technical Notes on Sargon 2.5 and the Chafitz Modular Game System

The MGS is a plastic case with a slide-out tray. The top of the chessboard is brown and white soft grain with algebraic-notation markings. In the tray is the receptacle for the plug-in ROM, a keyboard (supplied with a chess overlay), and a compartment with chessmen-standard Staunton chess pieces, magnetized, with a 21/4-inch King. There is an AC (alternating current) adapter supplied. An optional battery pack is available for \$39.95; on battery power, the unit can retain an adjourned position for about 24 hours. The total system price is \$375.

Sargon 2.5 plays at six levels. Level 4 gives a reply in 2 to 4 minutes, plays in tournament time, and is rated 1641. If you want to wait 20 to 40 minutes per move at level 5, the claimed rating is 1800.

took three games from Sargon 2.5. But when Sargon 2.0 won its first two games, apprehension mounted. We wondered if there was a faulty ROM in Sargon 2.5, but we decided it was unlikely. Later, Sargon 2.5 came back to win two straight games against Sargon 2.0 and redeem itself (see match results, table 3).

The circumstances of the first two losses to Sargon 2.0 are peculiar. In the first game, everything was even down to pawns and King against pawns and King. But Sargon 2.0 gained a tempo (an advantage in time) and promoted a pawn to Queen before Sargon 2.5 could. In the second game, Sargon 2.5 played very speculatively on the attack and lost a Bishop for a pawn, then later another pawn. A whole Bishop down going into the end game with no

Opponent of Sargon 2.5	USCF Rating	Results
Martellaro	(1700+)	2 wins, 1 loss
J. Irwin	(1850)	1 win
Sargon 2.0	(1600?)	2 wins, 2 losses, 1 draw

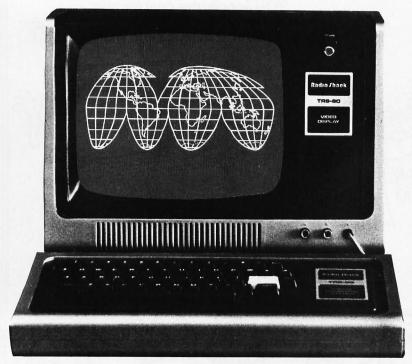
Table 3: An informal list of match results between Sargon 2.5 and other opponents.

compensation whatsoever caused me to declare a win for Sargon 2.0.

This is hard to quantify or justify, but it appears that Sargon 2.5 with its greater look-ahead capability plays more (what I would call) speculatively. Sargon 2.5 will play solid defense and sacrifice soundly, but it also appears to play a little more aggressively and loosely than Sargon 2.0. Sargon 2.0 is very solid and conservative and never risks too much. Because of this, Sargon 2.5 can get into trouble on the offensive.

It is also peculiar that in the games Sargon 2.5 won, it was on the defensive with White. (See the game score in table 2.) Sargon 2.0 huffed and puffed on the attack with Black for twenty moves, flailing away. When Sargon 2.5 was done fending off the attack, it was a Bishop and two pawns up and proceeded to mate. Astonishing.

The difference in strength between Sargon 2.5 and Sargon 2.0 seems small yet definite. My personal subjective experience is that Sargon 2.5 is more resilient on the defense, and I would prefer to play Sargon 2.0 as the weaker opponent. However, if you are running Sargon 2.0 on your microcomputer, the \$300-plus investment for the "improved" version is hardly worth it. Wait for Sargon 3.■



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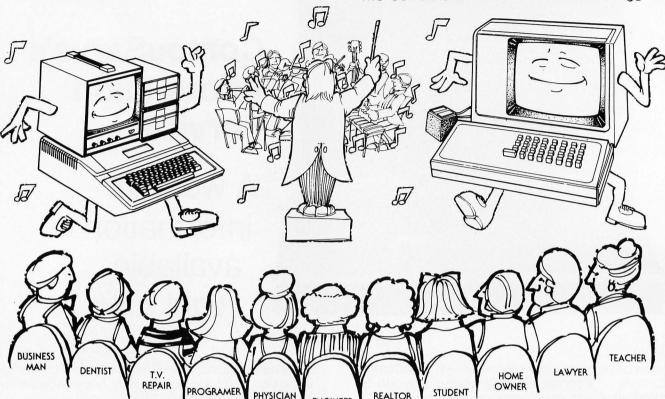
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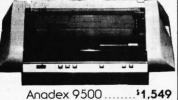
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Product Review

The SwTPC 6809 Microcomputer System

Tom Harmon, 1505 Magnolia Dr, Salisbury MD 21801

The SwTPC 6809 microcomputer system can be purchased in kit form (as the 69/K) for \$495 or assembled and tested (the 69/A) for \$595. Since I wanted to add sockets for all the integrated circuits, I chose the kit. (The assembled version doesn't use sockets.)

The 69/K and 69/A systems both include the MP-09 processor board, one MP-8M 8 K-byte programmable memory board, the MP-S2 RS-232C serial-interface card, and the MP-B3 motherboard with eight 50-pin slots and eight 30-pin slots. The case and power supply are also included.

The Processor Board

The MP-09 uses the Motorola 6809 microprocessor with a 1 MHz clock. The 6809 is the third-generation ad-

At a Glance_

Name

69/K (kit) or 69/A (assembled) computer

Use

6809-based personal computer

Manufacturer

Southwest Technical Products Corp, 219 W Rhapsody, San Antonio TX 78216 (512) 344-0241

Dimensions

length: 44 cm (17 inches) width: 39 cm (15 inches) height: 18 cm (7 inches)

Price

\$495 (for 69/K), \$595 (for 69/A)

Features

processor board containing 6809 microprocessor running at 1 MHz, RS-232C serial-interface card, 8 K bytes of programmable memory, fan

Hardware

RS-232C terminal (for input and output)

Software

SBUG-E monitor in ROM (included)

Hardware Options

extra memory boards, expansion kit for serial interface, MF-69 5-inch floppy-disk system (includes FLEX operating system)

Software Options

FLEX disk operating system, other software products from TSC (see text) that are supported by SwTPC

Documentation

looseleaf pages, 22 by 28 cm (8½ by 11 inches), in binder, with separate sections on kit construction (if applicable), schematics, parts layout, operation

dition to the 8-bit 6800 family. It includes two 16-bit index registers, two 16-bit stack pointers, two 8-bit accumulators which can be treated as a single 16-bit register for some operations, and a direct-page register for direct-memory addressing. The 6809 includes all addressing modes of the 6800 with the addition of program-counter relative, extended indirect, indexed indirect, and program-relative indirect. Assembly language written with program-counter relative mode can be moved anywhere in memory without reassembly.

The 6809 is not object-code compatible with the 6800. Although 6800 source code can be reassembled with minor changes, the code should be rewritten to take full

advantage of 6809 capabilities.

Sockets are provided on the board for three additional 2716 EPROMs (erasable programmable read-only memory devices). However, the documentation says the physical addresses of these may conflict with interface addresses and recommends they be switched off.

Included on the processor board is an integrated circuit that creates clock signals for various data-transfer rates. Because of the shortage of pins on the SS-50C bus, some of the clock signals share common bus lines and are

jumper-selected.

A DAT (dynamic address translator) allows physical memory to be assigned as logical memory in any desired order. Because of this, you don't have to strap memory boards into consecutive memory locations. The principal use for the DAT will be for multiuser/multitasking software, which is still being developed.

A welcome feature is that the memory addresses used for input and output have been moved to a higher location to allow the 6809 to support 56 K bytes of programmable memory instead of the 32 K bytes supported on

older SwTPC 6800 systems.

The MP-09 processor board is silk-screen masked and is of much higher quality than the memory board supplied with the kit. The MP-09 board is intended for use with the SS-50C bus and cannot be used with the older SS-50 bus unless modifications are made to the mother-board.

The SBUG-E Monitor

A 2 K-byte monitor (SBUG-E) is supplied in a ROM (read-only memory) that is pin compatible with a 2716 EPROM. The monitor contains disk bootstrap routines for both 5-inch and 8-inch floppy disks. A new DC-3 double-head single-density disk controller that is com-

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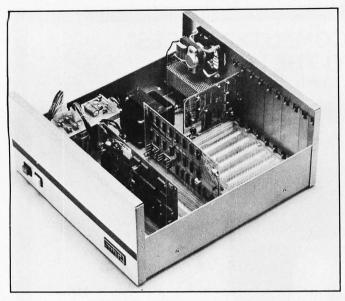


Photo 1: The SwTPC 6809 microcomputer system. The factory-assembled 69/A sells for \$595 and includes the three boards shown here. Front to back are the MP-09 processor board, the MP-8M memory board, and the MP-S2 RS-232C serial-interface board. The kit version 69/K is \$495.

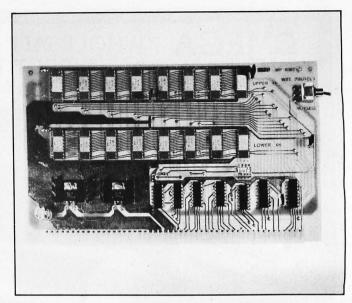


Photo 2: The MP-8M programmable memory board for the SwTPC 6809 microcomputer system. Both the kit and assembled versions of the computer are shipped with one of these 8 K-byte boards. This board is addressable to any 8 K-byte boundry within the first 32 K bytes of memory.

patible with the SS-50C bus is available from SwTPC for \$150. The older MF-68 disk controller cannot be used with the SS-50C bus without modification. It has been rumored that SwTPC may soon discontinue the MF-68 floppy-disk drive and replace it with a DT-5 unit, which uses the Siemens double-head drive.

The SBUG-E monitor also includes a memory diagnostic. It allows you to set and release breakpoints, examine and alter memory, and examine and alter 6809 registers. Unfortunately, SwTPC does not provide source listings of SBUG-E. However, a disassembled source listing has been published in 68 Micro Journal (June 1980).

Serial Interface

The MP-S2 serial-interface card is supplied set up for one serial port. It can be expanded to two ports by ordering the MP-SX expansion kit, which sells for \$25. The card must be installed in bus-row 0, driving the system console with a standard RS-232C port. A nice feature is

that you don't need extra cables or connectors since the DB-25 connector is mounted directly on the card.

Other Features

The MP-B3 motherboard uses the new SS-50C bus. Since I/O cards have decoding performed for sixteen addresses, the new cards are not downwards compatible with the SS-50 bus.

The power supply provides unregulated outputs of ± 16 VDC and + 8 VDC. Older SS-50 cards that obtained 12 VDC from the bus will now require on-board regulators.

The 6809 cabinet is constructed of heavy anodized aluminum and is a major improvement on the older SwTPC systems. I had no trouble getting the bolt holes to align perfectly.

The quality of the parts supplied with the 69/K kit is excellent. I did find several small components missing from the kit but had no trouble getting replacement parts from SwTPC.



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HEARTS 1.5 (Available for all computers)

An exciting and entertaining computer version of this popular card game. Hearts is a trick-oriented game in which the purpose is not to take any hearts or the queen of spades. Play against two computer opponents who are armed with hard-to-beat playing strategies.

CRIBBAGE 2.0 (TRS-80 only)

This is a well-designed and nicely executed two-handed version of the classic card game, cribbage. It is an excellent program for the cribbage player in search of a worthy opponent as well as the beginner wishing to learn the game, in particular the scoring and jargon. The standard cribbage score board is continually shown at the top of the display to (utilizing the TRS-80's graphics capabilities), with the cards shown underneath. The computer automatically scores and also announces the points using the tradi-

CHESS MASTER (North Star and TRS-80 only)

This complete and very powerful program provides five levels of play. It includes castling, en passant captures and the promotion of pawns. Additionally, the board may be preset before the start of play, permitting the examination of "book" plays. To maximize execution speed, the program is written in assembly language (by SOFTWARE SPECIALISTS of California). Pull graphics are employed in the TRS-80 version, and two widths of alphanumeric display are provided to accommodate North Star

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This is the classic gambler's card game. The computer deals the cards one at a time and you (and the computer) bet on what you see. The computer does not cheat and usually bets the odds. However, it sometimes bluffs! Also included is a five card draw poker betting practice program. This package will run on a 16K ATARI.

STATISTICS and ENGINEERING

DATA SMOOTHER (Not available for ATARI)

Price: \$14.95 Cassette \$18.95 Diskette

This special data smoothing program may be used to rapidly derive useful information from noisy business and engineering data which are equally spaced. The software features choice in degree and range of fit, as well as smoothed first and second derivative calculation. Also included is automatic plotting of the input data and smoothed results.

FOURIER ANALYZER (Available for all computers)

Use this program to examine the frequency spectra of limited duration signals. The program features automatic scaling and plotting of the input data and results. Practical applications include the analysis of complicated patterns in such fields as electronics, communications and business.

TFA (Transfer Function Analyzer)

Price: \$19.95 Cassette
\$23.95 Diskette
This is a special software package which may be used to evaluate the transfer functions of systems such
as hi-fi amplifiers and filters by examining their response to pulsed inputs. TFA is a major modification
of FOURIER ANALYZER and contains an engineering-oriented decibel versus log-frequency plot as
well as data editing features. Whereas FOURIER ANALYZER is designed for educational and scientific use, TFA is an engineering tool. Available for all computers.
FOURIER ANALYZER and TFA may be purchased

FOURIER ANALYZER and TFA may be purchased together for a combined price of \$29.95 (Cassettes) and \$37.95 (Diskettes).

REGRESSION I (Available for all computers)

EGRESSION I (Available for all computers)

Price: \$19.95 Cassette
\$23.95 Diskette
REGRESSION I is a unique and exceptionally versatile one-dimensional least squares "polynomial"
curve fitting program. Features include very high accuracy; an automatic degree determination option;
an extensive internal library of fitting functions; data editing; automatic data and curve plotting; a
statistical analysis (e.g., standard deviation, correlation coefficient, etc.) and much more. In addition,
new fits may be tried without reentering the data. REGRESSION I is certainly the cornerstone program
in any data analysis software library.

REGRESSION II (PARAFIT) (Available for all computers) Price: \$19.95 Cassette \$23.95 Diskette
PARAFIT is designed to handle those cases in which the parameters are imbedded (possibly nonlinearly) in the fitting function. The user simply inserts the functional form, including the parameters (A(1),
A(2), etc.) as one or more BASIC statement lines. Data and results may be manipulated and plotted as
with REGRESSION 1. Use REGRESSION I for polynomial fitting, and PARAFIT for those complicated functions.

REGRESSION I and II may be purchased together for \$36.95 (cassettes) and \$44.95 (diskettes)

Availability

DYNACOMP software is supplied with complete documentation containing clear explanations and examples. All programs will run within 16K program memory space (ATARI requires 24K). Except where noted, programs are available on ATARI, PET, TRS-80 (Level II) and Apple (Applesoft) cassette and diskette as well as North Star single density (double density compatible) diskette. Additionally, most programs can be obtained on standard 8" CP/M floppy disks for systems running under MBASIC.

BUSINESS and UTILITIES

MAIL LIST II (North Star only)

This many-featured program now includes full alphabetic and zip code sorting as well as file merging.

Entries can be retrieved by user-defined code, client name or Zip Code. The printout format allows the use of standard size address labels. Each diskette can store more than 1100 entries (single density) or the contract of the cont 2200 with double density systems)!

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Price: \$14.95 Cassette \$18.95 Diskette

An easy to use, line-oriented text editor which provides variable line widths and simple paragraph in-dexing. This text editor is ideally suited for composing letters and is quite capable of handling much larger jobs. Available for all computers.

PERSONAL FINANCE SYSTEM (ATARI only)

Price: \$34.95 Diskette

PFS is a single disk menu oriented system composed of 10 programs designed to organize and simplify your personal finances. Features include a 300 transaction capacity; fast access; 26 optional user codes; data retrieval by month, code or payee; optional printing of reports; checkbook balancing; bar graph plotting and more. Also provided on the diskette is ATARI DOS 2.

FINDIT (North Star only)

NDIT (North Star only)

Price: \$19.95
This is a three-in-one program which maintains information accessible by keywords of three types: Personal (e.g., last name), Commercial (eg: plumbers) and Reference (eg: magazine articles, record albums, etc). In addition to keyword searches, there are birthday, anniversary and appointment searches for the personal records and appointment searches for the personal records and appointment searches for the commercial records. Reference records are accessed by a single keyword or by cross-referencing two or three keywords.

DFILE (North Star only)

This handy program allows North Star users to maintain a specialized data base of all files and programs in the stack of disks which invariably accumulates. DFILE is easy to set up and use. It will organize your disks to provide efficient locating of the desired file or program.

COMPARE (North Star only)

COMPARE is a single disk utility software package which compares two BASIC programs and displays the file sizes of the programs in bytes, the lengths in terms of the number of statement lines, and the line numbers at which various listed differences occur. COMPARE permits the user to examine versions of his software to verify which are the more current, and to clearly identify the changes made during development.

COMPRESS (North Star only)

COMPRESS is a single-disk utility program which removes all unnecessary spaces and (optionally) REMark statements from North Star BASIC programs. The source file is processed one line at a time, thus permitting very large programs to be compressed using only a small amount of computer memory. File compressions of 20-50% are commonly achieved.

GRAFIX (TRS-80 only)

Price: \$12.95 Cassette \$16.95 Diskette. This unique program allows you to easily create graphics directly from the keyboard, You "draw" your figure using the program's extensive cursor controls. Once the figure is made, it is automatically appended to your BASIC program as a string variable. Draw a "happy face", call it #8 and then print it from your program using PRINT HSI This is a very easy way to create and save graphics.

TIDY (TRS-80 only)

DY (TRS-80 only)

Price: \$10.95 Cassette \$14.95 Diskette
TIDY is an assembly language program which allows you to renumber the lines in your BASIC programs. TIDY also removes unnecessary spaces and REMark statements. The result is a compacted BASIC program which uses much less memory space and executes significantly faster. Once loaded, TIDY remains in memory; you may load any number of BASIC programs without having to reload TIDY!

SIMULATIONS and EDUCATION

BLACK HOLE (Apple only)

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This is an exciting graphical simulation of the problems involved in closely observing a black hole with a space probe. The object is to enter and maintain, for a prescribed time, an orbit close to a small black hole. This is to be achieved without coming so near the anomaly that the tidal stress destroys the probe. Control of the craft is realistically simulated using side jets for rotation and main thrusters for acceleration. This program employs Hi-Res graphics and is educational as well as challenging.

VALDEZ (Available for all computers)

A simulation of supertanker navigation in the Prince William Sound and Valdez Narrows. The program uses an extensive 256X256 element radar map and employs physical models of ship response and tidal patterns. Chart your own course through ship and iceberg traffic. Any standard terminal may be

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TEACHER'S PET I (Available for all computers)

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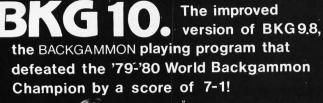
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- Version II adds printer/disk output of game in progress, board initialization at non standard positions, and simulation capabilities.
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- Complete game rules including: doubling, match play and Crawford Rule. Will play man-machine, either side or machine-machine with Version II.
- BKG 10. makes most moves in under 20 seconds (2MHZ) or 10 seconds (4 MHZ).
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SYSTEM REQUIREMENTS •

Z-80 Processor, 40k CP/M for BKG 10 Vers I, 48k for BKG 10 Vers II and GOMOKU, cursor addressable video terminal (specify terminal model, most makes supported), 8" or 5\\daggeq" floppy drive. Formats available for TRS-80 Model II, Northstar, Cromemco, others.

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The documentation supplied with the 69/K system is adequate, but the construction manuals are not as detailed as those of some other manufacturers. For example, you are told to install all resistors as a single step in construction, and you are expected to know the resistor color codes and be able to identify the polarity of all polarized capacitors. I would not recommend this kit for a beginning kit builder. However, an experienced builder should have no trouble.

Construction Hints

I selected low-profile tin soldier-tail sockets manufactured by Texas Instruments for use on the printed-circuit boards. These sockets may be purchased from a number of sources, including Digi-Key Corporation, POB 677, Highway 32 S, Thief River Falls MN 56701.

The straight pin-edge connectors on the motherboard seem to slope in one direction and the 10-pin male connectors should be installed with the slope in the same direction. This avoids problems when the printed-circuit boards are inserted later. You might also find it easier to remove the socket index pin before soldering the sockets to the board.

The Added Extras

In order to communicate with your microcomputer system, you'll need an RS-232C-compatible terminal. I selected the Heath H-19 video terminal over the SwTPC CT-82 because I prefer the larger 12-inch display size of the Heath. (The SwTPC CT-82 has a 9-inch display.) The normal format of the Heath H-19 is 24 lines by 80 characters, while the CT-82 format is 16 lines by 82 characters.

You'll probably want additional memory because only 4 K bytes of the supplied 8 K bytes of programmable memory are available for use. The SBUG-E monitor assigns a 4 K-byte area for a system stack and for internal tables and addresses. SwTPC sells additional MP-8Mb bare boards with edge connectors for \$17. By buying your own integrated circuits and memory from independent suppliers, you can save a considerable amount of money over assembled units.

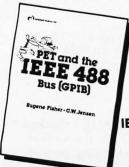
Digital Research Computers (POB 401565, Garland TX 75040) sells a 16 K-byte programmable memory board for the SS-50 bus (\$26). The board uses type-2114 integrated circuits instead of the type-4044 programmable memory devices used by the MP-8M board. The quality is excellent and well worth adding to your 6809 system.

Of course you'll also need either a cassette-tape unit (like the SwTPC AC-30) or a floppy-disk system for loading and saving programs.

Software

The FLEX 09 version 2.6 disk operating system is available from SwTPC. The price (\$35) includes a manual and object-code disk. FLEX 09 can be used with most of the 6809 software available from TSC (Technical Systems Consultants, POB 2574, West Lafayette IN 47906). TSC has a large amount of 6809 software, including a text editor, an assembler, several versions of BASIC, a debugging package, and others.

CSI (Control Systems Inc, 1317 Central, Kansas City KS 66102) has the UCSD Pascal compiler for \$419 that

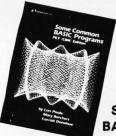


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This book provides the important information and documentation that PET/CBM users have sought for so long. After reading this book you will have

a good understanding of what a computer -

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will run on a 6809 system with 56 K bytes of programmable memory. The software is available on both 5-inch and 8-inch floppy disks, and includes operating system, compiler and linker.

System Checkout

The power-supply cables and voltages are first checked without any other boards installed. Then the mother-board is installed, and finally the remaining printed-circuit boards. You will need an RS-232C-compatible terminal connected to the serial-interface card to test for the proper message, "S-BUG 1.5 - 8 K", followed a blinking cursor.

When I performed the checkout, everything appeared to be normal until I attached a terminal and noticed that the video display consisted of question marks being produced much faster than the current data-transfer rate, which was 300 bps (bits per second). The SwTPC documentation states that if anything is printed, especially question marks, the computer is probably working and that the problem is probably with the terminal parity, bit format, or data-transfer-rate setting.

I spent a considerable amount of time checking for problems and couldn't find anything wrong until I used my ohmmeter and observed that the resistance between the 300 and 4800 bps lines on the motherboard measured about 2 ohms. I immediately suspected a solder bridge but was unable to find one I then called in a friend with a very accurate ohmmeter. He detected a dip in the resistance at the closest pin on the motherboard. Using a projector lens, he found two extremely small copper bridges that were covered by the green coating on the motherboard and were virtually impossible to see with the naked eye. After I removed the copper bridges with a small knife, the system worked beautifully.

The moral of this story is that you should be careful to check adjacent bus lines on the motherboard both initially and after assembly. Doing this will eliminate a lot of frustration and wasted time.

Conclusions

I'm pleased with the overall quality of the SwTPC 69/K, and I recommend it to any experienced kit builder. One big headache-saver is to check out individual finished boards on a working SS-50 or SS-50C system. I used a friend's SS-50 computer to test the 8 K-byte programmable memory board supplied with the kit.

If you don't have a means of testing individual boards, I strongly suggest the purchase of the 69/A assembled and tested system. When you consider the amount of time spent assembling and testing the unit, the extra \$100 seems like a bargain.

SwTPC does have technical services available, but the entire computer must be repacked and sent to San Antonio, Texas. Without the proper test equipment, it is difficult, if not impossible, to track down specific problems.

If you purchase factory-assembled boards, SwTPC does offer a factory exchange program. Boards can be exchanged for a fixed fee (\$40 for the MP-09 processor board). All factory-assembled products are included in the plan for 6 months, and SwTPC will arrange a service contract after the 6-month period. If you're using your computer for business, this service is ideal.



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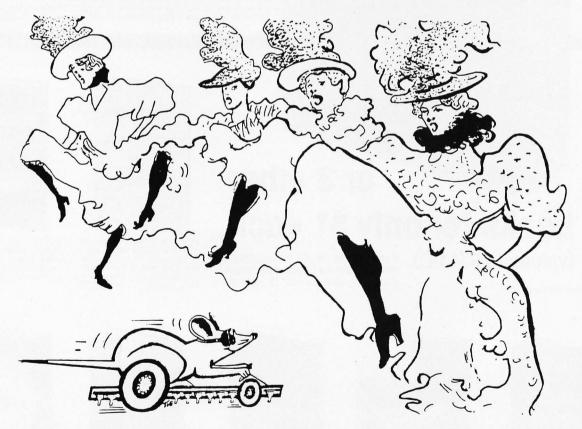
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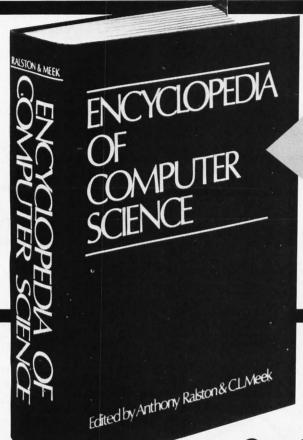
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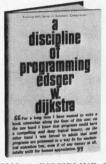
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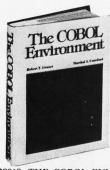
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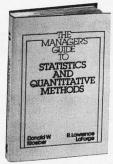
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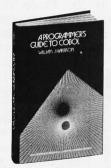
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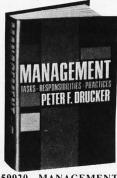
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The Picture-Perfect Apple

Phil Roybal 1111 Pippin Creek Ct San Jose CA 95120

A picture is worth a thousand words. And it was the capability of representing information in pictures that initially attracted me to the Apple II computer.

But images on a screen can be too personal an experience. Often no one else sees them. It would be great if there were a way to transcribe these images so that others could also appreciate them. There is a way to do it, and this article tells how.

The program discussed here was written in Apple (6502) assembly language for the Qume Sprint Micro 3, a daisy-wheel printer with a 16-bit parallel interface. The approach is quite general in nature; therefore, you will find it easy to adapt it to other hardware.

The high-resolution screen of the Apple II is actually a window into the memory between decimal addresses 8192 and 16,383. Anything you see there can be printed on paper. This means that if you have a graphics printer, you needn't go to a lot of trouble writing plotting routines for it. Those already available in the Apple languages and utility programs will suffice quite handily.

This capability can be put to good use the next time you need to produce a high-quality chart for a presentation, or an attention-getting cover for a report. You can do the job on the same letter-quality printer you used

to produce the report itself.

Even if you don't have one of these elegant but expensive printers, this routine is still useful. Very little depends upon either the printer or the interface. In fact, the bulk of the routine is concerned with decoding the high-resolution screen addresses. Therefore, you can quickly tailor the printer routine to your hardware.

The High-Resolution Graphics Screen

The Apple graphics screen is a tricky beast. If you calculate how much memory it should consume, it comes out:

280 dots \times 192 lines = 53,760 pixels

Then consider that there are eight colors that can be displayed. This means you throw in 3 bits per pixel to wind up with:

> $53,760 \times 3 = 20,160$ bytes of memory

Despite this, the screen takes up only 8192 bytes. How is this done?

The screen doesn't show every color in every location. Only blackand-white images take advantage of the full resolution of the screen. Colors show up in alternate columns (green alternates with violet, orange with blue, etc). Apple's video circuitry and the television set's response characteristics combine to make the rows of colored dots appear to fuse together. Thus, you can draw a "solid" horizontal line across the screen, regardless of the color you plot it in.

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While this bit of trickery does save memory, it makes analyzing screen images rather complex since you have to figure out what the color is at any given location. Fortunately, since most printers produce only black and white, the color issue is academic. If a dot is there, the printer prints it. The end result is that colors appear as less dense clusters of dots than solid white, providing a shading effect to images produced on the printer.

What causes the most difficulty is that the designer of the Apple saved himself a logic gate or two through the use of rather unorthodox screen addressing. As a result, adjacent screen rows do not occupy consecutive memory locations. It is the decoding of this high-resolution screen addressing which accounts for a good deal of the complexity of this program. The software has to use a series of counters to keep track of where it is on the screen. (Figure 2 shows how it works.)

The high-resolution screen of the Apple II is actually a window into the memory.

High-resolution screen addressing is easy to understand if it is considered as a series of hexadecimal rather than decimal numbers.

As shown in figure 1, the screen is divided into three major sets of horizontal lines which I call triads. Each triad is divided into eight groups of horizontal lines called octets. And finally, each octet consists of eight horizontal lines called fillers. A line consists of 280 dots, which are derived from 40 bytes of memory by using the lower 7 bits of each byte. This is how it works.

The triads begin with lines whose first bytes (leftmost characters) have hexadecimal addresses:

> 2000 2028 2050

If you poke 1s into these addresses

while the high-resolution screen is black, dots will appear along the left margin, evenly dividing the screen vertically into thirds.

Within a triad are octets. The octets begin with lines whose first bytes are incremented by hexadecimal 80 from the starting address of the triad. For example, the first triad, which starts at hexadecimal 2000, has octets beginning with lines whose first bytes have hexadecimal

addresses:

2000	
2080	
2100	
2180	
2200	
2280	
2300	
2380	

Each octet has eight lines within it.

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These lines start with bytes whose addresses go up in increments of hexadecimal 400 from the octet starting address. Thus, the first octet of the first triad has eight lines in it that start with the hexadecimal addresses:

This is a bit complex. It helps if you work out a table and verify it by pok-

ing information into the high-resolution screen area. Adapting the program to handle a different printer is relatively trivial compared to understanding the address scheme. Thus, this algorithm is a good base to build on, no matter what hardware you

A Tour of the Driver

The driver routine (see figure 2) knows that the screen is contained in the memory area between hexadecimal 2000 and 3FFF. Therefore, it moves the print head to the left margin and then starts with hexadecimal address 2000, in the first

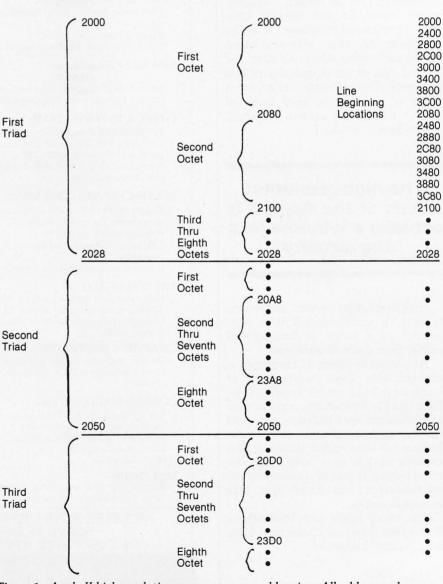
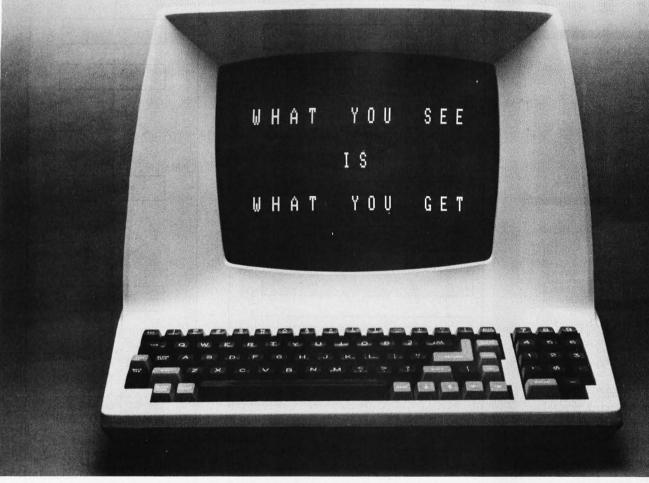


Figure 1: Apple II high-resolution screen-memory addressing. All addresses shown are in hexadecimal radix. The screen is divided into three major sets of horizontal lines called triads. Each triad is divided into eight groups of horizontal lines called octets. Each octet is divided into eight horizontal lines called fillers. Each line uses 40 bytes of programmable memory and consists of 280 dots.

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74LS30	.35	74LS139	.79	74LS244	1.79
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triad, first octet, and first filler line. Beginning at one end of the first line, it looks at the lower 7 bits of each byte until it has scanned (decimal) 40 bytes without finding a dot, or until it

has found a dot.

In the first case, the complete line is blank (all zeroes), so the driver issues a line feed. It then picks the next line (in this case, the second filler line in

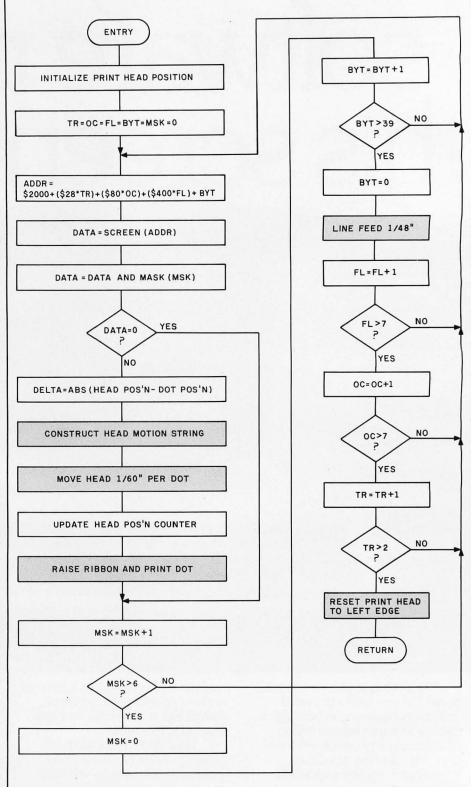


Figure 2: Flowchart for a program to drive the Qume Sprint Micro 3 plotter to print Apple II screen graphics. The shaded boxes indicate hardware-dependent code, although the code is very similar for all 16-bit parallel printers. Abbreviations are as follows: TR=triad counter; OC=octet counter; FL=filler counter; BYT=filler-line-byte counter; and MSK=seven-dot byte mask.





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the first octet) and again scans it from end to end. This pattern continues (if the whole screen is blank) through the eight filler lines of each octet, the eight octets of each triad, and all three triads, until the end of the screen is reached. Then the driver jumps back to the routine that called it.

When a nonzero bit (a dot on the screen) is found, the driver calculates the distance from the present print head location (normally over the last dot printed) to the new dot position. It then moves the print head into place in a single step (instead of ratcheting along over every dot position). When the print head is in place, the dot is printed.

In the driver written here, if at least one dot has been printed on a line, the next line will be scanned and printed from the opposite direction. This provides the fastest printing with minimum wear and noise under average conditions. While this scheme is not 100% optimized, it does yield very acceptable performance. The determination of scanand head-motion direction adds complexity to the algorithm without contributing to the basic capability, so this feature is omitted from the flowchart in the interests of clarity.

The bulk of this program is dedicated to screen-address decoding. The only section tightly woven about the hardware is the output routines. These come last in the source code to facilitate changing them without reassembling the entire driver. They assume that you are using a Qume printer receiving 16-bit parallel code in the format shown in figure 3. If you are using another printer and interface, just write code to send the correct control characters to your printer hardware.

Using the Plotter

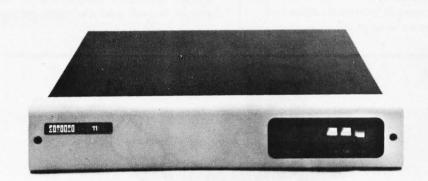
The driver was written for a printer that provides horizontal resolution of 120 steps per inch and vertical resolution of 48 steps per inch. Two horizontal increments are used for each screen dot, and one vertical increment is used for each line. As a result, the printer will reproduce the high-resolution graphics screen in a space about 11.3 by 9.8 cm (4.7 by 4 inches). This area will be centered on a 20.8 cm- (81/2-inch) wide page, and will start printing at wherever the paper is located at the time the driver is called.

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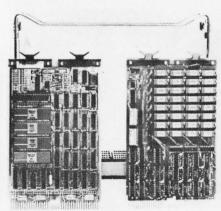
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resolution page 1, just turn on your printer and enter the routine with a CALL from BASIC or a G command from the monitor.

			OUTF	DT			
7	6	5	4	3	2	1	0
LOW B	YTE						
	ASCII MOTIO	CODE ON CO	S USE DES U	BITS SE AL	1-7 L 8 B	ITS-	
нівн	вуте						
				MOST	ON DIS SIGN BITS	IFICA	NT
		MOVE	-PRIN	DBES: T CHA T CAR			
			INF	PUT			
7	6	5	4	3	2	1	0
HIGH	BYTE						
PR	INTER	FLA REAL READ	OY —				
CA	RRIAG	E (P	OSITIC	N) RE	ADY-		
PR	INT H	EAD F	READY				

Getting a Copy of the Driver

A driver code is rather long for publication. In any case, typing it in is a masochistic form of entertainment. To alleviate these problems, I have made this code available on 5-inch floppy disk. The disk includes:

- object code assembled at hexadecimal location 9000 (for 48 K-byte systems), and hexadecimal location 5000 (for 32 K-byte systems)
- •source code in a text file

Also included is a version of this code adapted for use with Sprint 5 printers interfaced through Apple's Serial Interface Card.

To obtain your copy of this floppy disk, send a check for \$14.95 (California residents add 6% sales tax) plus \$1.00 shipping and handling to Contech, 1111 Pippin Creek Ct, San Jose CA 95120. Ask for the "Picture-Perfect Apple" software.■

Figure 3: The form in which the driver described in the text communicates with the Qume Sprint Micro 3 plotter. A strobe consists of a "I" bit in the appropriate position, with all other bits "0." If all strobes are raised simultaneously, the printer is reset and the print carriage moves to the left margin.



Figures 4a, 4b, and 4c: Three examples of Apple II high-resolution graphics transcribed by the Qume Sprint Micro 3 plotter, using the driver described in this article.

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Poking Data Into the High-Resolution Screen Area

Direct interaction with the Apple II high-resolution screen memory is an excellent way to test addressing schemes and explore the structure of Apple graphics images. To experiment on your own, get into the monitor mode (type CALL—155) and display the high-resolution screen by typing:

C050 C054 C057

and hit the Return key. You are looking at page 1 of the high-

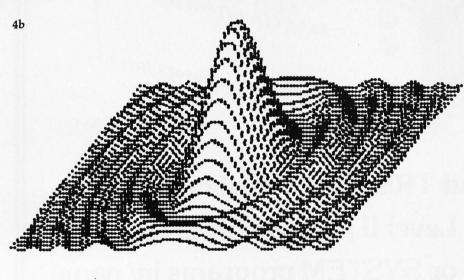
resolution screen. To clear it of garbage, fill it with 0s by typing:

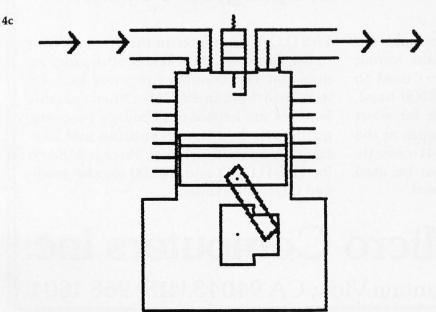
2000:0 2001 < 2000.3FFEM

followed by a return. Once you have a clean screen, type a hexadecimal address followed by a colon and FF. For example:

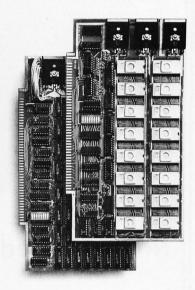
2000:FF

followed by a return. This will set the byte to all 1s and will produce a 7-dot-wide line segment at the appropriate place on the screen.





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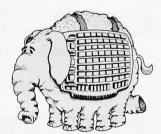
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BYTE January 1981 237

Micrograph

Part 3: Software and Operation

E Grady Booch 4314 Driftwood Dr Colorado Springs CO 80907

Some background on interactive computer-graphics systems was presented in Part 1. In Part 2, a description was given of the hardware for a low-cost color-graphics display processor, called Micrograph, which interfaces to a microcomputer as an intelligent peripheral device. In this, the third and final part, you will become familiar with the software for Micrograph, which implements the display-processor instruction set introduced in Part 1, and be given instructions for operating the system.

Software Perspective

Two packages of software are required to support Micrograph, as we have observed in the generalized graphics system in Part 1. The first package is the applications software, which executes in the host computer. This software creates and manipulates abstractions of images. The elements of these images are described to the display processor through the instructions in a display list. Within the display processor itself, there must reside a second software package that converts these instructions into a visible image.

In Part 1, we described one such instruction set for controlling a color raster-scan display processor, and it is summarized in table 1, here, in Part 3. Since emphasis has been on the display processor, and since the applications software is system specific, the remainder of this article will concentrate upon the other package: the software internal to the display processor. However, the protocol software in the host computer that is needed to carry out communication with Micrograph will be described.

Mnemonic	Name
CALL LCRAM LPIX LREG LSUB LSYM MOV RCRAM RET RPIX RREG RSUB RSYM SYM VEC WAIT	Call subroutine Load color memory Load pixel Load register Load subroutine Load symbol Move Read color memory Return Read pixel Read register Read subroutine Read symbol Display symbol Draw a vector Wait

Diagnostics are available under XERR.

Table 1: Summary of graphics primitives. These instructions control the graphics-display processor in Micrograph.

Software Description

The source software for Micrograph consists of approximately 2400 lines of Z80 assembly-language code plus internal comments. (See listing 2 in Part 1, BYTE, November 1980, page 280; listing 1 in Part 2, BYTE, December 1980, page 327; and listing 1, in this issue, page 240.) This code assembles to approximately 2.6 K bytes of object code and resides in the three system EPROMs (erasable programmable read-only memories) in the address space decimal 0 to 3071.

The Micrograph software was written on a Zilog Development System and conforms to the Zilog Z80 assembly-language standards. Structured programming and step-

wise refinement were used to develop the software. By virtue of these techniques, once I had cleared out the typos in the source, I required only four assemblies to complete the final working package.

Software Structure

Figure 1 (on page 264) indicates that, as a result of stepwise refinement, the Micrograph software is highly structured. The software consists of one main routine, three driving modules, seventeen routines that implement the instruction set, twelve shared utility routines, and five interrupt-service routines. These routines appear grouped together by their class, then alphabetically in the software source listing.

The routine MAIN drives the entire Micrograph software and handles a call to the power-up INIT (initialization). MAIN then enters an infinite loop of instruction fetches (via FETCH) and executes (via EXEC). In this sequence, Micrograph requests an instruction from the host computer and executes it. PRIMAT is then called by EXEC to calculate which instruction has been commanded and, in turn, calls the appropriate routine that processes the various options of the instruction.

These sixteen routines (CALLS through WAIT) correspond directly to the instruction set in table 1. Since the routines execute similar code, they may call any of several utility routines. These routines include null subroutine calls (GUSER and USER), routines for communicating with the host computer (GETBLK, SENDBK, and SENDBY), and some primitive

Text continued on page 260

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#5









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#6





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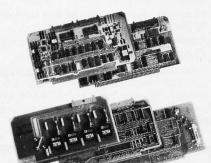
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Listing 1: The final third of the firmware for Micrograph control, written for the Z80 microprocessor used in the prototype. The first and second portions of the firmware appeared with Part 1 and Part 2 of this series.

07EA 07EB 07ED 07EE	E1 FDE1 F1 C9	1861 1862 1863 1864	POP HL POP IN POP AF	r	; F	ESTORE H AND L ESTORE IY EESTORE STACK
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		1874 ; 1875 ; CALLE 1876 ; 1877 ; 1878 ; 1879 ;	D BY	PUT RFIX LFIX		
		1880 ; REGIS 1881 ; 1882 ; 1883 ; 1884 ; 1885 ; 1886 ; 1888 ;	TERS	A B C D E H L IX IY	(TEMPORAR (CASE) (SUCCESS) (TEMPORAR (TEMPORAR (TEMPORAR (TEMPORAR (INDEX)	Y) Y) Y)
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		1893 ; 1894 ; 1895 ;	TURES	GDR1	(X) (Y) (VIEWPORT: (REFERENCE	
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		2017	; REGIS	TERS	NONE			
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		2021 2022	; STRUC	TURES	NONE			
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Listing 1 continued on page 244

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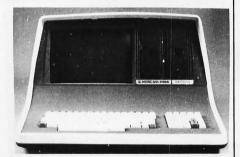


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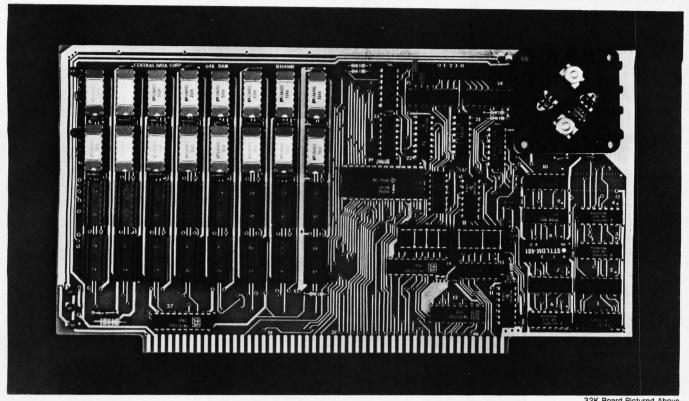
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Listing 1 continued:

Listing	1 continued:						
		2052 ; [2053 ; 6 2054 ; 6 2055 ; [2056 ; [2057 ; 6	DETERMOPERATE THE MAPPERATE OF THE MAPPE	TINES TION TO THE PIXE TO OUT TO THE PIXE THE PI	WHAT DISPLA IS READ OR W IL IS DETERM . PIXEL DEAL	Y DENSIT PRITE. TH NINED, TH S WITH T	ED. PIXEL FIRST Y IF USED, AND IF THE E PHYSICAL ADDRESS EN THE BIT ADDRESS IS HE PIXEL AT XY, THE XPECTS/RETURNS THE
			CALLS		NONE		
		2060 ; 2061 ; 2062 ;	CALLE	D BY	POKE PEEK		
		2063 ;	REGIST	TERS	Α	(COLOR,F	
		2065 ;			C D	(TEMPORA	
		2067 ;			E H	(POINTER	
		2069 ;			I.X	(POINTER)
		2071 ;			IY	(INDEX)	
		2073 ;	1/0		NONE		
		2075 ; 2076 ; 2077 ;	STRUC	TURES	GDRO GDR1 GDR14	(X) (Y) (DISPLAY	FORMAT)
		2078 ;			REFRESH	RAM	
08AC	FDE5 E5		XEL:	PUSH PUSH			SAVE IY
08AD 08AE	D5 F5	2082 2083		PUSH	DE		SAVE DE
08AF 08B2	DD7EDE E6EO	2084 2085		LD	A, (IX+GDR14 11100000B	4)	GET DISPLAY FORMAT
D884 U886	FE00 2800	2086 2087		CP JR	Ü Z,PIXELO		164 X 64 7 1JUMP IF SO
0888 088A	FEC0 CA4209	2088 2089		CF JF	11000000B Z,PIXEL6		;128 X 128 ? ;JUMP IF SO
08BD 08BF	FEE0 CAB709	2090 2091		OP JP	11100000B Z,PIXELC		;256 X 192 ? ;JUMP TF SO
08C2 08C5	C39C00 DD7E01	2092	XELO:	JF LD	XERR A, (IX+GDR1))	FERROR OTHERWISE
08C8 08C9	2F 67	2094 2095		CP1 LD	н, а		COMPLEMENT
0800	CB3C CB3C	2096 2097		SRL SRL	H		SHIFT SHIFT
08CE 08D1	DD6E00 CB3C	2098 2099		LD SRL	L, (IX+GDRO))	;LOAD X ;SHIFT
08D3 08D5	CB1D CB3C	2100 2101		RR SRL	L H		;SHIFT ;SHIFT
08D7 08D9	CB1D CB3C	2102 2103		RR SRL	L H		;SHIFT
0800	CB1D CB3C	2104 2105		RR SRL	L H		;SHIFT
08DF 08E1	CB1D 110020	2106 2107		RR LD	L. DE,RBOTTOM		;SHIFT ;LOAD BASE ADDRESS
08E4 08E5	19 DD7E00	2108 2109		ADD LD	HL, DE A, (IX+GDRO)	,	;ADD OFFSET ;GET X
08E8	E40C CB3F	2110 2111		AND SRL	00001100B A		;MASK ALL BUT 2 BITS ;SHIFT
08EE	CB3F CB41	2112 2113		SRL	A 0,C		;SHIFT ;CHECK READ FLAG
08F0 08F2	281E 4F	2114 2115		JR LD	Z,FIXEL3 C,A		JUMP IF NOT SET FLOAD C
08F3	F1 ZE	2116 2117		F'OF'	AF A,(HL)		FRESTORE A FGET PIXEL
08F5 08F7	CB41 2804	2118 2119		BIT JR	O,C Z,PIXEL1		;CHECK LSB ;JUMP IF ZERO
08F9 08FB	CB27 CB27	2120 2121		SLA	A		;SHIFT A ;SHIFT A
08FD 08FF	CB49 2808	2123	XEL1:	JR	I,C Z,PIXEL2		;TEST NEXT BIT ;JUMP IF ZERO
0903	CB27 CB27	2124 2125		SLA	A		;SHIFT ;SHIFT
0905	CB27 CB27	2126 2127		SLA	A		;SHIFT ;SHIFT
0909 090B	E6C0 D1	2129	KEL2:	POP	11000000B DE		; MASK ALL ELSE ; RESTORE DE
0900 0900	E1 FDE1	2130 2131		POP	HL		RESTORE HL
090F 0910	C9 4F		KEL3:		C.A		RETURN SAVE DATA
0911 0912 0914	F1 163F E6CO	2134 2135		FOF LD	AF D,00111111B		RESTORE COLOR LOAD A MASK
0914 0916 0918	CB41 280A	2136 2137 2138		BIT	11000000B 0,C		MASK COLOR FIEST READ FLAG
091A 091C	CB3F CB3F	2139 2140		JR SRL	Z,FIXEL4		SUMP IF NOT SET
091E 091F	F5 76	2141 2142		PUSH			SAVE AF
0920 0921	OF OF	2143 2144		RRCA RRCA	A,D		ROTATE RIGHT
0922 0923	57 F 1	2145 2146		LD POP	D,A AF		ROTATE RIGHT
0924	CB49		(EL4:		1,0		RESTORE AF TEST LSB

Listing 1 continued on page 246



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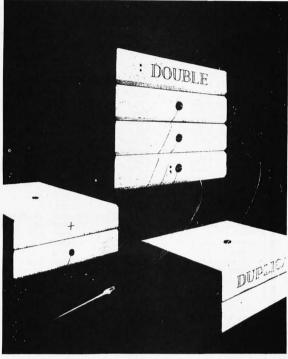
Listing 1 continued:

Listing	1 continued:					
0926	2810	2148		JR	Z,P1XEL5	JUMP IF ZERO
0928	CB3F	2149		SRL	A	SHIFT
092A	CB3F	2150		SRL	A	;SHIFT ;SHIFT
092C 092E	CB3F CB3F	2151		SRL.	A	SHIFT
0930	F5	2153		PUSH		SAVE AF
0931	7A	2154		LD	A,D	
0932	OF	2155		RRCA		ROTATE RIGHT
0933 0934	OF OF	2156 2157		RRCA		ROTATE RIGHT
0935	OF	2158		RRCA		FROTATE RIGHT
0936	57	2159		LD	D,A	GET THE MASK
0937	F1	2160		POP.	AF	RESTORE AF
0938	4F	2161	PIXEL5:		C,A A,(HL)	;SAVE MASK ;GET DATA
0939 093A	7E A2	2162		LD	D	MASK THE OLD
073B	B1	2164		OR:	C	AND DATA
0930	77	2165		L.D	(HL),A	SAVE PIXEL
0930	D1	2166		POP	DE HL	;RESTORE DE ;RESTORE HL
093E 093F	E1 FDE1	2167 2168		FOF	IY	RESTORE IY
0941	C9	2169		RET		FRETURN
0942	DD7E01	2170	FIXEL6:		A, (IX+GDR1)	;LOAD Y
0945	2F	2171		CPL	II. A	COMPLEMENT
0946 0947	67 CB3C	2172 2173		LD SRL	H,A	;LOAD H ;SHIFT
0949	DD6E00	2174		LD	L,(IX+GDRO)	;LOAD X
094C	CB3C	2175		SRL	Н	;SHIFT
094E	CB1D	2176		RR	L	SHIFT
0950 0952	CB3C	2177 2178		SRL	H	;SHIFT ;SHIFT
0954	CB1D CB3C	2179		SRL	H	SHIFT
0956	CB1D	2180		RR	Ĺ	SHIFT
0958	110028	2181		LD	DE,RBOTTOM+2048	; LOAD BASE ADDRESS
095B	19	2182		ADD	HL, DE	;ADD OFFSET
095C 095F	DD7E00 E606	2183		LD AND	A,(IX+GDRO) 00000110B	GET X GET X GET X GET X GET X
0961	CB3F	2185		SRL	A	SHIFT
0963	CB41	2186		BIT	0,0	;TEST LSB
0965	281E	2187		JR	Z,PIXEL9	JUMP IF NOT SET
0967	4F	2188		L.D POP	C,A AF	; SAVE A ; RESTORE A
0968 0969	F1 7E	2189 2190		L.D	A,(HL)	GET PIXEL DATA
096A	CB41	2191		BIT	0,0	TEST NEXT BIT
096C	2804	2192		JR	Z,FIXEL7	JUMP IF NOT SET
096E	CB27	2193		SLA	A	SHIFT
0970 0972	CB27 CB49	2194	PIXEL7:		A 1,C	;SHIFT ;TEST NEXT BIT
0974	2808	2196	I INCLI	JR	Z,PIXEL8	JUMP IF NOT SET
0976	CB27	2197		SLA	Α	; SHIFT
0978	CB27	2198		SLA	A	SHIFT
097A 097C	CB27 CB27	2199		SLA	A	;SHIFT ;SHIFT
097E	E6C0	2201	PIXEL8:		11000000B	; AND ALL ELSE
0980	D1	2202		FOF	DE	RESTORE DE
0981	E1	2203		POP	HL	; RESTORE HL
0982 0984	FDE1 C9	2204 2205		FOF RET	IY	RESTORE IY
0985	4F	2206	PIXEL9:		C.A	; RETURN ; RESTORE A
0986	F1	2207	· · · · · · · · · · · · · · · · · · ·	POP	AF	RESTORE STACK
0987	163F	2208		L.D	D,00111111B	GET THE MASK
0989	E6C0	2209		AND	11000000B	MASK ALL ELSE
098B 098D	CB41 280A	2210		BIT	O,C Z,PIXELA	;CHECK LSB ;JUMP IF ZERO
098F	CB3F	2212		SRL	A	;SHIFT
0991	CB3F	2213		SRL	A.	;SHIFT
0993	F5	2214		PUSH		; SAVE AF
0994 0995	7A OF	2215		LD RRCA	A,D	FORT THE MASK FROTATE RIGHT
0996	0F	2217		RRCA		ROTATE RIGHT
0997	57	2218		LD	D,A	RESTORE THE MASK
0998	F1	2219	E-THELL	POP	AF	RESTORE THE MASK
0999 099B	CB49 2810	2220 2221	FIXELA:	BIT	1,C Z,PIXELB	; CHECK NEXT BIT ; JUMP IF ZERO
05.5D	CB3F	2222		SRL.	A	;SHIFT
099F	CB3F	2223		SRL	A	FSHIFT
09A1	CB3F	2224		SRL	A	SHIFT
09A3 09A5	CB3F F5	2225 2226		SRL FUSH	A AF	;SHIFT ;SAVE AF
0946	7A	2227		L.D	A,D	GET THE MASK
09'A7	OF	2228		RRCA		FROTATE RIGHT
09A8	OF OF	2229		RRCA		ROTATE RIGHT
09A9 09AA	OF OF	2230		RRCA		;ROTATE RIGHT ;ROTATE RIGHT
09'AB	57	2232		LD	D,A	GET THE MASK
09AC	F1	2233		POP	AF	FRESTORE AF
09AD	4F	2234	FIXELB:		C, A	ISAVE A
09AE 09AF	7E A2	2235		LD AND	A,(HL)	FGET PIXEL DATA FMASK THE OLD
0980	B1	2237		OR	C	OR WITH C
09B1	77	2238		LD	(HL),A	SAVE PIXEL
0982	D1	2239		POP	DE	RESTORE DE
09B3 09B4	E1 FDE1	2240 2241		POP POP	HL IY	FRESTORE HL
09B6	C9	2242		RET		FRETURN
09B7	DD7E01	2243	PIXELC:	LD	A,(IX+GDR1)	MOVE Y TO A
09BA	2F	2244		CPL		3 COMPLEMENT
					1	Listing 1 continued on page 2

Listing 1 continued on page 248

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9
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Listing 1 continued.

Listing	1 continued:					
09BB	67	2245		L.D	H-A	SAVE IN H
09BC 09BF	DD6E00 CB3C	2246 2247		SRL	L,(IX+GDRO)	;GET X ;SHIFT H
0901	CB1D	2248		RR	Ĺ	;SHIFT L
0903	CB3C	2249		SRL	Н	SHIFT H
0905	CB1D	2250		RR	H	;SHIFT L ;SHIFT H
0907 0909	CB3C CB1D	2251 2252		SRL	H L	;SHIFT L
09CB	110018	2253		LD	DE,RBOTTOM-2048	FOINT TO BOTTOM
OPCE	19	2254		ADD	HL, DE	ADD OFFSET
09CF 09D2	DD7E00 E607	2255 2256		L.D AND	A,(IX+GDRO) 00000111B	;GET X ;MASK A
0904	CB41	2257		BIT	0,0	;TEST LSB
0906	2824	2258		JR	Z,FIXELG	TEST FOR READ
0908	4F	2259 2260		LD FOF	C,A	LOAD C
09D9 09DA	F 1 7E	2261		LD	AF A,(HL)	;RESTORE A ;GET PIXEL
09DB	CB41	2262		BIT	0 · C	TEST LSB
09DD	2802	2263		JR	Z,FIXELD	JUMP AROUND SHIFT
09DF 09E1	CB27 CB49	2264 2265 F	IXELD:	SLA	A 1,C	;SHIFT A ;TEST NEXT BIT
09E3	2804	2266	INLLD.	JR	Z,FIXELE	JUMP AROUND SHIFT
09E5	CB27	2267		SLA	A	SHIFT A
09E7	CB27	2268) T V E I E +	SLA	A	SHIFT A
09E9 09EB	CB51 2808	2269 I 2270	IXELE:	BIT	2,C Z,FIXELF	FIEST NEXT BIT
09ED	CB27	2271		SLA	A	SHIFT A
OPEF	CB27	2272		SLA	A	SHIFT A
09F1 09F3	CB27 CB27	2273 2274		SLA	A	SHIFT A
09F5	E680		IXELF:		10000000B	;SHIFT A ;MASK ALL OTHERS
09F7	D1	2276		F'OF'	DE	RESTORE DE
09F8	E1	2277		POP	HL	RESTORE HL
09F9 09FB	FDE1 C9	2278 2279		POP	IY	RESTORE IY
09FC	4F		IXELG		C,A	SAVE DATA
O9FD	F1	2281		POP	AF	GET COLOR
09FE 0A00	167F E680	2282 2283		LD AND	D,01111111B 10000000B	LOAD THE MASK
0402	CB41	2284		BIT	0,C	; MASK ALL ELSE ; TEST LSB
0A04	2807	2285		JR	Z,PIXELH	JUMP AROUND SHIFT
0A06	CB3F	2286		SRL	A	;SHIFT
0A08 0A09	F5 7A	2287 2288		PUSH LD	AF A,D	; SAVE AF ; GET THE MASK
DADA	OF	2289		RRCA	H, D	ROTATE RIGHT
OAOB	57	2290		LD	D,A	GET THE MASK
DADC	F1 CB49	2291 2292 F	IXELH:	POP	AF 1,C	RESTORE AF
OAOF	280A	2293	1 VE L. FI	JR	Z,PIXELI	;TEST NEXT BIT ;JUMP AROUND SHIFT
0A11	CB3F	2294		SRL	A	;SHIFT
0A13	CB3F F5	2295 2296		SRL.	A	SHIFT
0A16	7A	2297		FUSH	A,D	FRAVE AF
0A17	UF	2298		RRCA		FROTATE RIGHT
0A18	OF	2299		RRCA		*ROTATE RIGHT
0A19 0A1A	57 F1	2300		LD	D.A	GET THE MASK
DAIB	CB51	2301 2302 F	IXELI:	POP BIT	AF 2,C	; RESTORE AF ; TEST NEXT BIT
DAID	2810	2303		JR	Z, PIXELJ	JUMP AROUND SHIFT
OA1F	CB3F	2304		SRL	A	SHIFT
0A21 0A23	CB3F CB3F	2305 2306		SRL	A	;SHIFT ;SHIFT
0A25	CB3F	2307		SRL		SHIFT
0A27	F5	2308		PUSH		SAVE AF
0A28 0A29	7A OF	2309		L.D RRCA	A,D	GET THE MASK
DA2A	0F	2311		RRCA		ROTATE RIGHT
DAZB	OF	2312		RRCA		ROTATE RIGHT
0A2C	OF	2313		RRCA	D A	ROTATE RIGHT
OA2D OA2E	57 F1	2314 2315		LD POP	D,A AF	; GET THE MASK ; RESTORE AF
0A2F	4F		IXELJ:		C,A	SAVE A
0A30	7E	2317		LD	A,(HL)	GET PIXEL
0A31 0A32	A2 B1	2318 2319		AND OR	D	MASK THE OLD PART
UA33	77	2320		LD	C (HL),A	;OR DATA ;SAVE PIXEL
0A34	D1	2321		POP	DE	RESTORE DE
0A35	E1	2322		F'OF	HL	RESTORE HL
0A36 0A38	FDE1 C9	2323 2324		POP	IY	;RESTORE IY ;RETURN
01100	•	2325		1811		TRETORIE
		2326 ;	POKE :	****	************	**************
		2327 ;		IDITE	E DATA TO THE DIVE	AT XY. POKE SETS A
		2329	WRITE	FLAG	THEN CALLS PIXEL.	THE COLOR DATA IS
		2330 ;	EXPEC		N REGISTER A.	
		2331 ;			PIXEL	
		2333	UNLLO		ITALL	
		2334 ;	CALLED	BY	LPIX	
		2335 ;			PUT	

2337

2338

2339 2340

2341

REGISTERS

1/0

C

NONE

(WRITE FLAG)

Listing 1 continued on page 250

SIRIUS 80+ **High Performance Low Cost Floppy Add-Ons!**

The SIRIUS SYSTEMS 80+ Series of Floppy Disk add-ons are designed to provide unmatched versatility and performance for your TRS-80°. Consisting of four different addons, there is a 80+ Series Floppy Disk Drive to match your series. meet your needs.

COMMON CHARACTERISTICS

- 5ms track-to-track access time

- 5711S ITACK-10-TRACK ACCESS TIME AUTO-Eject 180 day WARRANTY Exceptional speed stability 11/2% Single/Double Density operation Mix any or all 80 + Series on the SS Standard cable

SPECIFIC CHARACTERISTICS
The SIRIUS 80+1 -a single sided, 40 track
Drive. Offering 5 more tracks than the Radio
Shack model, it cost \$120 less. Formatted
data storage is 102K/204K Bytes Single/

The SIRIUS 80+2 is a dual sided, 80 track (40 per side) Disk Drive. It appears to the TRS-80-as TWO 40 track drives yet COST LESS THAN HALF THE PRICEI Even greater savings result since data is recorded on both sides of the media instead of only a single side. This unit may require the SS Standard cable. Formatted data storage is 204K/408K Bytes Single/Double Density

Density. SIRIUS 80+2 \$449.95 The **SIRIUS 80+3** - a single sided, 80 track Drive. Offering 2½ times the storage of a standard Radio Shack Disk Drive, the 80+3 greatly reduces the need for diskettes corre-spondingly. Additionally, because of the in-creased storage and faster track-to-track access time, the 80+3 allows tremendously increased throughput for disk based pro-



grams! The 80+3 includes SIRIUS's TRAKS-PATCH on diskette (for use with 96 tpi drives). Formatted data storage is 204K/408K Bytes Single/Double Density. SIRIUS 80+3\$499.95

The SIRIUS 80+4 -a dual sided, 160 track (80 The SIRIUS 80-4 -a dual sided, 160 track (80 per side) 5¼" monster! The ultimate in state-of-the-art 5½" Floppy Disk Technology, the 80+4 is seen by the TRS-80* as two single sided disk drives. Thus, in terms of capacity, one 80+4 is equivalent to 4½ standard Radio Shack drives — at a savings of over 73% (not omention diskettes!!). (With a double density converter the available memory is huge!) The 80+4 (a 96 tpi drive) includes TRAKS-PATCH on diskette and may require the SS Standard cable. Formatted storage is 408K/816K Bytes Single/Double Density, SIRIUS 80+4

All 80 + Series Floppy Disk add-ons operate at 5ms track-to-track but are Expansion Interface limited to 12ms for the TRS-80* *TRS-80@ of Tandy Corp.

Save up to 10% with these SIRIUS Packages!

NEWDOS/80, SIRIUS 80+3, and Two Drive Cable	\$624.95
NEWDOS/80, SIRIUS 80+4, and Two Drive Cable	
NEWDOS/80, Two (2) SIRIUS 80+3's, Two Drive C	
NEWDOS/80 Two (2) SIRIUS 80+4's Two Drive (4:

PRIAM **Hard Disks Now Available** from SIRIUS SYSTEMS!



PRIAM's high-performance, low-cost Winchester disc drives speed up throughput and expand data storage from 20 megabytes to 154 megabytes. And a single controller can be used to operate 14-inch-disc drives with capacities of 33, 66, or 154 megabytes. So it's easy to move up in capacity, or reduce package size, without changing important system elements or performance. ■ DC Power required only!

- Fast, Linear Voice Coil Positioning
- 10 ms track-to-track positioning
- Fully servoed head positioning
- Dedicated servo tracks
- Simple, parallel Interface ■ Optional SMD Interface
- 50 ms Average Positioning time ■ 90 ms Maximum Positioning Time ■ 6.4 ms Average Latency

THE PRIAM LINEUP

Model/Disc Size	Capacity	Size	Weight	Price
DISKOS 3350 (14")	33Mbytes	7" × 17" × 20"	33 lbs.	\$2995
DISKOS 6650 (14")	66 Mbytes	7" × 17" × 20"	33 lbs.	\$3749
DISKOS 15450 (14")	154 Mbytes	7" × 17" × 20"	33 lbs.	\$4695
DISKOS 2050 (8")	20 Mbytes	4.62" × 8.55" × 14.25"	20 lbs.	\$2995
DISKOS 3450 (8")	34 Mbytes	$4.62'' \times 8.55'' \times 14.25''$	20 lbs.	\$3745
DISKOS 1070	10.6 Mbytes	floppy-size	(low)	\$2195
All PRIAM DISKOS Drives I		f 1.03 Mbytes/Sec.	, ,	
Optional SMD interface ava.	ilable for \$150.			

SIRIUS SYTEMS offer cases and enclosures for all PRIAM Hard Disk Drives. All 14" Winchester Drives will mount in our 14" Standard Case. The 8" Winchesters have two alternatives: a single drive case and a dual drive case. All SIRIUS SYSTEMS Winchester drive cases include Power Supply, internal cabling, switches, fan, extra AC outlet (not switched, but fused) and possess very adequate ventilation. Drive addressing is done on the rear of the Case and not on the drive iteset to provide ease of use during operation. All WINCHESTER DRIVE Cases are Warranted for a full year and come in our standard blue-black color scheme. Consult us for current availability and pricing.

Introducing the Versatile, Low-Cost OMEGA Series Controller

As new technological advances bring down the cost of fast, reliable mass data storage, the need for an inexpensive, versatile controller have become greater and greater. To meet this need, SIRIUS SYSTEMS OMEGA Series Controller was designed

was oesigneu.

The SIRIUS OMEGA Series Controller Module utilizes an on-board microprocessor to mediate data transfer to a wide variety of peripherals from an equally wide variety of host computer systems. Up to four Winchester Hard Disks (8" or 14"), four 5". Floppy Disks Drives and/or up to eight 8" Floppy Disk Drives may be in use at one time. Host systems interfacies accomplished via a parallel or a serial interface. With the addition of a Personality module, the OMEGA Series Controller Module is directly the OMEGA Series Controller Module is directly tace. With the addition of a Personality module, the OMEGA Series Controller Module is directly compatible with many popular computer systems (among them the TRS-80*, Apple, Heath, and others). Provision is made for the addition of a streaming tape drive, also.

SPECIFIC HARDWARE

- SPECIFIC HARDWARE
 FEATURES INCLUDE:

 Control of up to twelve Floppy Disk Drives
 (eight 8" and/or four 5½")

 8" and/or 5¼" Disk Drive Utilization

 Single (FM) or Double (MFM) density data
 - storage
 - storage

 Hard or Soft sectored diskette usage

 Utilization of "Quad" density (96 tpi) 8" or
 5¼" Disk Drives

- Control of up to four WINCHESTER type PRIAM DISKOS Disk Drives
 - 8" or 14" may intermix on the same cable
 Accommodates 8" and/or 14" drives of
 5.3Mbytes to 154Mbytes
 Ultra-Fast data transfers
- Extremely flexible host-controller interfacing

SPECIFIC SOFTWARE FEATURES INCLUDE:

- FEATURES INCLUDE:

 Dynamic format modifications via command words

 Extremely flexible format acceptance for unusual data storage formats

 Easily interfaces to standard operating systems (TRS-DDS · CP/M* etc)

 Operates in either get/put sector mode or data string mode

- data string mode
 Performance parameters may be changed by
 EPROM replacement or Dynaminic Reprogramming

CP/M® of Digital Research

Dedicated systems cards are also available on a limited basis for the STD-BUS and the S 100. These cards feature shared memory also (again, software selectable) in addition to the regular OMEGA Series Controller Module features. Consult SIRIUS SYTEMS for current price and availability for the entire line of OMEGA Series Memory Units and Controllers. Dealer inquiries are invited. ies are invited.

What TFORTH is - and what it has to offer YOU!

TFORTH is a unique growth programming language for the TRS-80* that combines the best features of an interpreter and a compiler all in one functional easy-to-use package. TFORTH cannot be simply compared with Fortran, BASIC or PASCAL. This high speed, high level modular code offers the speed found in many FORTRAN compliers yet retains the on-line conviences found in BASIC INTERPRETERS by flaggling input errors as they occur line-by-line. Unlike PASCAL, TFORTH needs no "run-time" package for support. Serving as an operating system, compiler, assembler, interpreter, virtual memory manager, all in one: TFORTH makes easy, efficient- structured re-entrant programs a natural consequence.

preter, virtual memory manager, all in one: TFORTH makes easy, efficient-structured re-entrant programs a natural consequence.

The key to TFORTH's flexibility and ease of use lies in its use of a stack for parameters and a unique dictionary for WORDS. These WORDS are stated in terms of other WORDS already defined in the dictionary. It is this rich set of WORDS that provides DO LOOPS, IF-THEN-ELSE statements, BEGIN-END statements, virtual memory, any number base (to base 32) for input or output, a macro assembler, re-entrant code, multithread dictionary, line editor, excellent math package (16 bit integers, double precision floating point, SIN, COS, TAN, EXP and LOG) and it runs under either TRSDOS* or NEWDOS. Assembler inherently nests with high level in an easy fashion. Complicated drivers for new devices take only a few lines of TFORTH which saves both memory and disk space!

TFORTH is a procedural language specifing a process rather than a desired result. The ability to have the language grow in the direction the user desires is excellent for novel applications. New data types and new processes can become part of the language. Due to the modular constructions, a very compact code is produced which executes at exceptionally high speeds between machine code and machine code plus 20% typical overhead speeds. Memory requirements can be "less" than assembler coding or other high level languages.

TFORTH comes complete for the TRS-808 with as little as 16K of memory and a single Disk Drive using either TRS-DOS* or NEWDOS. It provided on diskettes and an optional Math and Utilities package is available.

using either TRS-DOS* or NEWDOS. It provided on diskettes and an optional Math and Utilities package is available.

Through TFORTH an excellent way to develop new languages, provide simple control of device (including video monitors, A/D and D/A converters and burglar alarms) and to implement tasks requiring monitoring and decision is offered. Many WORDS to handle peripherals are part of basic TFORTH and others may be added easily, Often, substantial hardware development can be eliminated by using TFORTH to do the major digital or reduction of data.

For many applications a minimal task may be written in high level (or mixture of assembler and high level) code: loaded, assembled and prior to execution may be written to the disk as a ready to execute machine code/EXE module with the DOS.

TFORTH (on diskette - specify for Standard or 96 tpi Disk Drives)

\$129.95

TFORTH with the addition of TRAKS-PATCH (a powerful combination!) \$136.95

STATE-OF-THE-ART DISK DRIVES

OUME® DataTrak 8 8" Disk Drive **DOUBLE SIDED! DOUBLE DENSITY!**

High performance Double Sided Disk 8" Disk Drive ■ Single or Double Density ■ Door Lock and Write Protect INCLUDED! ■ Negative DC Voltage not required ■ Low Power Operation

- FAST! 3ms track-to-track access
- Low friction and minimum wea Superior Head Load Dynamics

QUME DataTrak 8\$574.95 (2/\$549 ea) Connector Set #3 (AC, DC, & Card Edge) Connector Set #4 (AC and DC)\$2.95

5¼" Disk Drives

MPI 51/52 & 91/92

- Fast! 5ms track-to-track access

- Fast: 5ms track-to-track access Exclusive Pulley-Band Design Unique Door/Ejector Mechanism Reliable 11/2% Speed Stability Single|Double Density Operation Industry/ANSI Standard Interface

MPI 51 (Single Head/40 tracks) 125K/250K Bytes Single/Double Density \$259.95 MPI52 (Dual Head/80 tracks (40/side))

250K/500K Bytes Single/Double Densi \$349.95

MPI 91 (Single Head/80 tracks) 250K/500K Bytes Single/Double Density MPI 92 (Dual Head/160 tracks (80/side))

500K/1000K Bytes Single/Double Density ...\$524.95

MPI Technical Manual * * Unformatted data storage

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We accept MC, VISA, AE, COD (requires Certified Check, Cashier's Check or Cash) and Checks (personal checks require 14 days to clear). SHIPPING AND HANDLING: \$7.00 per Floppy Disk Drive or 80+ Module ■ 5% for other items (any excess will be refunded) ■ Foreign Orders add 10% for Shipping & Handling. Payment in U.S. currency ■ Tennessee residents add 6% Sales Tax ■ VOLUME DISCOUNTS AVAILABLE

.\$6.95

Announcing the most important utility ever introduced for the TRS-80* Model I and Model II-

TM

FNHRAS is an Enhanced Basic extension module, which loads at the top of BASIC, adding many commands and background tasks-

□Over 30 new commands added to your

- •SORT-Multi-keying, multi-tagging array sort. Sorts thousands of items in mere seconds, all with one command!
- •JNAME-Use line labels along with line numbers in branching statements, as in assembly language, using the ENHBAS commands GTO and CSUB (special GOTO and GOSUB).

How many times have you wanted to use variables to reference line numbers? Now you can! GTO and CSUB allow variable expressions as operands, such as in GTO X+40.

- •WHILE / WEND-New, structured programming loop construct. Makes for more logical program flow (less GOTO's).
- •EXEC / EVAL-Two new, extremely pow-erful functions! EVAL evaluates an alge-braic expression in string form. With EVAL you can manipulate complex functions in string form, and then evaluate them. **EXEC** executes a string expression as if it were a BASIC program line! With EXEC, your computer can actually write its own programs and execute them!
- CALL-Pass control to machine language subroutines at any address, passing parameters both ways.
- •CLM / PAGE-Set up automatic page roll-over and other line printer functions from BASIC.

•All these and many more!

□In addition to the above commands, Model I ENHBAS contains vector graphics and drawing commands. Model II ENHBAS has many functions suited to business program-ming—ISAM file handling commands, RS-232 access, and many more; along with several Model I BASIC commands left out of Model II (PEEK, POKE, OUT, etc.).

DENHBAS includes many background utilities (Model I version):

ities (Model I Version).

*User-definable cursor

*Key click

*Two-tone beep on error

*Automatic lower-case

*Automatic debounce

*Short-entry commands

(Shift-letter prints command)

*Real Control keys

*One letter commands

*Formatted LISTings

ENHBAS is available for:

*TRS-80 is a reg. trademark of Radio Shack, a Tandy Co.

32K Model I Disk

Dealer and OEM inquiries invited.

The Cornsoft Group

6008 N.Keystone Ave., Dept. B Indianapolis, IN 46220 (317) 257-3227

```
Listing 1 continued:
```

: STRUCTURES NONE 2342 2343 SET WRITE FLAG 2344 FOKE : LD C, 0 DEOD 0439 WRITE THE PIXEL CALL PIXEL 0A3B CDAAO8 2345 FRETURN 2346 RET DASE C9 2347 2348 PLOT ******************** 2349 PLOT PLOTS A POINT AT XY AND AT A LARGER WIDTH IF 2350 NECESSARY. PLOT FIRST PUTS THE BASIC POINT AT XY 2351 AND IF A LARGER WIDTH IS SPECIFIED, AROUND THE POINT. 2352 2353 2354 PHIT 2355 UFC CALLED BY 2356 2357 REGISTERS (INDEX REGISTER) 2358 IX 2359 2360 2361 2362 STRUCTURES GDRO (X) 2363 GDR1 (VECTOR MODE) 2364 GDR5 2345 2366 FUT TO BASIC FOINT CD7E0A PLOT: CALL PUT DASE 7,(IX+GDR5) Z 0A42 DDCB057E TEST WIDTH FRETURN IF NOT SET 0446 CB 2368 RET DD3400 (IX+GDRO) 0447 2369 INC CD7EDA 2370 CALL PUT FUT THE NEXT POINT DA4A OA4D DD3401 2371 INC (IX+GDR1) ; INCREMENT Y 0A50 CD7EOA 2372 CALL PUT FUT THE NEXT POINT (TX+GDRO) DECREMENT X
PUT THE NEXT POINT 0453 003500 2373 DEC CD7EDA 2374 0456 CALL PUT UA59 003500 2375 (IX+GDRO) DECREMENT DASC CDZEDA 2376 CALL PUT FUT THE NEXT POINT 2377 OASE DD3501 (TX+GDR1) DEC DECREMENT Y 2378 0862 CD7EOA CALL PUT FUT THE NEXT POINT DD3501 CD7EOA DA65 2379 DEC (IX+GDR1) DECREMENT 2380 0468 CALL PUT FUT THE NEXT POINT DD3400 2381 DASB TNC (TX+GDRO) : INCREMENT X DAGE CD7E0A 2382 CALL PUT FUT THE NEXT POINT DD3400 2383 0A71 TNC (IX+GDRO) : INCREMENT 0A74 CD7EOA 2384 CALL PUT FUT THE NEXT POINT FRESTORE X 0A77 003500 2385 DEC (IX+GDRO) DAZA DD3401 2386 TNC (IX+GDR1) DAZD 09 2387 RET FRETURN 2388 2389 PUT **************************** 2390 2391 PUT PUTS A POINT AT XY IF IT IS NOT CLIPPED. PUT 2392 FIRST CALLS CLIP THEN PLOTS THE POINT IF IS NOT 2393 CLIPPED. 2394 2395 CALLS CLIP 2396 2397 2398 CALLED BY FLOT, MOU, SYM 2399 2400 REGISTERS NONE 2401 2402 NONE 2403 2404 STRUCTURES NONE 2405 ;TEST FOR CLIP;CHECK SUCCESS DAZE CDFF07 2406 PUT: CALL CLIP 0A81 CB41 2407 BIT 0,0 RETURN IF CLIPPED 0A83 CB 2408 0A84 CD390A 2409 CALL POKE FUT POINT 2410 0A87 CS RET FRETURN 2411 2412 SENDBK ***************************** SENDBK OUTPUTS B BYTES OF DATA STARTING AT HL. SENDBK FIRST GETS THE DATA, INCREMENTS THE POINTER, THEN CONTINUES THE LOOP UNTIL B IS ZERO. 2414 2415 2416 2417 2418 2419 CALLS SENDBY 2420 CALLED BY 2421 RSHE 2422 RSYM 2423 2424 REGISTERS A 2425 (BYTE COUNT) Ρ. 2426 (FOINTER) 2427 (POINTER) 2428 2429 NONE 2430 2431 STRUCTURES NONE 2432 0A88 7E SENDBK: LD A. (HL) GET THE DATA ; INCREMENT THE POINTER ; SEND THE DATA ; DECREMENT COUNT 0489 23 2434 2435 INC HL. CALL SENDBY CD910A DABA DARD 05 2436 DEC 20F8 DASE 2437 JR. NZ, SENDBK CONTINUE IF NON ZERO 2438 RET RETHEN

Listing 1 continued on page 252



ANALOG INTERFACES

Industrial, Scientific, Laboratory, or Commercial Microcomputer Users-

Industrial quality data conversion boards for APPLE, S-100. PET, TRS-80, AIM, and KIM systems. Tecmar can provide individual boards, data conversion subsystems, or complete Data Conversion Systems. Tecmar's growing product line offers outstanding features, meticulous engineering, exceptional documentation, and a seven year record of proven reliability.

TRS-80



TRS-80

Tecmar's new Analog to Digital converter Board (AD200) is designed to meet sophisticated data acquisition needs. The board accommodates various precision A/D modules by Analogic and Data Translation. These modules are easily interchanged to provide options such as 12, 14, or 16 bit accuracy; 125 KHz throughput; variable ranges and gains.

AD200XX AD200AP

S-100 A/D and Timer Board Apple A/D Board \$495

AD-200 Features

- 12 bit accuracy and resolution standard
- 30 KHz conversion rate standard
- Jumper selectable for 16 single-ended or 8 differential inputs
- External trigger of A/D
- Output formats: Two's complement, binary, offset binary
- Auto channel incrementing from any channel to any channel
- Data is latched providing pipelining for higher throughputs
- Provision for synchronizing A/Ds
- Utilizes interrupt for status test
- Jumper selectable input ranges: $\pm 10V$, $\pm 5V$, 0 to $\pm 10V$, 0 to $\pm 5V$ In addition the S-100 version:
- Complies with IEEE S-100 specifications
- Transfers data in 8 or 16 bit words
- Provides for expansion to 256 channels
- Is switch selectable I/O or memory mapped

Timer Features on S-100 Board

In addition to the A/D features, the S-100 Board contains a powerful timer circuit which can start A/D conversion and can also be used independently for time of day, event counting, frequency shift keying and many other applications.

- 5 independent 16 bit counters (cascadable)
- 15 lines available for external use
- Time of day
- Event counter
- Alarm comparators on 2 counters
- · One shot or continuous frequency
- · Complex duty cycle and frequency shift keying outputs
- Programmable gating and count source selection
- Utilizes vectored interrupt

Apple D/A Features \$295

- 12 bit accuracy and resolution
- 2 independent digital to analog converters
- 8 parallel latched output lines
- Jumper selectable output ranges: $\pm 10V$, $\pm 5V$, $\pm 2.5V$, 0 to +10V, 0 to +5V
- 3 microsecond conversion time
- Minimal software required

PET **TRS-80 KIM** S-100

The original Tecmar data conversion boards (AD-100 and DA-100) continue to solve less sophisticated conversion problems. These S-100 boards interface to the PET, TRS-80, AIM, and KIM through standard S-100 expansion interfaces.

AD-100 Features

- · 12 bit accuracy and resolu-
- 30 KHz conversion rate
- 16 single-ended or 8 differential inputs (specify AD100S or AD100D)
- Jumper selectable I/0 or memory mapped
- operation for S-100 systems
- Jumper selectable input ranges: $\pm 10V$, $\pm 5V$, 0 to +10V, 0 to +5V
- Minimal software required
- Complies with IEEE S-100 specifications.

DA-100 Features

- 12 bit accuracy and resolution
- 4 independent digital to analog converters
- 3 microsecond settling time
- Jumper selectable output ranges: $\pm 10V$, $\pm 5V$, $\pm 2.5V$, 0
- to +10V, 0 to 5V Jumper selectable I/O or memory mapped operation
- for S-100 systems
- Minimal software required Complies with IEEE S-100

specifications

\$250 Expansion board, power supply, and enclosure for PET Expansion board and power supply for TRS-80, KIM, or AIM 150

Ontions for AD-200

Options for AD-200	
Programmable gain up to 500	\$ 175
14 bit accuracy	645
16 bit accuracy	1,045
100 KHz conversion rate	445
125 KHz conversion rate	545
 Screw Terminal and Signal Conditioning panel 	250
Thermocouple cold junction compensation	125
 Rack mounting assembly with plexiglass cover 	125

S-100 Real Time Video Digitizer

- Digitizes and Displays in 1/60 sec, flicker-free
- 16 Gray Levels
- Switch Selectable to display Black and White Graphics (8 pixels/byte)
- Maximum Resolution: 512 pixels/line x 240 lines
- Minimal software \$850 requirements

S-100 BOARDS

8086 CPU W/vectored interrupts \$395 RAM 8Kx16/16Kx8 8086 \$495 PROM-I/O Serial and \$350 Parallel I/O Parallel I/O \$350

¹Reg. Trademark of Tandy Corp. ²Reg. Trademark of Commodore

& Timer



Low level, wide range permitting low level sensors

such as thermocouples, pressure sensors and strain

gauges to be directly connected to the module input

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TECMAR, INC. (216) 382-7599

't risk magnetic damage to EDP storage media

Many computer users have learned "the hard way" that accidental exposure to magnetic fields can erase or alter data and programs stored on disks and tapes. Such irretrievable loss can occur during media transit or storage if unprotected disks or tapes are exposed to the magnetic fields produced by motors, transformers, generators, electronic equipment, or even intense transient fields induced by electrical storms.

Data-Safe Products provide reliable, economical protection against stray magnetic field damage by shielding disks and tapes with the same high-permeability alloy used to shield cathode ray tubes and other magnetic-sensitive components. DISK*SAFE Floppy Disk Protectors, punched for 3-ring binder, sandwich two 8" disks, or smaller mini-disks, between sheets of magnetic shielding alloy encased in the strong vinyl pockets. (Binder sent free with 10 Protectors).

ISK*SA

FLOPPY DISK PROTECTORS



TAPE*SA

METAL CASSETTE SHIELDS

TAPE *SAFE Cassette Shields are constructed of magnetic alloy, with heliarc-welded seams and an easyopen hinged top. Each attractively-finished TAPE *SAFE holds one cassette in its original plastic box. A shelved metal FILE DECK (not shown) stores up to six TAPE*SAFEs for easy access. (One free with each six TAPE*SAFEs). VISA and MasterCard telephone orders accepted. Prices below include shipping.

DISK*SAFE Floppy Disk Protectors: 1-5, \$8.95 ea; 6-9, \$7.95 ea; 10 or more w/binder, \$6.95 ea;

TAPE*SAFE Cassette Shields: 1-5, \$14.95 each; 6 or more with free FILE DECK, \$12.95 each. TAPE*SAFE FILE DECK: \$10.95 each.

Data-Safe Products, Inc.

EGDR8

0000

1926 Margaret St., Phila., PA 19124 • 215/535-3004 Dealer Inquiries Invited

```
Listing 1 continued:
                     2439
                     2440
                     2441
                               SENDBY OUTPUTS ONE BYTE OF DATA. SENDBY WAITS UNTIL
                     2442
                               THE OUTPUT IS CLEAR TO SEND, THEN OUTPUTS THE DATA
CURRENTLY IN A. THE OUTPUT INTERRUPT STATUS IS
                     2443
                     2445
                               SET AGAIN.
                     2446
                     2447
                               CALLS
                                                NONE
                     2448
                     2449
                               CALLED BY
                                               RREG
                     2450
                                                SENDBK
                     2451
                                                XERR, RPIX, RCRAM
                     2452
                     2453
                                                          (DATA)
                               REGISTERS
                     2454
                     2455
                               1/0
                                                PORT 2
                                                          (STATUS)
                                                         (OUTPUT)
                     2456
                                               PORT 6
                     2457
                               STRUCTURES
                                                NONE
                     2458
                     2459
                     2460
        DDCB0F5E
                             SENDBY: BIT
                                            3, (IX+GDR15)
                                                                   TEST OUTPUT INTERRUPT
0A91
                                            NZ, SENDBY
                                                                   JUMP IF STILL SET
0A95
        20FA
                     2461
0A97
        0306
                     2462
                                      OUT
                                              (6),A
                                                                   DISABLE INTERRUPTS
                     2463
                                      DI
0499
        F3
                                                                   ;SET THE OUTPUT FLAG
;GET THE STATUS
;SEND THE STATUS
DASA
        DDCBOFDE
                     2464
                                      SET
                                            3, (IX+GDR15)
OA9E
                     2465
2466
                                      I D
                                            A,(IX+GDR15)
(2),A
        DD7EOF
                                      OUT
DAA1
        D302
                                                                   FENABLE INTERRUPTS
DAA3
        FB
                     2467
                                      EI
        C9
                     2468
                                      RET
                                                                   FRETURN
                     2469
                     2470
                               2471
                               USER IS A DUMMY ROUTINE WHICH IS THE DEFAULT CALL FROM CALLS. USER SIMPLY RETURNS FROM A
                     2472
                     2473
                               SUBROUTINE.
                     2474
                     2475
                     2476
2477
                               CALLS
                                                NONE
                     2478
                               CALLED BY
                                                CALLS
                     2479
                     2480
                               REGISTERS
                                                NONE
                     2481
                                                NONE
                     2482
                               1/0
                      2483
                      2484
                               STRUCTURES
                                                NONE
                     2485
2486
                             USER:
                                      RET
                                                                   RETURN
        C 9
DAAS
                     2487
                     2488
                               END OF MICROGRAPH *********
                     2489
                                      END
                     2490
```

CROSS I	REFERE	NCE									
SYMBOL	VAL	M DEFN	REFS								
CALLS	026F	718	123								
CALLSI	0287	729	719								
CASE	07A0	1826	892	1084	1324	1497	1709				
CASEO	07B3	1838	1832								
CASE1	0701	1842	1840								
CASE2	0705	1851	1849								
CASE3	07E9	1860	1858								
CLIF	07EF	1897	908	1329	2406						
CLIFO	0808	1909	1907								
CLIF1	0821	1919	1916	1917							
CLIP10	0894	1974	1972								
CLIF2	0835	1928	1926								
CLIP3	0848	1938	1935	1936							
CLIF4	085F	1947	1945								
CLIP5	0870	1956	1952								
CLIF'6	087A	1961	1950								
CLIP7	0888	1968	1962								
CLIF8	088E	1971	1954	1957	1959	1964	1966	1969			
CLIP9	0892	1973	1955	1960	1967	1970					
COLOR	0049	212	1660	1710							
CONST	8800	117									
CRO	1000	218	779	788	798	821	1188	1197	1207	1230	
CR1	1010	219									
CR2	1020	220									
DEFIN	008E	145									
ECOLOR	004D	99	212								
EGDR	0004	39	152								
EGDRO	0004	40	153								
EGDR1	0005	41	154								
EGDR10	000E	50	163								
EGDR11	000F	51	164								
EGDR12	0010	52	165								
EGDR13	0011	53	166								
EGDR14	0012	54	167								
EGDR15	0013	55	168								
EGDR2	9000	42	155						*		
EGDR3	0007	43	156								
EGDR4	8000	44	157								
EGDR5	0009	45	158								
EGDR6	000A	46	159								
EGDR7	8000	47	160								

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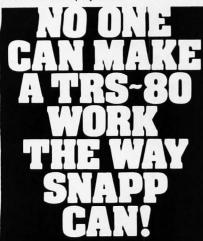


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	ELOO ELO1	0014	58	171										
	EL02	0018	59 60	172 173										
	ELO3 ELO4	001A 001C	61	174										
	ELO5 ELO6	001E 0020	62	175 176										
	ELO7	0020	64	177										
	EL10 EL11	0024	65 66	178 179										
	EL12	0028	67	180										
	EL13 EL14	002A	68	181 182										
	EL15	002E	70	183										
	EL16 EL17	0030	71 72	184 185										
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	ES06 ES07	003A	80 81	193 194										
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	ES12	003E	84	197										
	ES13 ES14	003F	85 86	198										
	ES15 ES16	0041	87 88	200										
	ES17	0042	89	202										
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	GDR11 GDR12	000B	164 165											
- 1	GDR13	0000	166											
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				351 519	352 520	388 574	389 575	395	396	421	422	448	449	
				637	638	2460	2464		583	604	605	632	633	
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	IVECT	8800	118	774										
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	L05 L06	001A	175 176											
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Write protect.
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401.016 bytes
(SD), 802.032 bytes (DD),
unformatted.
402.025 bytes
402.025 bytes
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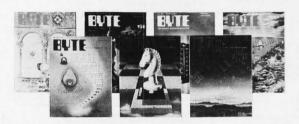
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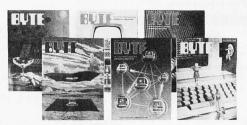
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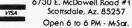
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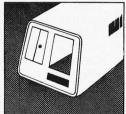
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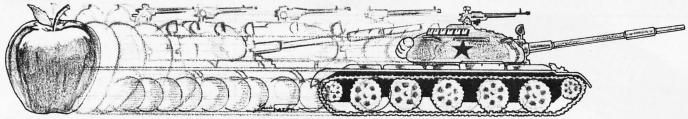
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clipping routines (CLIP and CASE). There are several utility routines maintaining the frame buffer (ie: PEEK and POKE, which place or return a pixel value at a given coordinate; PUT, which pokes a series of pixel values; and PIXEL, which does

213 316 317 317 321 322 324

the transformation from the coordinate plane to the physical memory). Only one routine in the entire package, PIXEL, directly manipulates the frame buffer. Besides PIXEL, all subroutines operate in a Cartesian coordinate system. Because of this structure, only PIXEL must be altered



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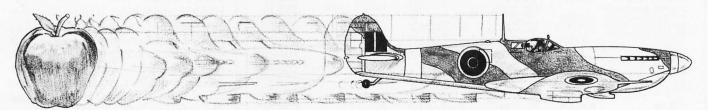
RED ATTACK! simulates an invasion by a mixed Soviet tank and infantry force against a defending battalion. As the defender, your task is to deploy your infantry units effectively to protect three crucial towns — towns that must not fall!

As the Russian aggressor, your objective is to crush the

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if different video-display hardware is used.

Also, there are five interruptservice routines. Four of these routines are directly connected to a hardware interrupt. For example, IN-PUT is called whenever the host sends a byte of data, and OUTPUT is called whenever the host receives a byte. XERR is called either by a nonmaskable interrupt (which signifies a fatal error in the hardware or in a user-supplied subroutine) or by any other routine capable of detecting an error. XERR then provides a debugging capability to the host and allows examination of memory or registers. Finally, FRAME is connected to the frame interrupt.

Whenever the video-display generator grants the bus to the microprocessor, an interrupt signal is generated on PIO (peripheral input/output) port 0. This interrupt allows a process to synchronize with the frame rate, since the interrupt occurs at the end of each frame. FRAME maintains a frame count, but also calls a routine, called NULL, located in programmable memory. If you desire to execute a routine at the frame rate, perhaps to perform some calculation for a game, simply load (via LSUB) a routine at NULL, and the software will call the routine at the start of every frame.

There isn't sufficient room to describe all of the features of this software. The source listing has many comments and provides a preamble to each routine describing the routine name, who calls it and whom it calls, a description, the registers affected, and the structures affected. Comments are also provided for every line of executable code; and there actually are more comments than code. The remainder of this discussion will cover some of the major structures and algorithms implemented in the Micrograph software.

Software Structures

As we mentioned in Part 1 of this article, there are two important abstractions that must be implemented in the Micrograph software. Abstractions denotes that the software appears as one thing to the user, while hiding the actual implementation. In this case, the abstractions allow the user to deal with manipulating images, rather than dealing with the bits and pieces of the frame buffer itself.

Text continued on page 266

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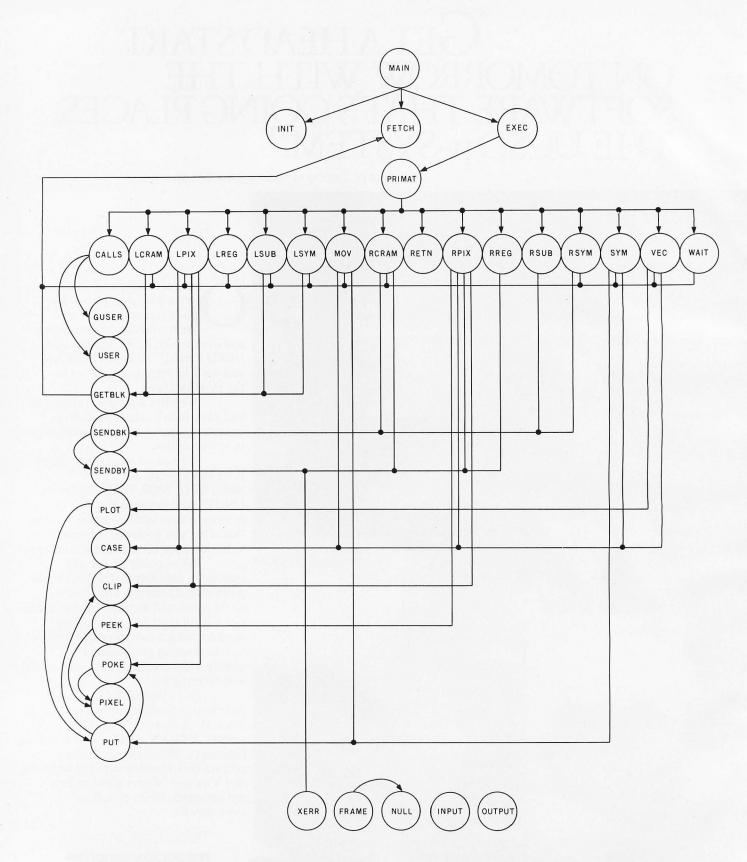


Figure 1: Hierarchy of subroutine calling in the Micrograph display-control program. The graphics primitives described in Part 1 are represented by the subroutines in the long horizontal row; all are called by the routine PRIMAT through an indirect process. The graphics-primitive routines may then call other routines, shown in the vertical column. The five routines shown in the horizontal row at the bottom are called by processor interrupts. Execution of a subroutine-return instruction causes control to branch to the routine EXEC.

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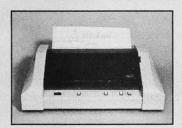
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Mnemonic	Name
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Table 2: Functions of the sixteen graphics-display registers of Micrograph.

Text continued from page 262:

One of the more important abstractions is the structure of the frame buffer appearing to be a Cartesian plane. In Micrograph, the user sees the system as a 256 by 256 pixel by 256 color display, which is physically and internally truncated to a lower resolution (eg: 64 by 64 pixels with four colors, 128 by 128 pixels with four colors, or 256 by 192 pixels with two colors). In reality, the frame buffer cannot be physically accessed using these same coordinates. Instead, the Micrograph firmware does the translation through the routine PIXEL from the Cartesian coordinates to the physical frame buffer.

Figure 2 shows the structure the system implements for the three resolutions available through Micrograph. Actually, all the 6847-supported resolutions are possible: the software, however, directly supports only three. The figure also indicates a border in which no individual pixels may be accessed.

The other critical structure that Micrograph must implement is the graphics-display register set. As Parts 1 and 2 explained, the graphicsdisplay registers define system-global parameters, such as line type (eg: solid, dashed, small, or fat), current color, viewport coordinates, and so on. In Micrograph, there are sixteen graphics-display registers, whose functions are summarized in table 2. Remember that these registers may be directly accessed through the instructions LREG and RREG and that they effect the execution of most of the other instructions.

There are a few other abstractions implemented by the Micrograph soft-



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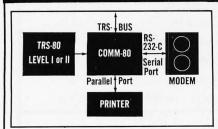
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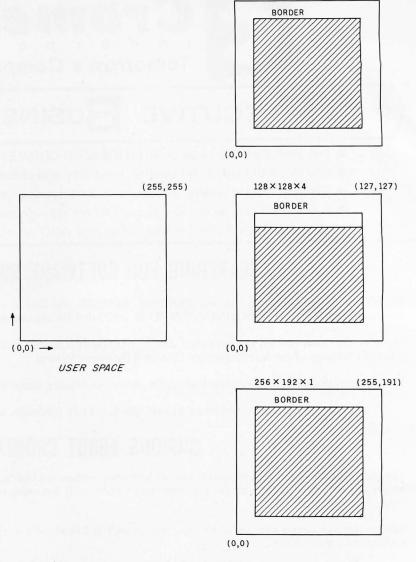


Figure 2: Pixel mapping structure of Micrograph firmware.

ware worthy of mention and mostly relating to display-list subroutine implementation, as shown in figure 3. For user-called microcomputer machine-language subroutines (accessed through CALLS), the microcomputer stack is used to handle subroutine nesting. A similar structure must be implemented for the graphics-primitive subroutines, as the figure indicates. In this case, a second stack is maintained and is pointed to by a base-register offset by another byte (GPC). This stack holds the nested graphics-subroutine names, not addresses. Another byte (SPTR) holds the current subroutine name.

To find the actual entry point of a subroutine, two more tables are used (SLINK, the subroutine address in memory, and SLONG, the subroutine length). To access the actual

address or length of a subroutine, SPTR is added to the table base for indexing the appropriate data. SLONG directly provides the subroutine length with a maximum of 256 bytes. The value in SLINK is added to SOFF, the subroutine offset, to point to the next instruction in the current subroutine.

(63,63)

64×64×4

Major Algorithms

The implementation of the Micrograph instruction set is relatively straightforward. However, there are a number of algorithms buried in the software that you should be aware of, including the algorithm for the routine PIXEL, the scan-line conversion routine, and the clipping routine. Since these routines are utilities used by several of the command-processing subroutines, they will be discussed first, followed



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Figure 3: Micrograph display-list subroutines. Figure 3a shows the stack used for nested user-called subroutines. Figure 3b shows the scheme used for keeping track of nested graphics subroutines.

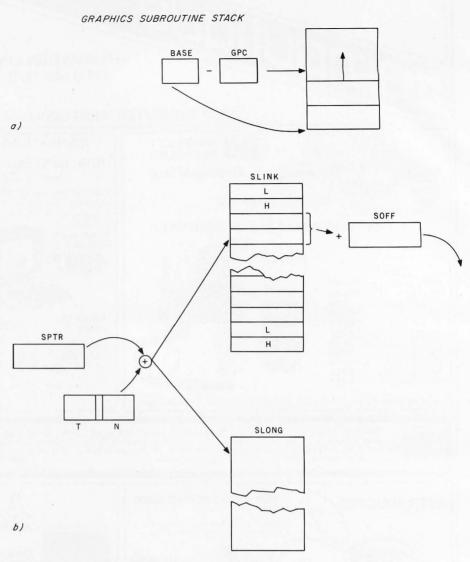
by some details on the command (ie: instruction) processing itself. Even if you don't plan to build a complete version of Micrograph, or if you have an existing graphics system, the following algorithms may easily be used to implement some important graphics-processing functions.

The routine PIXEL is the only routine that directly accesses the frame buffer: all other routines operate in the abstraction of the Cartesian plane. Hence, PIXEL must provide the mapping between these two frames of reference. Remember that the frame buffer is actually a block of memory up to 6 K bytes long. As figure 4 indicates, this block of memory is mapped directly to the display by the video-display generator. Since Micrograph supports three different formats, this mapping is not necessarily constant. Figures 5a, 5b, and 5c describe this transformation for each display resolution. These are essentially bitmanipulation operations, and because they are very similar, it will suffice to discuss one in detail, the 128 by 128 pixel (four-color) resolution in figure 5b.

PIXEL starts with clipped X and Y coordinates and, through the given bit manipulations with some moving, complementing, and shifting, forms a 16-bit offset from the start of the refresh memory. This offset is added to the start of the frame-buffer memory, which then points to a particular byte in the refresh memory. Since, in this case, there are four pixels packed in 1 byte, bits 3 and 4 of the clipped X value are used to point to one particular pixel. Since PIXEL sets or reads the color-value bits that correspond to the pixel, we must also match the byte representing the selected color to the pixel data. In this case we actually truncate the selected color and use only the top 2 bits as significant, which equates to four possible colors. Thus, there's a potential of 256 possible colors, if the hardware will support it.

Recall the description of a viewport in Part 1: a rectangular block that is part of (or the entire) display screen. Therefore, you can *clip* (ie: make in-

Text continued on page 274



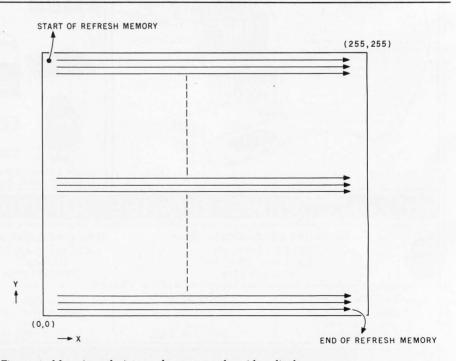
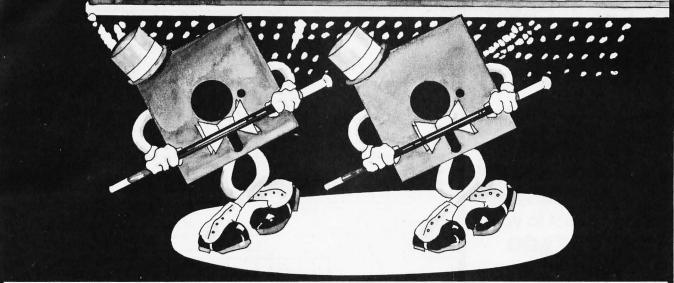


Figure 4: Mapping of picture elements to the video display.

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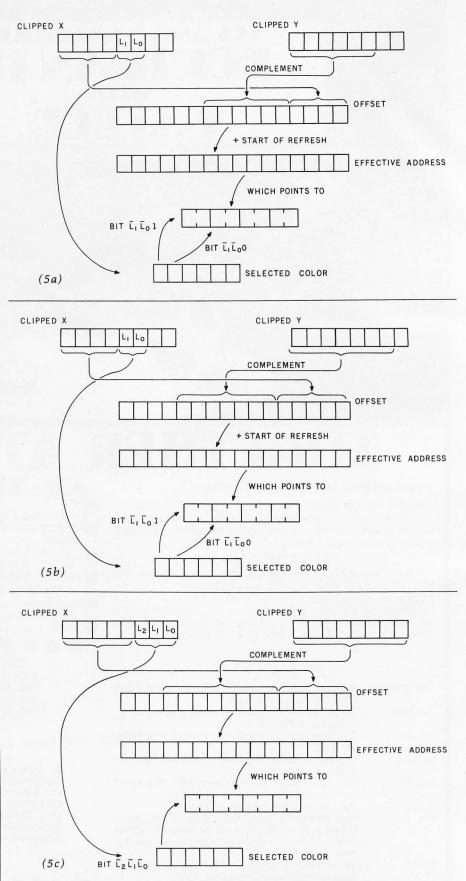


Figure 5: Variations in pixel mapping among the three available resolutions and formats. Figure 5a represents the X, Y to memory mapping for a 64 by 64 pixel by 4 color display format. Figures 5b and 5c represent mapping for 128 by 128 pixel by 4 color and 256 by 192 pixel by 2 color formats, respectively.

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Text continued from page 270:

visible) all points that are outside (or inside, in the case of masking) the viewport. This feature allows multiple images to be placed on the display

and for selective updating.

In Micrograph, four possible cases of clipping are defined, as shown in figure 6. These cases allow more flexible masking and selective updating. Of course, you must have some sort of algorithm to determine if a point should be clipped. The input parameters needed for this algorithm are the current X and Y points and the coordinates of the viewport, which we call X_1 , Y_1 , X_r , and Y_r . Figure 7 describes the algorithm for determining the case of the viewport, which is done by determining the relationship of the four viewport coordinates. Continuing, as the figure shows, you compare the current X and Y coordinates to determine where in the display they are located. Finally, to complete the clip algorithm, note what parts of the screen are not clipped relative to the case of the viewport.

There is one final algorithm that must be discussed, namely, the scanline conversion routine. This routine, actually located in routine VEC, draws a clipped line on the display given the current X and Y points as the start of the vector and the endpoint coordinates. Figure 8 provides the scan-line algorithm used by Micrograph, which computes every point along the vector. As the flowchart indicates, the routine first sets counter C to 0. This counter tallies the number of points that have been generated. Next, MM and MN are set to the maximum and the minimum, respectively, of the ΔX (delta X) and ΔY values (ie: the current X or Y value minus the respective endpoint value). These values determine whether the line is longer in the X or the Y axis. M is then set to half of the maximum value.

Next, a loop is started that first compares C and MM to verify that all the points have been plotted; if not, then M and MN are added. M is then compared to MM, to determine the necessity to increment your position in the shorter axis. Following the flowchart, the increment values for the X and Y axes are determined next. The longer axis is always incremented, and the shorter axis is only incremented if M is greater than or equal to MM. Next, the new X and Y

Text continued on page 278

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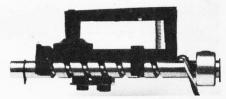
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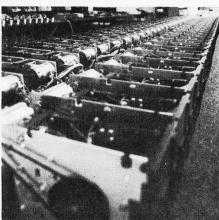
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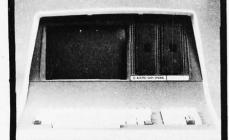
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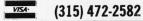
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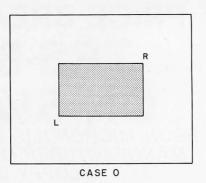
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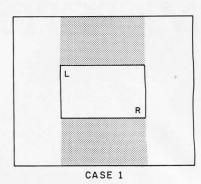
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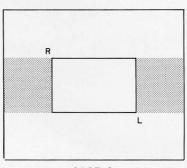
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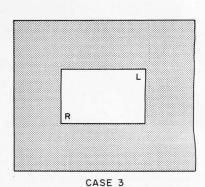










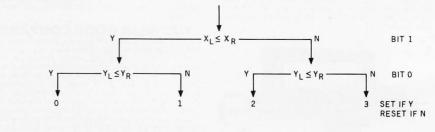


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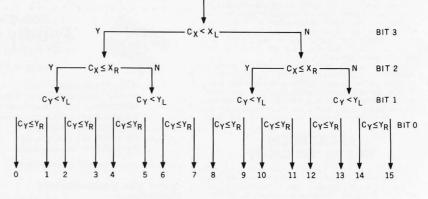
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Figure 6: Four possible cases of clipping. The L and R refer to the viewport's left and right boundaries.

CASE OF VIEWPORT



CLIP-OFF POINT



CASE OF VIEWPORT

SUCCESS (NOT CLIPPED) WITH CASE 8 OR 11

2 OR 14

Figure 7: Algorithm used for determining the clipping case of the viewport.

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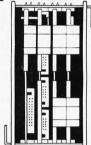
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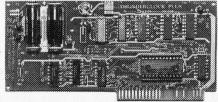
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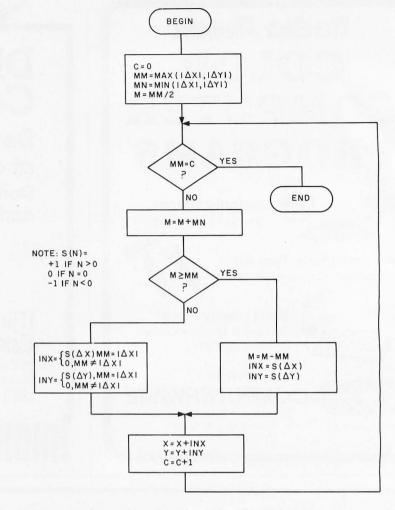


Figure 8: Micrograph scan-line algorithm. This algorithm computes the set of points along the vector to be displayed.

Text continued from page 274:

values are determined, the counter is incremented, and the point is plotted. Figure 9 provides an example of the use of this algorithm.

One final note: scan-line conversion routines are inherently slow, since they must compute *every* point along a vector. This particular routine has the advantage of requiring no division (except by 2, which can be done by shifting) or multiplication. Using a Z80 at about 2 MHz, the line is drawn faster than you can detect.

Operation

Once you have completed the Micrograph construction as in Part 2 and your software has been burned in the three EPROMs, the system is ready for use. First connect the RF (radio-frequency) or video output to your receiver. (This section should have already been checked as specified in Part 2.) Next, the input, output, and status ports must be con-

nected to your host computer. There is nothing special about this connection. Three 8-bit ports are required, plus a strobe line for each. There are no particular timing specifications for this interface. In this initial checkout, however, you can connect LEDs (light-emitting diodes) to the status and output lines, and rig the input and strobe lines. After this, Micrograph can be powered up.

First, the display will appear in the 64 by 64 pixel, four-color format, with the display area blanked. A border will also appear, and if you watch the status port, it will come up in the INIT status, followed by the FETCH status. (If you have problems here, try powering up again....I had problems with an unreliable power-up circuit.) The INIT status indicates that the system is ready to accept commands.

The protocol for communicating with the system is simple: whenever the INPUT status is low (ie: false), Micrograph is ready for data or a





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good to be true? A General Ledger which has been running in the real world long enough to shake out the bugs and has sold for many times our price of \$100.00. Too good to be true? Ok, if we have to, we can wait for the word to spread. But just to get the ball rolling—let me give you the particulars:

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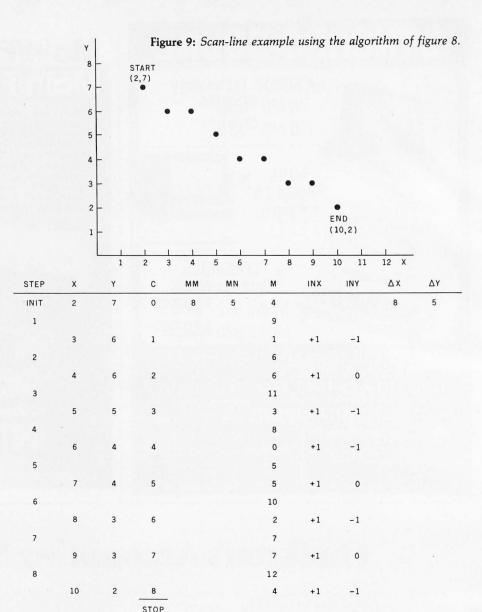
command. Remember that some of the instructions require several bytes and the host must keep track of how many bytes to send. If the INPUT is high (ie: true), then the system is busy processing, and the input is pending. If Micrograph is sending data, the OUTPUT status will be high, indicating that there is data to be received. OUTPUT will go low once the host has strobed the output port, signifying that data has been received.

Finally, the host may detect frame interrupts and error conditions. If the ERROR status bits go high, this signifies that Micrograph has detected a hardware or software failure. Diagnostics are available through the command XERR to examine memory or registers or to reset the system. Also, the formats and detailed descriptions of the commands and graphics-display registers are in the Micrograph Reference Manual (available from the author for \$20.00, postage paid). The manual provides further details on the system design and construction.

Conclusion

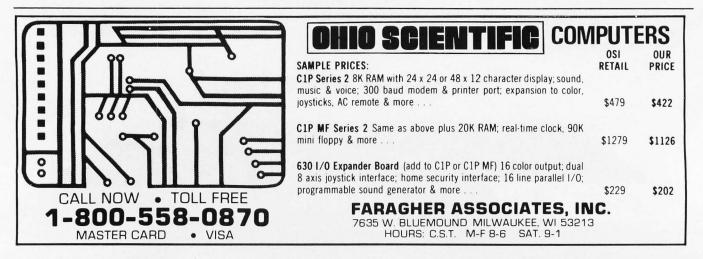
This series of articles has examined interactive computer-graphics systems, with a particular emphasis upon raster-scan graphics-display processors. I have presented an instruction set for a color raster-scan display processor for a microcomputer, called Micrograph; the hardware construction details; and the software design for the system, which provides a color graphics and alphanumerics display in any of three resolutions.

The field of computer graphics is boundless. Especially with the availability of low-cost colorgraphics systems for the consumer,



such as Micrograph, further research is needed for determining how to produce good-quality images with moderate-resolution displays, using techniques such as *ordered dithering* and *shading*. This series of articles will

enable you to achieve a low-cost color display. I hope that it has given you an understanding of some state-of-the-art graphics techniques, along with an appreciation of what remains to be studied.



of the inicorn

Mark of the Unicorn, an aggressive new software company, is currently supplying three products. Mince Scribble, and Amethyst

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here are several other useful features automatic word-wrap. "querying inclace to globally change strings," modes which allow selective rebindings of the command characters to provide special function of relatures (which may be user created and modified in Amerityst), settable tab increments and left gift margins for use with the fill paragraph and center line commands. Not to it-ention the command which deletes surrounding which uppercase lowercase or capitalize words or the command which deletes surrounding

A Ill this without the display clutter normally associated with such fancy screen editors. This is an editor that was previously available only to maxi-computer users, now provided for the real world of incrocomputing by Mark of the Unicorn

6 cribble is a text formatter that continues the philosophy of Mince. Its command set allows you to specify the logical structure of a document rather than the specific formatting details or example.

Advantages of Scribble (menumerate) simple command syntax, powerful commands, proportionally spaced output.)

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A methyst combines Mince. Scribble the BDS C compiler, source code for the command set and an extensive user support program. Inclusion of the command source code, which is written in C makes it possible for you to reconfigure the programs to create any new or personal commands you like A methyst is ideal for use as a development system editor compiler combination, or as a user changeable text processing system. Of course, you can certainly run Mince Scribble or the Amethyst package on your 48K or larger 8080-8085-280". NS16000-CP M** system with no modifications whatsoever limities and Amethyst in the control of the processing system.

A methyst. Mince, and Scribble are all written in C and will be compatible with Unix*** and any C based Unix looks, alikes offered to the microcomputing community, as well as being compatible with urrent CP M systems. A Vax-VMS version will also be available in the near future.

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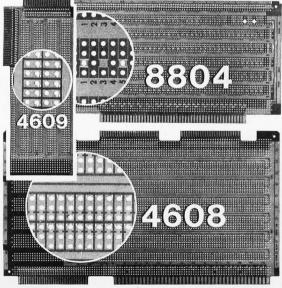
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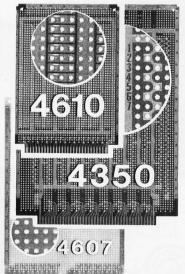
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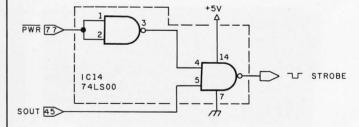
I have a Radio Shack TRS-80 microcomputer, and would like to interface your LED (light-emitting diode) display. (See "Self-Refreshing Graphics Display," October 1979 BYTE, page 58.) Can you tell me what pins I should use on the TRS-80's 40-pin Expansion Interface connector? Randy Biggs

I am glad that you want to build this device. I listed the signal names on the schematic diagram, but am happy to list the bus-signal pins as well. (See table 1.) ...Steve

Table 1

Pin Desig	nations	
Signal	Apple II	S-100
Ď7	42	90
D6	43	40
	44	39
	45	38
	46	89
		88
		35
		36
	9	83
		82
		29
	6	30
		31
	4	81
	3	80
		79
	25	1 & 51(+ 8 V) 100
	20	
OTTOBL		see figure 1
	Signal D7	D7 42 D6 43 D5 44 D4 45 D3 46 D2 47 D1 48 D0 49 A7 9 A6 8 A5 7 A4 6 A3 5 A2 4 A1 3 A0 2 +5 V GND 26

Figure 1

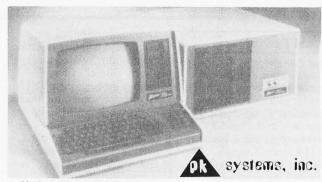


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Vocal Power

Dear Steve,

I need your expertise in circuit design once again. I recently interfaced a voice synthesizer to my Heath H-8 computer, and I need a power supply for it because the H-8 doesn't supply enough current for both itself and the synthesizer.

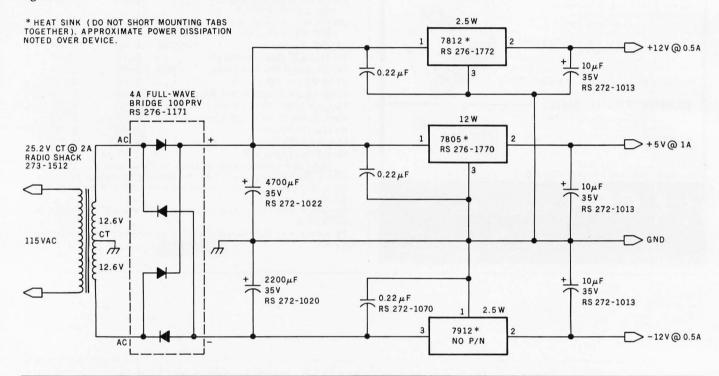
The power supply I am using now is my own crude design, unregulated and poorly filtered. I have looked through past BYTE articles for something that might work, and I have found nothing. Could you be of help, Steve? What I need is ±12 V at 500 mA and +5 V at 350 mA. There is very little "surge" demand. The ±12 V should be within 10% and regulated, the +5 V within 5%, also regulated.

Ted G Benglen II

Figure 2 is a schematic diagram for the power supply you describe. If you have any more questions on seat-of-the-pants seriesregulated power-supply

design, I recommend you read my new book entitled Build Your Own Z80 Computer, which will be available from BYTE Books (70 Main St, Peterborough NH 03458) in February. 1981. There is a complete chapter devoted to this subject....Steve

Figure 2



EMG + TRS-80 = ??

Dear Steve,

I am currently using a TRS-80 Level II 16 K microcomputer in my classroom. I am a Special Education specialist who teaches 7th and 8th grade learning-disabled students. I am trying to put together a program using stress-free learning techniques. What I would like to do is interface an EMG (electromyogram) unit to the TRS-80. Your name was given to me as a possible resource. I would appreciate any assistance that you could provide. William Engelhardt

It is not particularly difficult to connect the single-bit output of the EMG unit from my article "Mind

Over Matter: Add Biofeedback Input to Your Computer" (June 1979 BYTE, page 49) to a TRS-80, if you have the Radio Shack Expansion Interface or a COMM-80. Either unit provides a printer port at memory address hexadecimal 37E8.

The easiest method is to attach the EMG output to pin 21 of the printer connector (ground is on pin 34). This is ordinarily used as the printer BUSY line. Pins 23, 25, 28, 29, 19, 32, and 30 should be grounded. In BASIC, execute a PEEK(14312) when you want to read the EMG input. If it returns as decimal 128, then the EMG output is high; if it returns 0, then its output is low.

If you would prefer not to

go through the expense of the expansion interfaces for a single-bit input, then I refer you to my May 1980 BYTE article (see "I/O Expansion for the Radio Shack TRS-80, Part 1: Principles of Parallel Ports," page 22), which describes how to construct a parallel port for any address....Steve

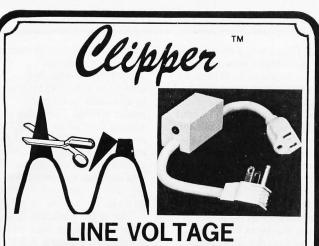
SDK-86 Inquiries

Dear Steve,

I am a subscriber to BYTE, and I have enjoyed reading your articles for over two years. Your articles have increased my knowledge of digital circuitry and microcomputers. Thus, one purpose of this letter is to thank you for your effort. Although I constantly read articles in BYTE and other technical magazines, I am only now thinking of assembling my own computer. Perhaps you could answer some of my questions:

In your article on the Intel SDK-86 computer kit (see "The Intel 8086," November 1979 BYTE, page 14), the data-rate generator is fed by a 612,500 Hz clock. It seems to me that the 8-bit counter (a 74LS393) would divide this by 256 to produce a minimum rate of over 2 kHz. Where does the 110 bps (bit per second) rate come from?

I am considering the purchase of an Intel SDK-85 kit and a Heathkit H-19 (smart video terminal). I believe that they will be compatible; how hard can the interfacing



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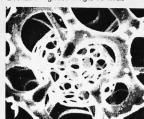
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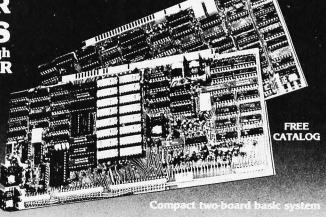






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be? Since the serial I/O (in-put/output) port of the SDK-85 runs at 110 bps, it seems that the initial loading of the H-19 may take as long as 3 minutes. What is the best way to interface a printer to the computer at the same time?

I am interested in obtaining BASIC firmware; I have seen advertisements for BASIC stored in ROM (read-only memory), but it seems that it may be written for a specific computer system, rather than the 8085 microprocessor in general. Can I get firmware compatible with the SDK-85 computer that will handle I/O? Is the performance increase of the SDK-86 over the SDK-85 really worth \$550? Chin Y Chang

Thank you for the vote of confidence. I'll try to answer your questions in order:

On the SDK-86 computer, the data-rate generator is fed by a 1.8432 MHz clock. The 74LS393 and other circuitry reduce this to approximately 1760 Hz (actually a bit higher) to provide 110 bps. This unit can go as high as 4800 bps, with the change of a few jumpers.

The H-19 and SDK-85 could communicate serially. Provision is made on the SDK-85 board for the addition of an MC1488 and an MC1489 (quad line driver and quad line receiver, respectively) for RS-232 operation. Since the only data rate is 110 bps, things will indeed be slow, unless

you write your own I/O routines. Interfacing to a printer requires knowledge of the printer's specifications. If it communicates serially, then a switch would allow you to use the printer in place of the video monitor quite easily. Selection of the best printer for interfacing is dependent upon your programming abilities.

Lawrence Livermore BASIC is available in read-only memory from a few manufacturers (such as National Semiconductor). Call National's local sales offices for details. The memory devices contain only the BASIC interpreter, but no I/O routines; compatibility with the SDK-85 system will, again, depend on your abilities.

The SDK-86 is not aimed at the experimenter market. While you may benefit in the long run, your questions suggest that you might be biting off a little too much. If you want a 16-bit computer, save the \$1000 cost of an SDK-86 kit and put it toward an assembled system...Steve

Questions, Questions, Questions

Dear Steve,

I have a couple of questions regarding your article "I/O Expansion for the Radio Shack TRS-80, Part 1." (See the May 1980 BYTE, page 22.) It appears

that figure 7 is a diagram of the prototype board pictured in photo 3. Where do the capacitors come in? And what are their values?

I know just enough about electronics to get myself into trouble. I know what the components are and how they work, but I don't know how to match them up into a working circuit.

Also, could you furnish more information about using the extra logic on IC5 to operate the three additional ports? I am particularly interested in a combination security system and external-device control and monitor. I don't think 8 bits is enough for what I have in mind.

I have done some figuring on the additional ports. It appears to me that, for each additional port, I will need (to decode the port address) one 74LS04, one 74LS30, and one 14-pin DIP switch. For input capabilities, I would need two 74LS125s and two 74LS75s.

Since there are four inverters unused on IC7, three could be used with the latches for the three other ports.

Kerry A Wilson

You are correct. Figure 7 is the circuit of photo 3. The extra capacitors are for decoupling and protective filtering. These components are added because they are a good idea and not because they are necessary for the port function described. Whenever TTL (transistor-

transistor logic) components are used in a design, capacitors are attached across the power-supply pins to eliminate noise in the power wiring. The value is usually 0.01 µF to 0.1 µF, and one should be added for every three integrated circuits (this figure is variable and depends on circuit density and power consumption as well).

The larger capacitor is a 10 uF electrolytic type which is attached between +5 V and ground where the power enters the board. Whenever an interface is remotely powered, it is possible that the wires attaching it to the power source will pick up noise. Adding a capacitor at the end of the power cable helps reduce this noise. The exact value is a function of cable impedances and circuit reactance, but, in low-current circuits, 10 μF to 100 μF is acceptable. High-quality designs may be a little more particular, and tantalum electrolytics are generally used.

The additional logic necessary to expand figure 7 for three more ports would be six 74LS125s, six 74LS75s, and three of the remaining inverter sections of IC7. For each port, you would duplicate the circuit of ICs 1, 2, 3, 4, and 7a; however, use the other strobe lines on IC5, the 74LS155. Those lines are described in detail in the second part of my article. (See the June 1980 BYTE, page



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42.) The addressing for the other ports is already decoded in the original circuit. As the switches are shown, the first port is 00. The other three will be 01, 02, and 03 respectively.

Be careful to keep your wiring short and neat because this circuit is attached to the main computer bus. If the computer malfunctions, then you may need to add extra buffers to the data and I/O buses. ...Steve

Transmission-Transmission Logic?

Dear Steve,

I have been interested in monitoring my car's gas mileage for several years, but until recently I have been prevented from doing anything about it because there was no inexpensive way for me to measure the low fuel-flow rate in a car. Now a fuel-flow sensor is available from Zemco Inc. 12907 Alcosta Blvd, San Ramon CA 94583, for \$19. They sell The Compucruise and any replacement parts for the unit at reasonable prices. A speed sensor and magnet-replacement kit are also available for \$4.50 and \$15, respectively, but my odometer (I have a 1974 Toyota Celica) sends a marker pulse to an emissions-control device, which I can use.

I designed the circuit shown in figure 3 to display miles per gallon. The circuit is simple, and though it does not contain a microprocessor, it could be connected to a computer for more sophisticated analysis. It comprises two signal conditioners to convert the outputs of the speedometer and the fuelflow sensor to TTL levels, a divide-by-N counter to count fuel pulses, and a 3-digit latching counter and display to count odometer pulses. A pair of one-shots (monostable multivibrators) are used to latch and then clear the display.

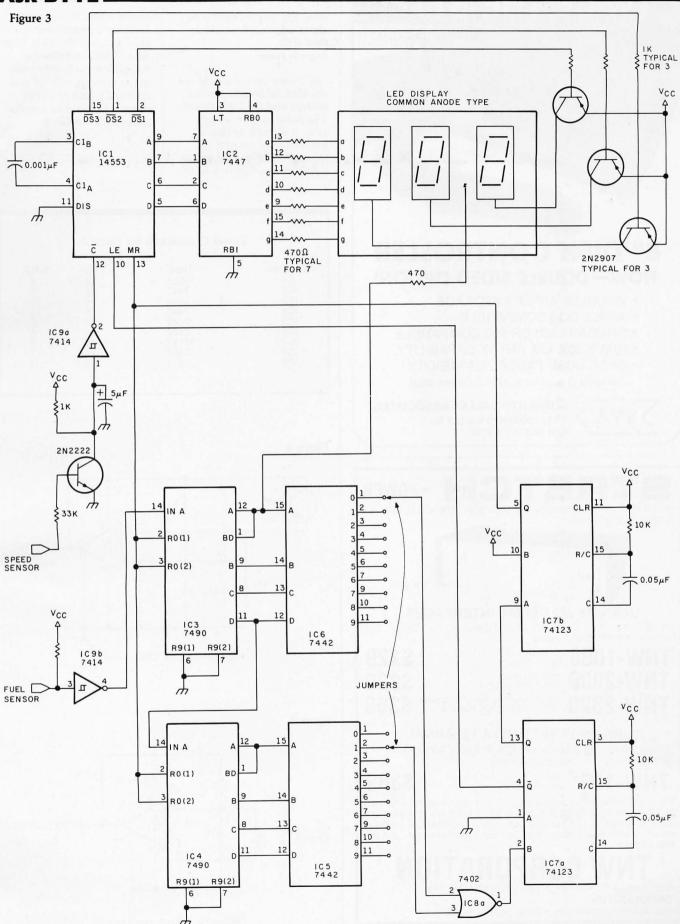
My odometer sends 376 pulses per tenth of a mile. I do not know how the pulses are created inside the speedometer case, but, with an oscilloscope and a resistor-substitution box, I determined that the pulse train switches between 0 and 5 V with a 50% duty cycle and has a 1 k-ohm impedance.

In the fuel sensor, a rotating vane interrupts a light beam from a 12 V bulb to a phototransistor 3730 times per gallon.

Dividing 3730 by 376 gives 9.92 (ie: roughly 10), so if I count 10 pulses from the flow sensor with the divide-by-N counter and then display the count from the odometer, it will read tenths of a mile per gallon. This reading is converted to mpg (miles per gallon) by shifting the decimal point left one place. Two 7490 decade counters, two 7442 BCD-to-decimal decoders, and a NOR gate make up the divide-by-N counter where N can be any number from 0 to 99 by moving the inputs to the NOR gate to the appropriate pins on the 7442s. As an extra, I tied the decimal point to the leastsignificant bit of the flow counter so that the decimal point blinks as the fuel flows. On the highway, the decimal point blinks about once per second and the mpg reading is updated about every five seconds. The readout can be converted to display miles per hour by switching the input to the first one-shot from the divide-by-N counter to a 555 timer with a 9.6-second period.

My question concerns the interfaces from the sensors to the TTL. The two interface circuits I show on the schematic were designed by trial and error because transistors are a mystery to me (I used the 2N2222 because it is ubiquitous). The buffer from the odometer seems to work well enough, but I occasionally get erratic readings from the flow sensor, which is mounted to the car body near the distributor and ignition coil. Should I be using shielded cable or provide filtering before feeding the signal to the Schmitt trigger? If you can

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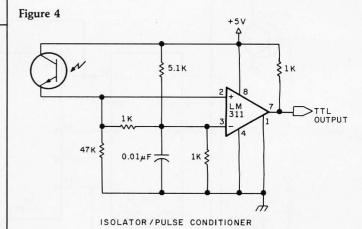
offer any improvements to either interface I would appreciate it.

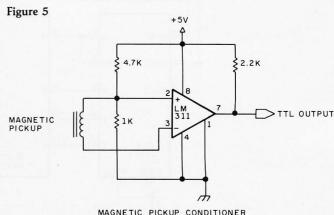
Roger H James

If I were you, I would use shielded cable between the sensors and the logic board. The pulse output, as you said, is a result of the gasoline flow causing the wheel to spin and interrupt a light beam. Figure 4 is a

circuit which more readily conditions phototransistor pulse outputs. It might help. Also, I have provided a magnetic-transducer conditioner (see figure 5), if you eventually care to use a magnetic pickup to acquire speed data....Steve ■

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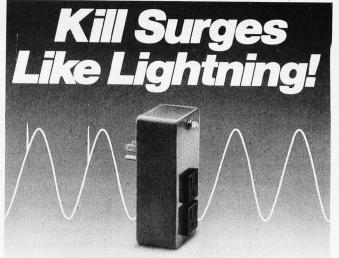
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BYTE's Bits

Clarifications to TRS-80 **ROM Article**

After reading Terry Li's article in the October 1980 BYTE ("Radio Shack's Modifications to the TRS-80," page 182), I feel I must make a few comments.

Adding lowercase to the TRS-80 Model I is not done by adding or changing a ROM (read-only memory). In an unmodified TRS-80, seven programmable memory integrated circuits are used for the video display. When the lowercase modification is performed, an eighth programmable memory device is added for bit 6, which indicates upper- or lowercase characters.

In some cases, a new character generator ROM is added because earlier model TRS-80s had character generators that did not give good lowercase characters.

To use lowercase, the Level II BASIC ROMs must be upgraded. The INKEY\$ problem seems to indicate that this is done when the lowercase modification is installed.

LPRINTing a character after PEEKing it from video memory is possible. A

simple BASIC statement can check to see if the character is in the valid range for the printer. If it is not, another statement can change the ASCII (American Standard Code for Information Interchange) value to a valid one.

The new Level II BASIC ROMs do not have a smaller capacity (less bytes of memory). Some changes have been made that consumed some of the memory space originally used by the messages "RADIO SHACK LEVEL II BASIC" and "MEMORY SIZE". The entry points for all I/O (input/output) routines are unchanged, so most of the present TRS-80 software will work. Also, no routines have been eliminated.

With the old Level II BASIC ROMs, the shiftdown-arrow gives control characters when other keys are pressed with it simultaneously. However, the value 26 is generated first. When the shift-down-arrow key is not released, then pressing other keys generates the control values (eg: 01 for "A"). Most software that uses the control value feature of the TRS-80 neglects the value 26. Any

of this software, however, should work with the new Level II BASIC ROMs.

In regard to using the Electric Pencil with the TRS-80, a number of publications have presented information on how to use the Electric Pencil with the Radio Shack lowercase modification. Some commercial software is also available for modifying the Electric Pencil.

Thomas de Man Voszegge 7 2318 ZI Leiden Holland

Sources at Radio Shack told me that all points made in this letter are essentially correct. However, Radio Shack would like a few points clarified: When the lowercase modification is performed by Radio Shack, the character generator ROM is often replaced because early Model I TRS-80s had character generators that had lowercase characters without descenders that fell below the line (eg: "y," "g," and "p"). The new ROM gives these letters true descenders, thus making these letters much easier to read.

The new Level II BASIC

ROMs use the same amount of memory as did their predecessors. Radio Shack has modified some code to correct keyboard bounce and cassette loading problems, and some new code has been added. Radio Shack stresses that all the original routines are still contained in the ROMs and the entry points for all published routines remain unchanged....SM

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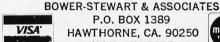
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Software Received

Data Master. Accessory package to Information Master (see separate listing) for the Apple II. Floppy disk, \$100. High Technology, POB 14665, Oklahoma City OK 73113.

The Pascal Data Base. Data base for the Apple II. Floppy disk, \$400. Arizona Computer Systems Inc, POB 125, Jerome AZ 86331.

Information Master. Data base for the Apple II. Floppy disk, \$150. High Technology, POB 14665, Oklahoma City OK 73113.

(T.(L.C))-LISP. Version of LISP programming language for CP/M computers. Cassette, \$150. The LISP Company, POB 487, Redwood Estates CA 95044.

Linear Circuit Analysis Program. Electronics analysis program for the PET/CBM. Cassette, price not available. Commodore Business Machines (UK) Limited, 818 Leigh Rd Trading Estate, Slough Berks, England.

Single Disk Sort Version 2.0. Disk-sort utility for the Apple II. Floppy disk, \$49.95. Datacope, 5706A W 12th St, PO Drawer AA, Hillcrest Sta, Little Rock AR 72205.

Text File Copy. Wordprocessing utility for the Apple II. Floppy disk, \$49.95. Datacope, 5706A W 12th St, PO Drawer AA Hillcrest Sta, Little Rock AR 72205.

The Datacope Scribe. Word processor for the Apple II. Floppy disk, \$79.95. Datacope, 5706A W 12th St, PO Drawer AA, Hillcrest Sta, Little Rock AR 72205.

Microcomputer-Aided Design of Active Filters. Electronics analysis program for the Apple II. Cassette, \$16.95. Hayden Book Company Inc, 50 Essex St, Rochelle Park NJ 07662.

Super Nova. Graphics game for the TRS-80. Cassette, \$14.95. Big Five Software Company, POB 9078-185, Van Nuys CA 91409.

Up Periscope. War game for the TRS-80. Cassette, \$14.95. Ramware, 6 South

St, Milford NH 03055.

Warpath. War game for the TRS-80. Cassette, \$14.95. Ramware, 6 South St, Milford NH 03055.

Disk-O-Tape. Utility program for the Apple II. Cassette, \$12. Dann McCreary, POB 16435, San Diego CA 92116.

Asteroids in Space. Graphics game for the Apple II. Floppy disk, \$19.95. Quality Software, 6660 Reseda Blvd, Suite 105, Reseda CA 91335.

Monty Plays Monopoly. Computer-opponent program for the Apple II. Floppy disk, \$34.95. Personal Software Inc, 1330 Bordeaux Dr, Sunnyvale CA 94086.

The Voice. Utility program for the Apple II. Floppy disk, \$39.95. Muse Software, 330 N Charles St, Baltimore MD 21201.

Interactive Fiction: Six Micro Stories. Role-playing game for the TRS-80. Floppy disk, \$14.95. Adventure International, POB 3435, Longwood FL 32750.

Pascal/Z Version 3.0. Version of Pascal programming language. Eight-inch floppy disk, \$395. Ithaca Intersystems Inc, 1650 Hanshaw Rd, POB 91, Ithaca NY 14850.

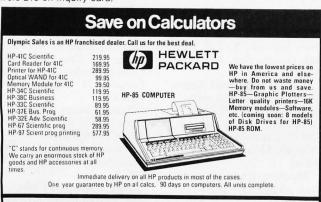
Adaptable UCSD Pascal System for CP/M. Version of UCSD Pascal programming language for CP/M systems. Eight-inch floppy disk, \$350. Softech Microsystems, 9494 Black Mountain Rd, San Diego CA 92126.

Asteroid. Graphics game for the Apple II. Floppy disk, \$19.95. Adventure International, POB 3435, Longwood FL 32750, (305) 682-6917.

EMU 02. 6502 machinelanguage emulator for the TRS-80. Cassette, \$24.95. Allen Gelder and Company, POB 11721, Main PO, San Francisco CA 94101.

Super Step. Single-step routine for Z80 machine language on the TRS-80. Cassette, price not available. Allen Gelder and Company, POB 11721, Main PO, San Francisco CA 94101.

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Software Received_

utility to relocate Radio Shack T-BUG software. Cassette, \$9.95. Allen Gelder and Company, POB 11721, Main PO, San Francisco CA 94101

Enhanced Paper Tiger Graphics Software. Highresolution image printer for the Apple II. Floppy disk, \$44.95. Computer Station, 12 Crossroads Ctr, Granite City IL 62040.

Visilist. Utility program for VisiCalc and the Apple II. Floppy disk, \$19.95. Computer Station, 12 Crossroads Ctr, Granite City IL 62040.

Mailing List. Mailing list software for Heathkit/ Zenith computers. Floppy disk, \$49.95. Hayden Book Company, 50 Essex St, Rochelle Park NJ 07662.

Programming in Apple Integer BASIC. Tutorial software. Floppy disk, \$39.95. Hayden Book Company, 50 Essex St, Rochelle Park NJ 07662.

Conflict. War game for the Apple II. Cassette, price not available. Keating Computer Services Pty Ltd, POB 448, Double Bay, Australia 2028.

Indexed Sequential Access Method. ISAM disk software for the PET/CBM computers. Floppy disk, \$99.95. Creative Software, POB 40, Mountain View CA 94040.

Mychess. Chess program with graphics for the TRS-80. Floppy disk, \$50. Computer Services, 2431 Lyvona, Anchorage AK 99502.

Helicopter Battle.

Graphics game for the Atari 400 or 800. Cassette, \$9.95. Custom Electronics Inc, 238 Exchange St, Chicopee MA 01013.

Tractor Beam. Graphics game for the Atari 400 or 800. Cassette, \$9.95. Custom Electronics Inc, 238 Exchange St, Chicopee MA 01013.

Disk Cataloger. Diskutility program for the TRS-80. Cassette, \$16.95. Hayden Book Company, 50 Essex St, Rochelle Park NJ 07662.

Energy Miser. Energy-use estimation utility. Cassette, \$19.95. Hayden Book Company, 50 Essex St, Rochelle Park NJ 07662.

Chem Lab Simulations 1 and 2. Tutorial simulation programs for the Apple II. Floppy disk, \$99.95 each. High Technology, POB 14665, Oklahoma City OK 73113.

Infinite BASIC. BASIC-language utility for the TRS-80. Floppy disk, \$49.95. Racet Computes, 702 Palmdale, Orange CA 92665.

Infinite Business. Extension to Infinite BASIC (see separate listing). Floppy disk, \$29.95. Racet Computes, 702 Palmdale, Orange CA 92665.■

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BYTE's Bugs

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The program for "Lost Dutchman's Gold," by Bob Liddil (December 1980 BYTE, page 268) was translated from the Radio Shack TRS-80 to the Apple II by Jamie Tietjen.

Moore's Number

The October 1980 BYTE contained an error on page 347 in the "What's New" section. The phone number for Moore Business Forms Inc should read (800) 323-8325. We are sorry for the inconvenience this has caused. ■

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Books Received

The following is a list of books received at BYTE Publications during this past month. Although the list is not meant to be exhaustive, its purpose is to acquaint BYTE readers with recently published titles in computer science and related fields. We regret that we cannot review or comment on all the books we receive; instead, this list is meant to be a monthly acknowledgment of these books and the publishers who sent them.

The Art of Electronics. Horowitz and Hill. New York: Cambridge University Press, 1980. 17.7 by 25.1 cm (71/4 by 101/4 inches), 716 pages, hardcover, ISBN 0-521-23151-5, \$24.95.

Computer Programming in the BASIC Language. Neal Golden, New York: Harcourt, Brace, Jovanovich Inc, 1981. 15.3 by 22.6 cm (6¹/₄ by 9¹/₄ inches), 312 pages, hardcover, ISBN 0-15-359090-4, \$7.50.

Computer Security, A Management Audit Approach. Norman L Enger and Paul W Howerton. New York: AMACOM, 1980. 15.3 by 22.6 cm (61/4 by 91/4 inches), 264 pages, hardcover, ISBN 0-8144-5582-4, \$21.95.

Data Base: Structured Techniques for Design, Performance, and Management. S Atre. Somerset NJ: John Wiley & Sons, 1980. 15.3 by 22.6 cm (61/4 by 91/4 inches). 442 pages, hardcover, ISBN 0-471-05267-1, \$27.95.

Electrical Wiring Handbook. Edward L Safford. Blue Ridge Summit PA: Tab Books Inc, 1980. 12.5 by 20.2 cm (51/8 by 81/4 inches), 432 pages, softcover, ISBN 0-8306-1245-9, \$8.95; hardcover, ISBN 0-8306-9932-5, \$15.95.

Handbook of Microprocessor Applications. John A Kuecken. Blue Ridge Summit PA: Tab Books Inc. 1980. 12.5 by 20.2 cm (51/8 by 81/4 inches), 308 pages, softcover, ISBN 0-8306-1203-3, \$8.95; hardcover, ISBN 0-8306-9935-X, \$14.95.

Pascal, David L Heiserman. Blue Ridge Summit PA: Tab Books Inc, 1980. 12.5 by 20.2 cm (51/8 by 81/4 inches), 350 pages, softcover, ISBN 0-8306-1205-X, \$9.95; hardcover, ISBN 0-8306-9934-1, \$15.95.

Principles of Firmware Engineering in Microprogram Control. Michael Andrews. Potomac MD: Computer Press Inc. 1980. 15.3 by 22.6 cm (61/4 by 91/4 inches), 347 pages, hardcover, ISBN 0-914894-63-3, \$21.95.

Programming in BASIC for Personal Computers. David L Heiserman, Englewood Cliffs NJ: Prentice-Hall Inc, 1981. 15.3 by 22.6 cm (61/4 by 91/4 inches), 333 pages, softcover, ISBN 0-13-730739-X, \$7.95; hardcover, ISBN 0-13-730747-0, \$17.95.

A Reference Guide to Practical Electronics. Robert G Krieger Sr. New York: McGraw-Hill Book Company Inc, 1981. 13.1 by 20 cm (5% by 8 inches), 212 pages, softcover, ISBN 0-07-0345492-8, \$7.50.

6502 Software Design. Leo J Scanlon. Indianapolis IN: Howard W Sams Company Inc, 1980. 13.1 by 20.8 cm (5% by 81/2 inches), 270 pages, softcover, ISBN 0-672-21656-6, \$10.50.

Z8000 Assembly Language Programming. Leventhal, Osborne, Collins. Berkeley CA: Osborne/McGraw-Hill, 1980. 15.9 by 22.6 cm (6½ by 9¼ inches), 604 pages; softcover, ISBN 0-931988-36-5, \$19.99. ■

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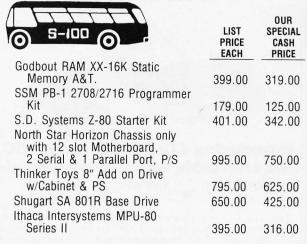
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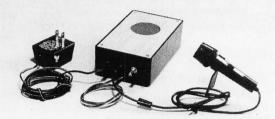
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Book Reviews

How To Program Your Programmable Calculator

by Dr Stephen L Snover and Dr Mark A Spikell, Prentice-Hall Inc, Englewood Cliffs NJ, 1979, 271 pages, softcover, \$7.95

Reviewed by Richard Keck Rte 1 Neoga IL 62447

How To Program Your Programmable Calculator is a very versatile book, with many examples from simple straightline programs to complex decision-making loop programs for calculus. The book has two sections: one for the TI-57 and EC-4000 calculators, and the other for the HP-33E. Examples and presentation are identical with the exception of different keystrokes for the different sections.

The book can also be used as an aid in deciding which calculator to buy. Using the book does not require a programmable calcu-

Due to the large number of examples and explanations, this book should be useful in a classroom environment. Since it has over 100 problems, as well as answers, it can easily be used as an introduction to programming or as a miniunit on the use of programmable calculators in the classroom.

The book is specifically designed for the less expensive programmable calculators. However, as a TI-58 owner, I believe its usefulness as a reference manual for subroutines is reason enough for even experienced calculator programmers to purchase it. Whether you are new to programmable calculators or an old pro, How To Program Your Programmable Calculator is a valuable addition to your library of programs and books.

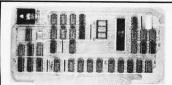
Structured Pascal

by Jean-Paul Tremblay, Richard B Bunt, and Lyle M Ospeth, McGraw-Hill Book Company, Hightstown NJ, 1980, \$10.95

Reviewed by Peter Grogono 4125 Beaconsfield Ave Montreal, Quebec H4A 2H4 Canada

Structured Pascal is a textbook for a first course in a computer-science curriculum at the university level. It is a supplement to An Introduction to Computer Science: An Algorithmic Approach by the same authors, but can be used independently. It is a bulky book, measuring 81/2 by 11 inches, and although it contains more than 400 pages, there are no diagrams. Although primarily intended as a language manual. Structured Pascal is also concerned with programming style and contains many example programs. These programs are more varied than those customarily found in introductory texts, and each is presented in the form of a complete listing with examples of input and output, not as a collection of fragments. The range of applications is wide. In addition to programs that implement standard algorithms such as sorting, searching, Gaussian elimination, and numerical integration, there are programs which compute parimutuel payoffs and mortgage payments, and which process hockey-league results, transpose musical scores, and add polynomials. The book is fairly well organized, but there are some anomalies. For example, the Pascal CASE statement is described in a chapter entitled "Advanced String Processing."

It is unfortunate that a book that attempts to do so much should be so flawed.



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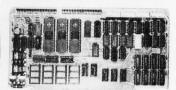
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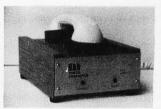
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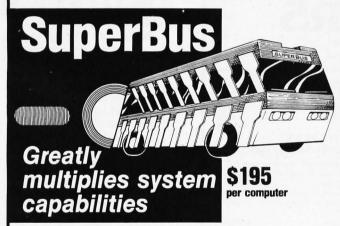
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MasterCard, VISA and C.O.D. orders accepted. Specify N or B keyboard when ordering SuperBus. Dealer inquiries invited. Visicalc is a trademark of Personal Software, Inc.; WordPro, of Professional Software, Inc.; and CMS, of CMS Software, Inc. Some of the flaws are minor; they seem to be due to the fact that this book, like so many recent texts, is a set of lecture notes prepared for publication. The choice of the programming language used for the examples (a local dialect of Pascal called Manitoba Pascal) seems to be the cause of some major problems

There are two differences between standard Pascal and Manitoba Pascal that have a major impact on the value of the book to the evergrowing Pascal community. The first difference is that Manitoba Pascal provides slightly more flexibility in string processing than does standard Pascal, Stringhandling capabilities are used extensively in the examples, and two chapters are devoted almost entirely to "strings and things." The examples make frequent use of a predefined set of somewhat inefficient and inflexible string-handling procedures and functions. Consequently, they are not really Pascal programs at all; they are programs in a primitive string-processing language that happens to have been embedded in Pascal. The problem here, and in other sections of this book, is that Pascal is treated as a poor man's PL/I, and is not allowed to stand on its own.

The second difference between Manitoba Pascal and standard Pascal is minor, but it has had a serious effect on the book. Students at the University of Saskatchewan punch their programs on cards, and keypunch machines do not have keys for square brackets. Consequently, where standard Pascal has '[...]', Manitoba Pascal has '(...)'. Computer users of 1980 are inconvenienced by the technology of 1890. In Pascal, '(A,B,C,D)' is an enumerated-type descriptor, and '[A.B.C.D]' is a set constant. Enumerated types are an abstraction of the constant identifiers frequently used in assembly-language programs to represent a small number of states or choices, and sets are an abstraction of bit-strings. They are among the innovations of Pascal that are particularly notable for their expressive power. Yet, in Structured Pascal these two useful constructions are hopelessly confused. On page 11, we are shown an enumerated-type declaration and told that it is a "set"; furthermore, we are incorrectly told that "set operations" may be applied to enumerated types, but we are told neither here nor elsewhere how these set operations are represented in Pascal. Later, on page 255, we are told, "Pascal does not have bit-strings." It is not surprising that the example programs make use of neither set types nor enu-

merated types; in fact, the programs hardly employ user-defined types at all.

Is this just a question of style? Does it really matter if some people use more type declarations than others in their Pascal programs? My own view is that it does matter. The lesson of the Sixties was that programming languages must be more expressive, not just more powerful. This is what structured programming and data abstraction are all about. In Structured Pascal (note the title!), structured programming is defined in one sentence on page 4: "Structured programming is really little more than the application of a particular discipline to the practice of programming." This is the attitude of people who "go on a diet" rather than eat nutritious food regularly. It is more than a question of style when a textbook that professes to describe a programming language entirely omits the most expressive features of that language.

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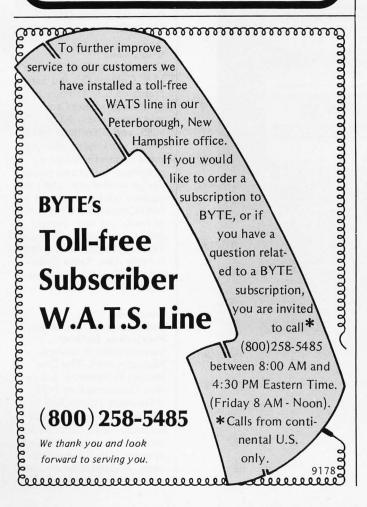
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January

Courses from Zilog, Boston MA and Cupertino CA. Three- to 5-day courses on microprocessors, the Z8000 and Z8 circuits, programming, and architecture are being offered by Zilog, Training and Education,

10340 Bubb Rd, Cupertino CA 95014, (408) 446-4666, Attn: Kathy Trappen. Contact Zilog for a complete schedule of places and times.

January-February

Courses from George Washington University, Washington DC. Computerperformance evaluation, communication systems and networks, microcomputers in control systems, structured programming, and protection of computer assets are some of the areas of study being presented in these courses from George Washington University. Contact the Director, Continuing Engineering Education, George Washington University, Washington DC 20052, (800) 424-9773.

January-February

Data Processing Courses, Houston and Dallas TX, and London, England. Dataprocessing operations management and fundamentals of data processing for executives are the courses offered by the University of Chicago. For schedules of times, and additional information, contact the University of Chicago, Center for Continuing Education, MC Seminar Division, 1307 E 60th St, Chicago IL 60637, (800) 223-7450. In New York state, call collect (212) 953-9022.

January-March

Courses from Intel, Boston MA, Chicago IL, and San Francisco CA. Introductions to microprocessors and microcomputers; 8080/ 8085, and 8086/8088 system design workshops; development systems workshops: peripheral integrated-circuitdesign workshops; and other courses are being offered by Intel Corporation. For a list of times and fees, contact Intel Corporation, Customer Training Department, 3065 Bowers Ave, Santa Clara CA 95051, Attn: Registrar-MS SV3-1.

January-June

Seminars from Worcester Polytechnic Institute, various cities in eastern Massachusetts. The Continuing Professional Education Department of WPI (Worcester Polytechnic Institute) is presenting 2-day seminars on fundamentals of data processing, distributed systems, data communications, microprocessors, and other computer-related



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January 7-9

The Fourteenth International Symposium on Minicomputers and Microcomputers, Hotel del Coronado, San Diego CA. The scope of the symposium will cover technology, hardware, software, engineering, languages, sys-

tems architecture, operating systems, numerical methods, computer networks, and other aspects of computing. Contact the Secretary, MIMI '81 San Diego, POB 2481, Anaheim CA 92804.

January 8-11

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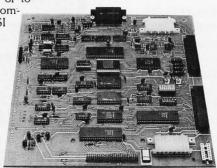
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Bridgeport CT. Seminar topics will include educational software for elementary schools, mathematics curricula, and sciences; simulations; computer education; computer-aided instruction in foreign languages; and more. Contact Monica Kantrowitz, President, Queue, 5 Chapel Hill Dr, Fairfield CT 06432.

January 17-19

Machine Othello Tournament, University of California, Santa Cruz CA. This 2-day tournament is open to individuals and teams that register by January 10. The tournament consists of eight rounds of play, with each contestant allotted 30 minutes per game. To register, send your name, program designation, and equipment description to Professor Peter W Frey, 421 Kerr Hall, University of California, Santa Cruz CA 95064, (408) 429-4005.

January 17-23

The First Annual Alpha

Micro User's Society Convention, Deauville Hotel, Miami Beach FL. Seminars; conferences; demonstrations; meetings for businessmen, programmers, and analysts are being featured. The convention is strictly for Alpha Micro users. Contact William L Miller & Associates, 8380 SW 151 St, Miami FL 33158, (305) 233-1216.

January 27-29

Advanced Semiconductor Equipment Exposition, San Jose Convention Center, San Jose CA. Over 100 exhibitors will feature equipment at this trade show. The show's emphasis is on new products and emerging technology in the semiconductor processing and production fields. Contact Cartlidge & Associates, 491 Macara Ave, Suite 1014, Sunnyvale CA 94086, (408) 245-6870.

January 28-31

The Third IMMM/Data Comm International Japan Exposition, Harumi Exposition Center, South Hall, Tokyo, Japan. Over 15,000 scientists, design engineers, technical managers, applications engineers, and other specialists are expected to attend this show. The Internepcon Japan/Semiconductor International conference is held concurrently. The conference program will include talks on microcomputer-controlled datacommunications systems, peripheral interfacing, software management, and more. Contact Industrial and Scientific Conference Management Inc, 222 W Adams St, Chicago IL 60606, (312) 263-4866.

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February 1981

February 2-5

The Second Middle East **Electronic Communications** Show and Conference, Bahrain Exhibition Centre, Bahrain. This conference will cover communications research, technology, and administration in satellite communications, digital communications, networks and industrial systems, and business communications. An exhibition will also be held. Contact TMAC, 680 Beach St, Suite 428, San Francisco CA 94109, (800) 227-3477.

February 4-5

Computer and Office Automation Show and Conference, Hyatt Regency Hotel, Vancouver, Canada. This conference will feature data-processing equipment, small-business computers,



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BLACKJACK MASTER: A Simulator/Tutor/ Game (Wazaney) A serious game that performs complex simulations and evaluations of playing and betting strategies. 05303, TRS-80 Level II tape, \$24,95:05308, TRS-80 Disk Version, \$29.95

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This money saving program simulates a disk memory system for Integer BASIC programs. It allows multiple programs in memory at the same time which can be run from a catalog. 04804, APPLE II tape, \$24.95

LINE & VARIABLE CROSS REFERENCE GENERATOR (Johnson) Provides a cross-refer ence of line numbers and variable names. 07301, PET tape, \$16.95

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Automatically maintains a cross-reference listing of all your programs, their location by disk number, their function and use. Catalogs, lists and sorts programs. 05203, TRS-80 Level II tape, \$16.95; 05208, TRS-80 Level II Disk, \$21.95

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FINPLAN: A Financial Planning Program for Small Business (Montgomery) Allows you to enter data from a balance sheet into the program, to make assumptions about future growth of business, and to have the computer project results for up to a five-year period based on those assumptions. And if you change any data, the program revises all resulting data automatically. The disk version can only be used with TRSDOS Version 2.3. 05103, TRS-80 Level II tape, \$69.95; 05108, TRS-80 Level II Disk Version, \$74.95

DATA MANAGER: A Data Base Management System and Mailing List (Lutus) Store information on a floppy disk, and retrieve it quickly and easily by specific names, or by category. 04909, Apple II Disk Version, \$49.95

MCAP: A Microcomputer Circuit Analysis Program (Savon) Performs a linear voltage, impedance, or transfer impedance analysis of an electronic circuit. 04501, PET; 04503, TRS-80 Level II; 04504, Apple II; each tape \$24.95; 04513, Heathkit/Zenith Disk, \$29.95

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February 9-13

Reliability Engineering, Testing and Maintainability Engineering, University of California, Los Angeles CA. This course is designed for reliability, product assurance, logistics, quality assurance, and design engineers. The course is intended for those required to design and to predict the reliability of components, equipment, and systems. The fee is \$750. Contact Continuing Education in Engineering and Mathematics, UCLA Extension,

POB 24901, Los Angeles CA 90024, (213) 825-1047.

February 14-16

International Conference on Microcomputer Applications to Industrial Controls, Jadavpur University, Calcutta, India. Papers will be presented on the applications of microcomputers to industrial controls in the areas of general systems. Contact Dr Sushil Dasgupta, Professor and Head, Electrical Engineering Department, Jadavpur University,

40B, Southern Ave, Calcutta-700029, India.

February 17-18

Integrating Word Processing and Electronic Data Processing: Technology, Architecture, Planning, The Harvard Club, New York NY. The topics of this seminar will be the study of word processing today and its future, the evaluation and selection of systems, electronic mail and communications, and the automated office. For further details, contact the seminar coordinators at the Center for Management Research, 850 Boylston St. Chestnut Hill MA 02167, attn: Ms Karen Smolens, (617) 738-5020.

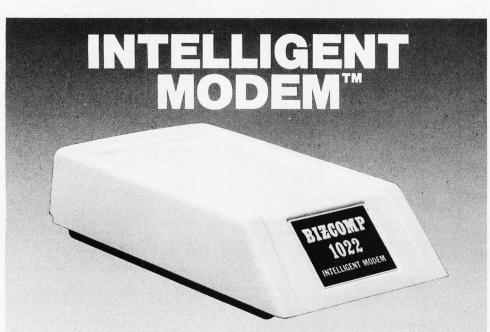
February 18-20

Business- and Personal-Computer Sales and Exposition and the Houston Business Show, Houston Civic Center, Capitol Ave and Bagby St, Houston TX. Data-processing managers, systems analysts, programmers, educators, hobbyists, and user's groups will find this exposition useful. The business show is primarily designed for purchasing and office managers, executives, business owners, attorneys, accountants, and physicians. For details, contact Produx 2000 Inc, POB 2000, Bala Cynwyd PA 19004, (215) 457-2300.

February 23-26

Computer Science Conference, Stouffer's Riverfront Towers Hotel, St Louis MO. The conference is sponsored by the ACM (Association for Computing Machinery). The Ninth Annual Computer Science Employment Register will be conducted. This register aids in matching computer scientists and data-processing specialists with employer opportunities. For information, contact Orrin E Taulbee, ACM Computer Science Employment Register, Department of Computer Science, University of Pittsburgh, Pittsburgh PA 15260, (412) 624-6475.

February 26-27
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tion. University of Southwestern Louisiana, Lafayette LA. Papers will be read on operating systems, data-base management and support, distributed computers systems, and related topics. Contact William R Edwards, c/o the Computer Science Department, University of Southwestern Louisiana, POB 44330, Lafayette LA 70504, (318) 264-6284.

March 1981

March 8-11 TI-MIX 1981, Marriott Hotel, New Orleans LA. This is a conference for Texas Instruments equipment users. Thirty-six sessions consisting of individual presentations, panel discussions, and workshops, are planned. Two exhibit rooms featuring the latest computer equipment from Texas Instruments will be open. Contact TI-MIX, M/S 2200, POB 2909, Austin TX 78769, (512) 250-7151.

March 11-13

Business- and Personal-Computer Sales and Exposition and New York Business Show, Madison Square Garden, New York NY. See February 18-20 for details.

March 17-20

The Fourteenth Annual Simulation Symposium, Tampa FL. Papers describing digital discrete simulation and other techniques, such as continuous or analog, will be read. This symposium is a forum for the exchange of ideas and techniques in computer simulation, Contact Annual Simulation Symposium, POB 22621, Tampa FL 33622.

March 23-25

Office Automation Conference, Albert Thomas Convention Center, Houston TX. This conference will present seminars on concepts and methods behind the latest office technologies and an exhibit of equipment. Contact Office Automation Conference, POB 9659, Arlington VA 22209, (703) 558-3617.

March 24-26

The Southwest Semiconductor Exposition, Phoenix Civic Plaza Convention Center, Phoenix AZ. Over 140 equipment and materials makers will exhibit over \$12 million of semiconductor, hybrid, and printed-circuitboard production, processing, and test equipment. Contact Cartlidge & Associates Inc, 491 Macara Ave, Suite 1014, Sunnyvale CA 94086, (408) 245-6870.

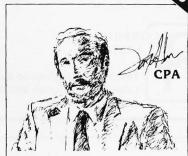
March 31-April 2

Cincinnati Business Show, Cincinnati Convention-Exposition Center, Cincinnati OH. Office equipment and services including automated systems, communications, computers, telephone systems, word processing, data processing, printing equipment, and other office supplies, will be featured. A program of business seminars is also scheduled. Contact Ray G

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Heath Users Club

The Triad Heath Users Group meets at 1 PM on the second Saturday of each month at the Sears Activity Room in the Hanes Mall in Winston-Salem, North Carolina. Contact Hughes Hoyle at (919) 378-1050, or Steve Minor, 424 Cliffdale Dr. Winston-Salem NC 27104, (919) 765-7717.

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The Computer Systems Group at the University of Waterloo is the publisher of WATNEWS, a newsletter on educational computing. WATNEWS describes software systems that are developed at the University of Waterloo. The newsletter's purpose is to communicate with people involved in the presentation of computer science, business data processing, and related courses. Some of the software featured in the newsletter includes Waterloo BASIC for the Commodore PET and an enhanced version for the IBM Series I computer. Other articles have featured a Pascal compiler and structured programming in macroassembly languages. For more information, contact WATNEWS, Computer Systems Group, University of Waterloo, Waterloo, Ontario, Canada N2L 3G1, (519) 885-1211.

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Mokurai Cherlin, of APL Business Consultants Inc, is organizing Group/380, a user group for the anticipated System/380. Mr Cherlin's intent is to prepare in advance for use of this machine, so that people will know what to do with it when, and if, it arrives. The first project of Group/380 is to compile a directory of software for the System/370 that is free, low-cost, or suitable for personalcomputing use.

Individual memberships for \$10 and corporate memberships for \$25 can be obtained from Group/380, POB 1131. Mt Shasta CA 96067. Members will receive a newsletter, instructions for program submissions, and access to a computerized data base of relevant hardware and software information.

Independent Heathkit **Vendors Listed**

Heathkit computer owners can find the hardware and software they need with this directory of suppliers compiled by Buss: The Independent Newsletter of Heath Company Computers. The newsletter includes over sixty suppliers of Heathkitcompatible products. The suppliers are not affiliated with Heath. The Buss directory is available for \$7.50 from Buss, 325-B Pennsylvania Ave SE, Washington DC 20003, (202) 544-0484.

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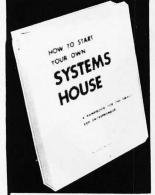
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HOW TO BECOME A SUCCESSFUL COMPUTER CONSULTANT by Leslie Nelson, May 1980

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28. No. 16 Future markets • The way to real big money • Avoiding the legal pitfalls • How consultants' associations can help you • The National Register of Computer Con-How others did it: real-life sample cases sultants and much more.



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Newsletter for the Sinclair ZX80 Microcomputer

Syntax ZX80 is a monthly newsletter for Sinclair ZX80 users. Featuring news and reviews of ZX80 hardware and software, the publication focuses on the Z80Abased microcomputer from Sinclair Research Ltd, Cambridge, England. The newsletter provides forecasts of applications, technical details for homebrewers, and a forum for users to share advice about programs and vendors. The yearly subscription rate (twelve issues) is \$25. Syntax ZX80 is available from Ann Zevnik, Editor, The Harvard Group, Bolton Rd, RD 2, Box 457, Harvard MA 01451, (617) 456-3661.

OSI Users Group

An OSI (Ohio Scientific) users group is forming in New Jersey. Contact the OSI Users Group, 4 Swimming River Rd, Lincroft NJ 07738, (201) 747-8888, atten: Bob Childs. ■

BYTE's Bits

Free MusicSystem **Updates**

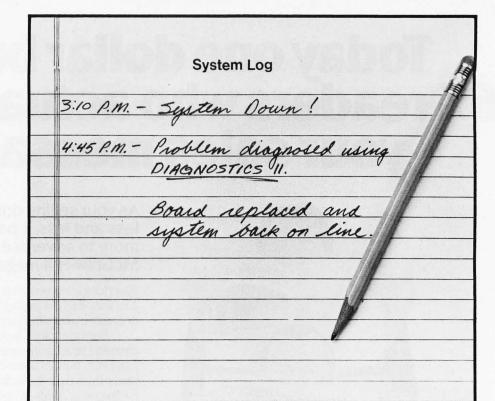
If you have purchased Mountain Hardware's MusicSystem, a musicsynthesis package for the Apple II contained in a combination of hardware and software, you are entitled to receive, free of charge, version 2.0 of the MusicSystem software, if you did not receive it with your purchase. According to Avery E Dee, vice president of marketing at Mountain Hardware, copies of the MusicSystem with earlier releases of the software (probably version 1.2) were sold with the intention of sending the version 2.0 software free of charge to registered owners. Unfortunately, the company has

no way to contact owners who have not sent in the MusicSystem warranty card.

Version 2.0 of the software includes significant improvements in the capabilities of the system, including user definition of musical instruments, quicker file loading, and printout of musical scores on the Apple Silentype printer. Music-System owners who have not received version 2.0 of the system software should send in their warranty cards (indicating the version received with the system) or call or write Mountain Hardware Inc at 300 Harvey West Blvd, Santa Cruz CA 95060, (408) 429-8600.

Radio Network for 6502 Microcomputer Users

There now are three radio nets for the microcomputer user on the amateur-radio frequencies. The East Coast Apple Net is on or near 7260 kHz every Saturday morning at 1300 UTC (Coordinated Universal Time)—ie: 9 AM Eastern Daylight Time. Transmission mode for this 40-meter net is lower sideband with W1UKZ in Scituate, Massachusetts, as net control. In the Greater Boston area there is a 2-meter net for those interested in Apple computers on the Norwell repeater (144.65/145.25 MHz). This net meets at 8 PM local time every Wednesday. W1UKZ, WA1ZKB, and others act as a control for this net. The Atari International Computer Net meets Tuesdays at 0100 UTC-ie: 9 PM Eastern Daylight Time, Monday evenings—with W1UKZ in Scituate again serving as the control. These nets transfer news about everyday computer subjects and specific news on computers and new products, and there are program swaps. For more information, contact David P Allen, W1UKZ, 19 Damon Rd, Scituate MA 02066. ■



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specifications

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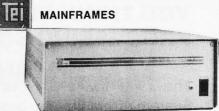
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Many articles have been written about the various new personal computers now on the market, including the Atari 400 and 800 and the Texas Instruments (TI) 99/4, but few have tried to compare these newer units against the most popular computers.

Because of this, I have decided to do a comparison of the four most popular computers (Apple II, Com-

Teri Li POB 481 Peterborough NH 03458

modore PET, Exidy Sorcerer, and the Radio Shack TRS-80 Model I) against the TI 99/4 and the Atari 400 and 800. (The BASIC is the same for both the Atari 400 and 800.) To make this as fair as possible, I have compared only the computers that come with versions of BASIC supplied with the machines in ROM (read-only memory) at the time of purchase, without extended hardware (such as disk

This comparison is in the form of three tables. (See tables 1 thru 3 on pages 320 thru 327.) The BASIC command, statement, or function is on the left, followed by six columns, one for each of the computers (PET, Apple II, TRS-80, Atari, TI 99/4, Sorcerer). To the right of these columns is a brief explanation of each of these commands (since not all are self-explanatory). If a particular computer interprets a BASIC command differently from the others, a notation of the difference is made.

For the Apple II computer, especially, this is true as there are two versions of BASIC which you can get with it: Integer BASIC and Applesoft. Unless otherwise stated for the Apple. the commands apply to both ver-

There are only a few additional comments that I need to make about these comparison tables.

I have not gone into a great deal of detail on the graphics capabilities of these machines, but briefly speaking, the TRS-80 has the worst point resolution, while only the Apple II, Atari 400 and 800, and TI 99/4 have color graphics. In graphics mode, the Apple II, Atari 400 and 800, and Sorcerer offer the most versatility, while the PET is the easiest to use.

Last, the TI has the most cumbersome BASIC to use. It lacks a "free memory" command, it allows only line numbers (not statements) to be used in IF . . . THEN statements, and it does not allow the use of multiple statements per line.

As for the rest, check out the tables and decide for yourself which of these computers is best suited to your needs.

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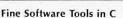
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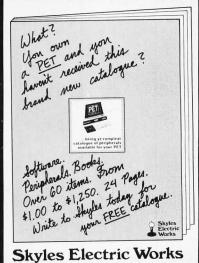
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Circle 266 on inquiry card. Circle 265 on inquiry card.

System	Commodore PET	Apple II	Radio Shack TRS-80	Atari 400,800	TI 99/4	Exidy Sorcerer
AUTO mm, n		~	~		Number	
BREAK mm					~	ite,
CLEAR	CLR	~	_	~		-
CLEAR n			_			
CLOAD	LOAD	LOAD	-	V	OLD	~
CLOAD?	VERIFY		~			
CONTINUE	CONT	CONT	CONT	CONT	-	CONT
CSAVE ,	SAVE	SAVE	,	~	SAVE	_
DELETE mm		DEL	~			
EDIT mm	cursor	cursor	~	cursor	cursor	
HOME		_				
НІМЕМ		-				
LIST mm-nn	_	~	~	~	~	_
LOMEM		-				
MAN		~				
NEW	_	_	_	~	~	~
RESEQUENCE mm, nn					~	
RUN mm	-	-	~	~	~	~
SYSTEM	SYS	CALL - 151	~	BYE	BYE	BYE
TROFF		NOTRACE	~		UNTRACE	
TRON		TRACE	~		TRACE	
UNBREAK					~	
(Screen Format)	40 by 24	40 by 24	64 by 16	40 by 24	32 by 24	64 by 3
(Character Resolution, m by n)			3 by 2		8 by 8	8 by 8
(Total pixels)		280 by 192	128 by 48	320 by 192	256 by 192	512 by 2

Table 1: Availability of BASIC system commands in six microcomputer families. In this table, and tables 2 and 3, a check indicates the presence of a feature in a given microcomputer BASIC, while a blank indicates its absence. A word or words in the table entry

BASIC Statements	Commodore PET	Apple II	Radio Shack TRS-80	Atari 400,800	TI 99/4	Exidy Sorcerer	
General Statements							
APPEND					~		
CLS		~	~	Vini	CALL CLEAR		
CALL address		~					
CALL CHAR					~	EN	
CALL COLOR				COLOR	~		

Table 2: Availability of BASIC statement types in six microcomputer families.

Explanation of Command

Automatically numbers the lines of a program as you enter them from the keyboard, starting with line mm, using the increment n. Not available in Applesoft.

Sets a breakpoint at line number mm; program execution will halt upon reaching this breakpoint.

Sets all numeric variables to zero and all string variables to null.

Sets aside n bytes of memory for storage of strings; also sets numeric variables to zero and string variables to null.

Loads a BASIC program from cassette tape.

Compares a program in memory to a program on tape; the two must match exactly.

Continues execution of a program after reaching a BREAK (TI) or STOP statement (all) during program execution, or after program is halted by operator (after a Control-C, Break key, Stop key, etc).

Saves a BASIC program in memory to cassette tape.

Deletes program line mm from the program. The TI uses this command to delete programs or data files from its filing system.

Enters EDIT mode for line number mm. Lets you manipulate the characters in line number mm. The Apple, Atari, Exidy, and PET computers use on-screen editing to do this via LIST and cursor controls.

Moves cursor to top line, leftmost position of video, in Applesoft only. CALL - 976 has same function for Integer BASIC.

Sets address of highest memory address available to a BASIC program; protects data, graphics, or machine-language routines located in high memory.

Lists all program lines between (and including) line numbers mm and nn. Apple Integer BASIC uses comma instead of hyphen.

Sets lowest address available to a BASIC program. Reset by NEW, DEL, and Control-C key.

Apple Integer BASIC only: resets AUTO line-numbering feature to manual numbering.

Deletes entire program from memory and resets all pointers and variables to zero and null.

Renumbers program from beginning or starting with line mm, incrementing in steps of nn.

Begins execution of program, starting at beginning or at line number mm.

Puts you in monitor mode for execution of machine-language programs. Atari and TI use BYE only to go to calculator mode from BASIC.

Turns off trace features.

Tells you which line number of the program is currently being executed. Very useful in tracking down programming bugs.

Removes breakpoint set by the BREAK command.

Normal screen format for text operation, number of characters per line by number of lines on screen.

Individual character positions on screen can be broken down into a matrix of dots, *m* rows of *n* dots per row. Not applicable to Apple II, Atari 400/800 or the PET.

Actual number of total pixels (picture elements) that can be individually turned on and off by the program when in full graphics mode.

indicates that the feature described under the "Explanation" column is available for a given computer using this name. These tables are not meant to be an exhaustive description of any of the six computer systems.

Explanation of Statement

Allows data to be added to the end of a data file.

Clears the video screen and returns the cursor to the top line, leftmost position of the video. See also HOME.

Branches to the machine-language subroutine at the specified address addr.

Allows you to define a new character for the video display to be used by your program.

Allows you to define the background color to be used for the individual characters.

System	Commodore PET	Apple II	Radio Shack TRS-80	Atari 400,800	TI 99/4	Exidy Sorcerer
CALL JOYSTK		55-7-13		STICK	~	
CALL SCREEN		HCOLOR =		SETCOLOR	~	
CALL SOUND				SOUND	,	
CLOSE .	~				~	
COLOR = n	_	L STANFOLD				
DATA	~	~	_	~	~	~
DEF FN (name)	~	~			DEF	
DEFINT			~			
DEFDBL			_			
DEFSNG						
DEFSTR			~			
DIM var(k)	~	~	~	~	~	
DISPLAY					~	
DRAWTO		HPLOT		~		
DSP var		_				
END	,	_	_	_	,	_
EOF					~	
ERROR (mm)			_			
FOR TO STEP, NEXT	~	~	~	V	· ·	~
GOSUB linenum, RETURN	~	~	-	~	-	~
GOTO linenum	_	~	~	·	~	~
GR		~				
GRAPHICS	- market will			~		
HLIN AT					CALL HCHAR	
IF expr THEN linenum	~	~	~	V	~	~
IF expr THEN ELSE			~	t.	~	
IF expr GOSUB RETURN	~	~	~			~
IF expr GOTO	~	~	~			~
IN (port)		IN#expr	~			~
INPUT ''msg'', var	_	~	_	~	~	~
INPUT#n,var	~	RECALL	~		~	~
LET var = expr	~	~	_	V	~	_
LPRINT "msg" or LPRINT var		CHARLES IN PARTY	~	~		
ON ERROR GOTO linenum		ONERR	~	TRAP		
ON expr GOSUB, RETURN	~	_	~	~	V	-
ON expr GOTO linenum	_	_	~	_	~	_

Explanation

Checks the joystick port for input.

Allows you to select the background color of the video. HCOLOR = exp lets you select the color to be used in hi-res (high-resolution) graphics mode in Applesoft.

Lets you define the sound output to be used by your program.

Closes device (tape, printer, etc) data file.

Sets the color of the point for the next plot (in low-resolution graphics for the Apple II).

Holds data for access by a READ statement.

Lets you define a single-line function, called by using FN and the function name.

Defines as integer all variables beginning with the specified letter, letters, or range of letters.

Defines as double-precision floating-point all variables beginning with the specified letter, letters, or range of letters.

Defines as single-precision floating-point all variables beginning with the specified letter, letters, or range of letters.

Defines as string variables all variables beginning with the specified letter, letters, or range of letters.

Allocates space in memory for a variable array with as many dimensions as numbers in k, and with the specified size per dimension. Apple Integer BASIC allows one-dimensional arrays only.

May be used in place of PRINT, or to specify the format of data stored on tape. DISPLAY specifies ASCII format.

Draws a line from the last plotted point to this position. HPLOT can also plot a single point in high-resolution graphics or a series of points connected in sequence.

Displays value of the specified variable each time it changes. Available in Apple Integer BASIC only.

Ends execution of program and returns to command mode.

Writes End-of-file mark to a data file.

Simulates the error specified by the number mm, to test ON ERROR GOTO routines.

Creates an iterative loop, with optional step size specified. If no step size is given, a step of 1 is used. Leaving a loop before it is finished will cause problems later.

Branches to the specified line number and continues program execution from that point until a RETURN is found. Execution then returns to the statement following the GOSUB command.

Branches to the specified line number.

Turns on low-resolution graphics. HGR selects page 0 of high-resolution graphics, HGR2 selects page 2.

Turns on graphics mode.

Draws a horizontal line at the specified line number. TI lets you specify the number and type of characters in the line.

Tests an expression. If it is true, the statement following the THEN is executed before executing the next program line. If it is false, program execution proceeds to the next line.

Same as above, except execution goes to the ELSE only if the argument is false. In either case, execution continues on the next program line. TI allows only line numbers after THEN and ELSE.

Same as an IF . . . THEN, except a GOSUB is executed.

If the expression is true, then program execution proceeds directly to the specified line number and continues from there.

Goes to the specified port and gets the value there. Both the argument and the result must be in the range of 0 thru 255. IN# selects specified motherboard slot for input, with 0 being the keyboard.

Goes to keyboard and awaits user input. An optional message may be printed to the video display as a prompt.

Inputs data from cassette. RECALL (for Applesoft only) reads data into single array. (Applesoft and Apple Integer BASIC have INPUT statement, too.)

Assigns the argument to the specified variable.

Sends value of the variable specified or a message contained within quotes to a printer. See also PRINT# for the PET and TI.

Error-trapping routine: if an error occurs within the program, then program execution goes to the specified line number and continues from there.

Evaluates expression; on the integer value of the expresssion, *expr*, transfers control to the *expr*th number after the word GOSUB. Returns to line after this line when RETURN is encountered.

Same as above except control does not return to next line.

System	Commodore PET	Apple II	Radio Shack TRS-80	Atari 400,800	TI 99/4	Exidy Sorcerer
OPEN	-				V	
OPTION BASE (x)					V	NAME OF THE OWNER OWNER OF THE OWNER
OUT portnum, val		PR#expr	~			
PADDLE		PDL		~		
PEEK	~	~	~	-	CALL GCHAR	~
POINT			~			
POP		~		~		
POKE locn, val	~	~	~	,		~
PRINT "msg" or PRINT var	~	~	~	~	~	_
PRINT@			~	POSITION		
PRINT#	_	~	~		_	~
PRINTUSING			-			
PTRIG	and the second	State on the same				
READ var,var	~	~	~	-	-	-
RECALL		~				
REM	~	~	~	~	~	_
RESET (x,y)			_			
RESTORE	~	-	~	~	~	-
RESUME linenum		~	~	della re-	ente de	
SET (x,y)	of the state of the state of	PLOT, HPLOT	~	PLOT	in her yellows and her	
SPEED = expr		~				
STOP	~	~	-	-	~	~
STORE		~				
TAB	~	~	-		-	~
TEXT		~				
UPDATE				100	-	
VLIN AT		~			CALL VCHAR	
VTAB (x)		~				
WAIT A,B,C	~	~				~
String Functions						
ASC (string)	~	~	~	~	~	~
CHR\$ (code)	~	-	~	~	~	~
FRE (X\$)	~		~	-		~
INKEY\$	GET	GET	~		CALL KEY	
LEFT\$ (string,n)	~	~	~			~
LEN (string)	V	~	V	-	V	V
MID\$ (string,p,n)	V	~	~		SEG\$	V
POS (str1,str2,n)		leading implica			~	
RIGHT\$ (string,n)	~	~	~			~
STR\$ (expr)	~	~	~	~	~	~

Explanation

Opens a device to either input or output a data file.

Sets the lowest-allowable subscript of an array, x, to either 0 or 1.

Sends the specified value ($0 \le val \le 255$) to the specified I/O port ($0 \le portnum \le 255$). PR# selects motherboard slot (0 thru 7) for output, where 0 = video monitor.

Gets the value of the paddle input.

Returns the value stored in the specified location. Atari and TI are restricted to video locations only.

Checks the specified video location (graphic) and returns a 1 if it is on, returns a 0 otherwise.

Removes the most recent addition from the stack.

Loads the specified value into the specified location. Both numbers are decimal, and $0 \le val \le 255$.

Sends the message within the quotes or the value of the specified variable(s) to the video display.

Same as above, except printing begins at the specified video location.

Sends data to the cassette drive.

Prints according to the specified format.

Returns a 0 if the game-paddle pushbutton is depressed, otherwise a 1 is returned. STRIG is used for the joystick button.

Assigns the values stored in the data statements to the variables listed.

Reads contents of a numeric array from cassette; available in Applesoft only.

Remark indicator; computer does not execute anything following the REM (for the rest of that line only).

Turns off the graphics block at position (x,y).

Resets the data pointer to the first item in the first DATA line. With Atari and TI, a line number may be specified, and the pointer will be set to the first item of data in that line.

In Applesoft only, resumes program execution from the error routine at the specified line number.

Turns on the graphics block (x,y). Apple Integer BASIC and Applesoft can plot low-resolution graphics with PLOT. Applesoft can also plot a high-resolution graphics point with HPLOT.

Determines speed at which characters are sent to the screen or other output device (Applesoft only).

Halts program execution and returns to the READY prompt.

Writes contents of a numeric array to cassette (Applesoft only).

A print modifier: the variable or message is printed at the specified column.

Converts from graphics mode to all-text mode.

Allows an opened file to be both read from tape and written to tape, changing values in the process.

Draws a vertical line at the specified column. TI lets you specify number and type of characters in the line.

Moves the cursor x lines down from the top of the display screen.

Temporarily halts program execution until certain conditions are met.

Returns the ASCII value of the first character of the string.

Returns a one-character string defined by the value of code, $0 \le code \le 255$. If a control code is specified, that function is executed.

Returns the amount of memory available for string-variable storage.

Scans the keyboard once and returns the character pressed. If none of the keys are pressed during the scan, returns a null.

Returns n characters from the specified string, starting at the left.

Returns the length of the specified string, 0 for a null string.

Returns a substring of length n, starting at position p in the specified string; Atari uses a subscripting procedure.

Returns the starting position of substring str2 inside of string str1, beginning the scan at character position n in str1.

Returns n characters from the specified string, starting at the right.

Converts the specified numeric expression to a string

System	Commodore PET	Apple II	Radio Shack TRS-80	Atari 400,800	TI 99/4	Exidy Sorcerer
STRING\$ (n,char)			~			
VAL (string)	~	~	V	~	~	~
VARPTR var			~	ADR		

BASIC Functions	Commodore PET	Apple II	Radio Shack TRS-80	Atari 400,800	TI 99/4	Exidy Sorcer
(Precision)	9	10	6 or 16	10	14	6
ABS (expr)	-	~	~	~	~	~
ATN (expr)	~	~		~	~	~
CINT (expr)			~			
CDBL (expr)			V			
CLOG (expr)		~		~		
CSNG (expr)			~			
COS (expr)	~	~	~	~	~	~
ERL (expr)			~			
ERR (expr)			~			
EXP (expr)	~	~	~	_		~
FIX (expr)			V			
FRE (expr)	~		∠ (also MEM)	~		V
INT (expr)	~	~	~	~	~	~
LOG (expr)	~	~	~	~	~	~
MOD (expr)		~				
POS (expr)	~	~	~			_
RANDOMIZE	RANDOM		RANDOM		V	
RND (0)	~	RND	-	~	RND(1)	~
RND (expr)		~	-			
SCRN (x,y)		~				
SGN (expr)	~	V	~	~	~	~
SIN (expr)	~	V	~	~	~	_
SPC (expr)	~					
SPC (num)		~		NULL		~
SQR (expr)	~	~	~	V	~	~
TAN (expr)	~	V	V		~	~
TI (expr)	~					
USR (X)	~	~	~			~
AND, OR, NOT						

 Table 3: Availability of BASIC mathematical and other functions in six microcomputer families.

Explanation

Returns a string of length n composed of the specified character.

Converts a string of numerals (eg: "68") to its numeric value (eg: 68).

Returns the memory address where the name, value, and pointer of variable var are stored.

Explanation

The number of significant digits with which the computer operates. The TRS-80 has double-precision (sixteen-digit) capability, but all machine-supplied functions are truncated to six digits.

Gives the absolute value of the specified expression.

Gives the arctangent in radians; Atari can be set up to use angular measures in degrees.

Converts the expression into the largest integer not larger than the expression; $-32768 \le expr \le 32768$.

Converts the expression to double-precision (sixteen-digits).

Returns the base-10 (common) logarithm of the specified expression; CLOG (0) will give an error, CLOG (1) = 0.

Converts the expression to single-precision (six digits).

Returns the cosine of the expression, where expr is in radians.

Returns the line number of the current error.

Returns a value related to the current error.

Returns the natural exponential (e expr = EXP (2expr)).

Returns the integer equivalent of the expression, truncated.

Tells you total number of unused and unprotected bytes in memory. MEM does not include unused string space. FRE(A\$) will tell you amount of unused string space.

Returns largest integer not greater than the expression ($-32768 \le expr \le 32768$).

Returns natural logarithm (base e) of the expression; the expression must be positive.

Modulo arithmetic: returns remainder after two numbers are added/subtracted, allows for some division. Available in Apple Integer BASIC only.

Returns a number indicating the current position of the cursor on a line: available in Applesoft only.

Reseeds the random-number generator.

Returns a pseudorandom number between .000001 and .999999; in Applesoft and TI BASIC, RND(0) returns the last random number given.

Returns a pseudorandom number between 1 and the value of the expression ($1 \le expr \le 32768$). In Applesoft if expr < 0, then the same value is returned each time expr is used.

Returns the color value at screen position (x,y); available in Integer BASIC only.

Returns a -1 if the expression is negative, 0 if it is 0, or +1 if it is positive.

Returns the sine value of the expression; expr must be in radians.

Returns the number of skips specified in the argument. Range $0 \le expr \le 255$. SPC(0) = 256 skips.

Prints the specified number of spaces.

Returns the square root of the specified expression: expr cannot be negative.

Returns the tangent of the expression, the expression must be in radians.

Sets the real-time clock to the value specified.

Passes the value X to a machine-language subroutine and executes subroutine. Address of the routine must already have been POKEd into memory.

These three operators perform the given logical operations on numeric variables or expressions. (NOT works on a single number.) In most cases, these operators work bit-by-bit on the numeric values expressed in binary. For example, 3 OR 5 equals 7: 3 is binary 011, 5 is binary 101, and 7 (the result) is 111 (011 OR 101).

Programming Quickies

Rotation Algorithm

Samuel Bates, SPO 1263, Sewanee TN 37375

Many unique and elegant designs can be produced using straight lines. Listing 1 shows a program for creating such designs. Using the "rotation" algorithm, curved patterns that appear to be three-dimensional will be produced.

The main functions of the program (which is written in Hewlett-Packard HP 3000 BASIC) are POLY and ROTATE. When given information on the size and location of a polygon, POLY draws the figure and numbers the vertices. ROTATE takes a number of points and does the following:

• A small distance is measured along the line between the first two points.

- The same distance is measured between the second and third points, and a line is drawn between these points.
- The first two steps are repeated until the program cycles back to the beginning point.
- The program begins again, measuring along the lines of the new polygon just formed.

Other functions in the program are JOIN, which draws a line between two points; MID, which takes the midpoint of two lines; PRINT, which prints the coordinates of a point; and POINT, which creates a point when given X and Y coordinates. TO, RECALL, and LIST are for creating and using specific routines.

All figures shown (1 thru 5) were drawn with a Hewlett-Packard 7202A plotter.■

Listing 1: "Rotation" written in HP 3000 BASIC. The READ statements retrieve graphic parameters from the individual files shown with each figure.

```
FILES *
10
                                                      350
                                                           C=N=0
    DIM AS[72], BS[72], E[30,2]
                                                           FOR I=L+2 TO LEN(AS)
30
    DIM M(100,2), N(40), F$(3), R(10)
                                                      370 IF AS[I,I]=" " THEN 450
    I MAGE 4D, X, 4D, """
                                                           FOR J=1 TO 10
40
                                                      380
    IMAGE 4D, X, 4D
50
                                                      39 0
                                                           IF A$[I, [] = B$[J, J] THEN 420
60
    DEF FNE(2) = (E[I+1,2]-E[I,2])+2
                                                           NEXT J
                                                      400
70
    P$= "FLT"
                                                      410
                                                           RETURN
80
   F=25
                                                      420
                                                           N=N+1
90 P=0
                                                      430
                                                           R[N]=J-1
100 PRINT "FILE NAME";
                                                      440
                                                           NEXT I
110
    INPUT AS
                                                      450
                                                           X = \emptyset
     ASSIGN AS. 1. S
120
                                                           FOR J=1 TO N
                                                      460
    IF S=3 THEN 100
130
                                                      470
                                                           X = X + R[J] * 10 + (N-J)
    PRINT "BEGIN"
140
                                                           NEXT J
                                                      480
     PRINT ":";
150
                                                      490
                                                           C = C + 1
     ENTER 255, A9, A$
160
                                                      500
                                                           N[C]=X
170
     PRINT
                                                           N = 0
                                                      510
180
     GOSUB 240
                                                      520
                                                           MAT R= ZER
     GOTO 150
192
                                                      530
                                                           IF I <= LEN(A$) THEN 440
     STOP
                                                           IF D>X9 THEN 580
200
                                                      540
     DATA "POLY", "JOIN", "MID", "ROTATE"
210
                                                      550
                                                           GOSUB D OF 590,710,790,340,1090
     DATA "PRINT", "POINT", "TO", "RECALL"
220
                                                      560
                                                           GOSUB D-5 OF 1410, 1130, 1270, 1460
     DATA "CLEAR", "LIST", "QUIT"
                                                           GOSUB D-9 OF 1510,200
                                                      57 0
230
240
     x9 = 11
                                                      580
                                                           RETURN
250
     RESTORE
                                                      590
                                                           N[2]=N[2]/(2*SIN(3.14159/N[1]))
260
     FOR D=1 TO ×9
                                                      600
                                                           N[3]=N[3]*3.14159/180
270
     READ BS
                                                           PRINT PS; "L"
                                                      610
                                                           FOR I = P TO N[1]+P
     IF AS[1, LEN(B$)]=B$ THEN 320
280
                                                      620
                                                           G=(I-P)*6.28319/N[1]+N[3]
29 0
     NEXT D
                                                      630
     FRINT "NONEXISTENT COMMAND"
                                                           M[I+1,1]=N[4]+10*N[2]*COS(G)
300
                                                      640
310
     RETURN
                                                      650
                                                           M(I+1,2]=N(5]+10*N(2]*SIN(G)
320
     IF D>6 THEN 550
                                                      660
                                                           PRINT USING 50; M[[+1,1], M[[+1,2]
     L=LEN(B$)
330
                                                      67 Ø
                                                           NEXT I
340
     B$="0123456789"
                                                           PRINT PS; "T"
                                                      680
```

Programming Quickies _

```
P=F+N[1]+1
69 a
700
     RETURN
713
     IF C/2#INT(C/2) THEN 780
720
     PRINT PS; "L"
730
     FOR I=1 TO C STEF 2
740
     PRINT
            USING 40; MENEI], 1], MENEI], 2]
750
     PRINT USING 50; M(N(I+1], 1], M(N(I+1], 2]
760
     NEXT I
770
     PRINT PS; "T"
730
     RETURN
790
     P=P+1
     M[P, 1] = (M[N[2], 1]+M[N[1], 1])/2
800
810
     M(P,2]=(M(N(2),2)+M(N(1),2))/2
     PRINT "POINT"P"="M(P, 11; M(P, 2)
820
830
     RETURN
     FOR I = 1 TO C
840
     E[[,1]=M[N[]],1]
850
     E[1,2]=M[N[1],2]
860
870
     NEXT I
880
     E([,1]=E(1,1]
890
     E[1,2]=E[1,2]
     PRINT PS; "L"
900
     FOR I=1 TO C
910
     N[ ] = F*L /SOR(FNE(1)+FNE(2))
900
930
     IF N(1) > . 5 THEN 1070
     NEXT I
940
950
     FOR I=1 TO C
960
     E[1,1]=E[1,1]+N[1]*(E[1+1,1]-E[1,1])
     E([,2]=E([,2]+N([]*(E([+1,2]-E([,2])
97 a
983
     IF I#1 THEN 1010
            USING 40; E[[, 1], E[[, 2]
990
     PRINT
1020
      GOTO 1020
             USING 50; E[1,1], E[1,2]
      PRINT
1010
1020
      NEXT I
      F[[.1]=F[1.1]
1030
      E[1,2]=E[1,2]
1040
1050
      PRINT
             USING 50; E[[, 1], E[[, 2]
1260
      GOTO 917
      PRINT PS; "T"
1270
1080
      RETURN
1090
      FOR I=1 TO C
      PRINT "POINT"N([]"="M[N([], 1]; M[N([], 2]
1100
1110
      NEXT I
      RETURN
1120
1133
      READ #1.1
1140
      IF
          END #1 THEN 1190
1150
      READ #1; B$
      IF B$#A$[4] THEN 1150
1160
      PRINT "DUPLICATE NAME"
1170
1130
      GOTO 1260
      PRINT #1; A$[4]
1190
1200
      PRINT ">";
1210
      ENTER 255, A9, A$
1350
      PRINI
1230
      PRINT #1: AS
      IF A$ #"END" THEN 1200
1240
1250
      PRINT #1; END
1260
      RETURN
1279
      READ #1.1
1280
      A$= A$[8]
1290
      IF END #1 THEN 1330
      READ #1; B$
1300
1310
      IF BS=AS THEN 1350
1320
      GOTO 1390
      PRINT "NONEXISTENT ROUTINE"
1330
1340
      RETURN
1350
      READ #1; 45
1360
      PRINT AS
1370
      IF AS= "END" THEN 1400
1380
      GOSUB 240
1390
      GOTO 1350
```

1400

RETURN

Listing 1 continued on page 330

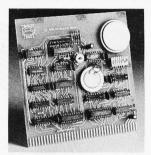
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Warning: Installation requires opening the Model II, which may void its warranty. We suggest that you wait until the warranty period has expired before installing the CCB-II.

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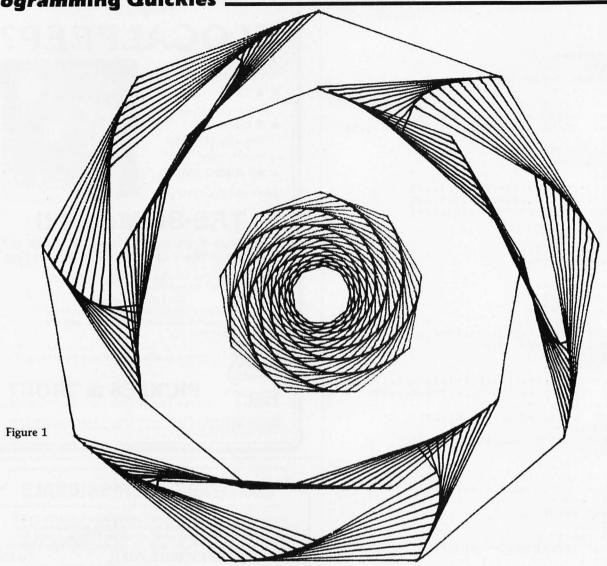
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To explore these, or many other challenging positions, please contact Dan Meagher by calling (617) 329-2660 or mall your resume to him (in complete confidence) at:



Programming Quickies.



:POLY 9 200 0 5000 5000 PLTL :POLY 9 275 0 5000 5000 PLTL :JOIN 11 2 13 4 14 5 16 7 17 8 19 1 PLTL :ROTATE 11 12 13 4 3 2 PLTL :ROTATE 14 15 16 7 6 5 PLTL

:ROTATE 17 18 19 1 9 8 PLTL :CLEAR PLOT CLEARED :POLY 9 100 0 5000 5000 PLTL :ROTATE 1 9 8 7 6 5 4 3 2 PLTL :QUIT DONE

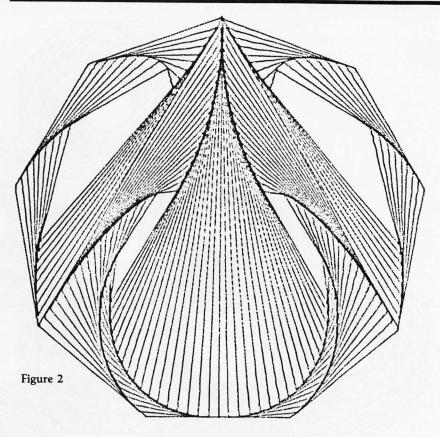
```
Listing 1 continued:
1410 P=P+1
     M[F.1]=N[1]
1420
     M[P,2]=N[2]
1430
1440 PRINT "POINT"P
1450 RETURN
     MAT MEZER
1450
1470
     MAT N=ZER
1430
     P=0
     PRINT "PLOT CLEARED"
1490
1500
      RETURN
      READ #1.1
1510
1520
      R=0
     PRINT
1530
     IF LEN(A$) > 4 THEN 1630
1540
1550 IF END #1 THEN 1620
```

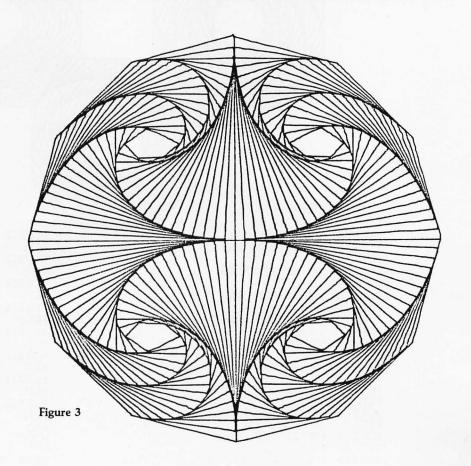
```
READ #1; AS
PRINT " "; AS
 1560
 1570
        IF A$ #"END" THEN 1560
 1580
 1590
        PRINT
       IF R THEN 1620
 1600
 1610
        GOTO 1560
        RETURN
 1620
 1630
        R = 1
           END #1 THEN 1690
       IF
 1640
        READ #1: B$
 1650
 1660
       IF B$#A$[6] THEN 1650
        A$= B$
 1670
 1630
       GOTO 157₽
       PRINT AS" NON-EXISTENT"
 1690
       RETURN
 1700
17 10 END
```

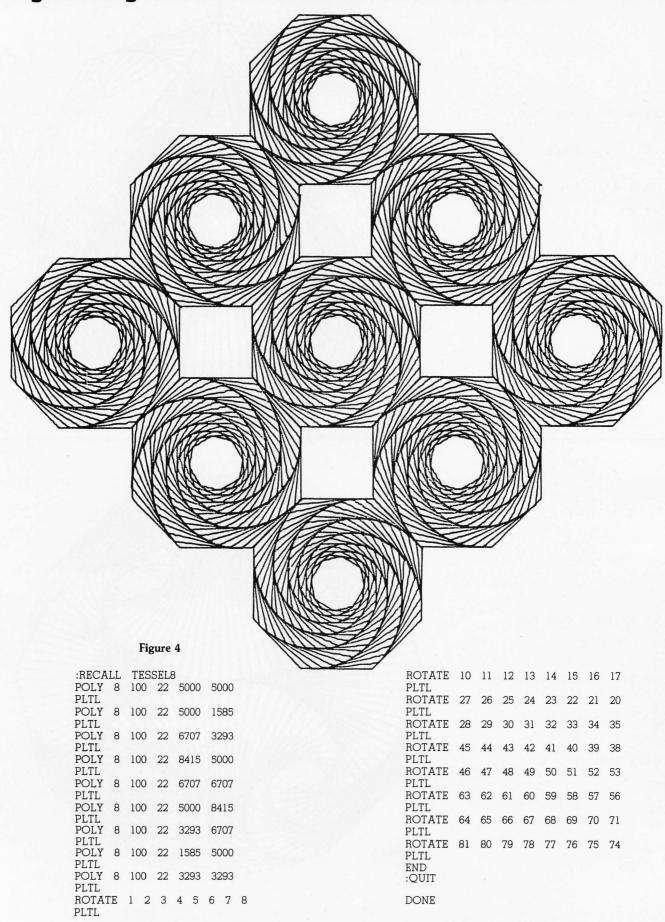
Programming Quickies.

:POLY 9 200 0 5000 5000 PLTL :MID 5 6 POINT 11 = 2252.52 :JOIN 4 1 7 1 11 1 4999.99 PLTL :ROTATE 1 11 5 4 PLTL :ROTATE 1 11 6 7 PLTL :ROTATE 1 4 3 2 PLTL :ROTATE 1 7 8 9 PLTL :QUIT

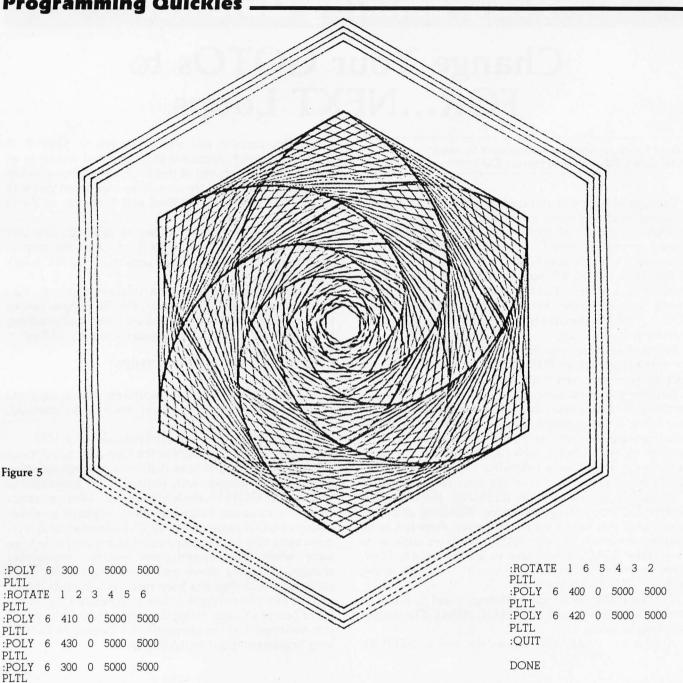
DONE







Programming Quickies



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Programming Quickies

Change Your GOTOs to FOR...NEXT Loops

David Carew, Interactive Management Systems, 3700 Galley Rd, Colorado Springs CO 80909

In terms of computer architecture, virtually all currently available microprocessors are termed "stack-oriented" machines. Virtually all implementations of BASIC interpreters on stack-oriented machines make use of a pushdown stack to implement FOR . . . NEXT loops. Because of this, FOR . . . NEXT loops run much faster than loops implemented with a GOTO statement. GOTO statements involve some sort of line search; whereas FOR . . . NEXT statements get their "traffic direction" directly from the stack.

My purpose here is to demonstrate how you can gain the extra efficiency of FOR . . . NEXT loops for *all* the looping constructs you write in BASIC.

Suppose you want to access a particular part of an internal table of data items (in DATA statements). Perhaps you enter a string which you convert to a particular negative number. Later you wish to find that negative number in your DATA table, knowing that the datatable items immediately following the matching "key" can be processed further to satisfy your requirements.

Obviously, you'll wish to RESTORE the data-table pointer and loop through the table, READing and comparing until you have a match. However, there can be no assumptions made in your BASIC program code as to how many READs it will take to get the match. How, then, can you implement such a loop using FOR . . . NEXT construction?

Two methods are shown in listings 1 and 2. Either of them will run in virtually any BASIC dialect. The simpler is shown in listing 1.

Almost any BASIC that allows the user to STEP the

loop-index variable will also allow you to STEP 0. A STEP 0 does not increment the index and results in an endless loop. To get out of this loop, test as shown in line 530 of listing 1 and set the loop index high when you wish to exit the loop. This method will even run in Radio Shack's Level I BASIC.

An alternative method, shown in listing 2, also uses manipulation of the loop index from within the loop. It may be implemented in those versions of BASIC which may not allow STEP 0.

If you need more than 32,766 iterations of a loop, then you need this speed optimization. For the extreme case or for the purist who wants his endless loops really endless, the user could manipulate the index again by adding:

$$515 \text{ IF I} = 32765 \text{ THEN I} = 1$$

However, for short loops, the added processing overhead of the extra IF statement will cut much of the speed advantage.

Some may consider the manipulation of a FOR . . . NEXT loop index from within the loop a bit too devious for their taste, but I believe that, even without considering speed advantages, such constructs are preferable to "backward GOTO" implementations. Modern structured-programming techniques place emphasis on elimination of GOTO statements. GOTO implementations require more care to get up and running and are prone to go awry when later modification requires line-number changes. Tracking down and reworking GOTO references after a change has been made is tedious business, and the one you overlook is sure to generate a fine example of Murphy's Law. Using the method I have described, you no longer lack an alternative to "backward GOTO" loop implementations in BASIC.

Listing 1: An example of using a FOR...NEXT loop to replace a GOTO statement. The technique shown in this listing works with versions of BASIC that allow STEP 0, including Radio Shack Level I and Level II BASIC.

140 REM CALL READ LOOP SUBR: K = KEY ITEM 150 GOSUB 500

500 RESTORE 510 FOR I = 1 TO 2 STEP 0 520 READ X 530 IF X = K THEN I = 3 540 NEXT I 550 RETURN **Listing 2:** An alternative method of replacing a GOTO statement with a FOR...NEXT loop. This method can be used in versions of BASIC that do not allow STEP 0.

140 REM CALL READ LOOP SUBR: K = KEY ITEM 150 GOSUB 500

500 RESTORE 510 FOR I = 1 TO 32766 520 READ X 530 IF X = K THEN I = 32767 540 NEXT I 550 RETURN



META TECHNOLOGIES

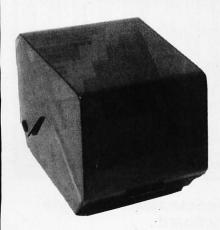
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MISCELLANEOUS

Analog Interface Switching Modules

ATEC Systems, POB 128, Mendon NY 14506, (716) 924-3822, has introduced a series of switching modules that can be used as an analog interface between any microprocessor 8-bit I/O (input/output) port and the signals to be switched in automatic test equipment, instrumentation, and control-system applications. In the matrix mode, any switch selected can be latched or unlatched. In the multiplexer mode, only one switch can be closed at any time. The latches are solid state, and the switches are sealed reed relays, with a life of more than 100 million operations. By selecting the required interface module, the complete matrix or multiplexer can be controlled from an 8-bit I/O port or from the IEEE-488 bus. The modules range in price from \$80 to \$100. Complete systems can also be ordered.

Circle 460 on inquiry card.

Cryptography Kit

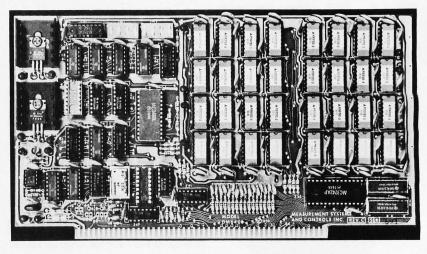
The Cryptographic Primer Kit educates computer users about cryptography, thereby enabling them to encode and protect data against unauthorized access. An RS-232 interface board is included in the kit. The interface board functions at 300 bps (bits per second) and contains the WD20001F LSI (large-scale integration) implementation of the National Bureau of Standards Data Encryption Standard. A Cryptographic Primer describes how the user can implement different cryptographies in software in conjunction with the board. It also provides examples for debugging software. An assembly and wiring manual includes wiring diagrams, assembly and operating instructions. The kit is priced at \$395 unassembled or \$495 assembled. Contact Western Digital, 3128 Redhill Ave, Newport Beach CA 92663, (714) 557-3550.

Circle 461 on inquiry card.

Socket Wrap Identification

The Socket Wrap-ID is used to identify pin numbers on wire-wrapping sockets. It consists of a socket-sized plastic panel with numbered holes in the pin location. The Socket Wrap-ID is slipped onto the socket before wrapping. Users can write on it for identification of location, integrated-circuit part number, or function. It is available from O K Machine and Tool Corporation, 3455 Conner St, Bronx NY 10475, (212) 994-6600.

Circle 462 on inquiry card.



S-100-Compatible, Bank-Selectable, 64 K-Byte Memory Board

The DMB6400 is a 64 K-byte, bank-selectable, dynamic memory module from Measurement Systems & Controls, 867 N Main St, Orange CA 92668, [714] 633-4460. It is compatible with Alpha Micro, Cromemco, North Star, MP/M, and most other S-100 bus computers. It uses output port addressing for the bank select and is configured as four independent 16 K banks of memory. Any of the 256

possible I/O (input/output) ports can be decoded, and eight banks of memory are possible for each port. Each bank can be turned on or off at system reset, and phantom can be used by any of the four banks. The board will run with all 8080 and 8085 microprocessors at 3 MHz. It will also run with most Z80As and the Marin Chip M9900 microprocessor.

Circle 463 on inquiry card.

AIM16 A/D Converter

The CmC AIM16 is a sixteen-channel A/D (analog-to-digital) converter designed for most microcomputers, including the PET, Apple II, TRS-80, and KIM. The converter is connected through the computer's 8-bit I/O (input/output) port or through one of CmC's (Connecticut microComputer) custom interfaces. Each of the sixteen inputs is converted to an 8-bit digital signal. The input voltage range for the AIM16 is 0 to 5.12 V, with input voltage converted to a count be-

tween 0 and 255. Resolution is 20 mV per count, with accuracy at $0.5\%\pm1$ bit. Conversion time is less than 100 microseconds per channel. The converter has a suggested retail price of \$179. Power supplies are available for \$14.95 and \$24.95, depending upon the required voltage. Contact Connecticut microComputer Inc, 34 Del Mar Dr, Brookfield CT 06804, [203] 775-4595.

Circle 464 on inquiry card.

Where Do New Products Items Come From?

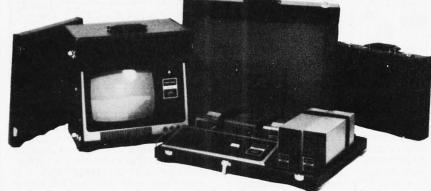
The information printed in the new products pages of BYTE is obtained from "new product" or "press release" copy sent by the promoters of new products. If in our judgment the information might be of interest to the personal computing experimenters and homebrewers who read BYTE, we print it in some form. We openly solicit releases and photos from manufacturers and suppliers to this marketplace. The information is printed more or less as a first-in first-out queue, subject to occasional priority modifications. While we would not knowingly print untrue or inaccurate data, or data from unreliable companies, our capacity to evaluate the products and companies appearing in the "What's New?" feature is necessarily limited. We therefore cannot be responsible for product quality or company performance.

MISCELLANEOUS



Computer **Carrying Cases**

A series of carrying cases for the Apple II and TRS-80 Model I computers have been introduced by Computer Case Company, 5650 Indian Mound Ct, Columbus OH 43213, (614) 868-9464. These cases can hold the computer, disk drives, and monitor in a fully operational configuration. There is no need to disconnect and reconnect cables each time the computer is moved. The lids have storage space for manuals, disks, papers, and other items. The computers and disk drives are held in position with security straps and cradled in foam rubber for protection. The cases are constructed of luggage material covered in vinyl with padded handles, protective pads, and steel skids. The APIOIS case holds the Apple with a single disk drive or a tape recorder; it sells for \$109. A larger



case, the AP102D, selling for \$119, holds the Apple and two disk drives. The AP103M holds the Apple, two drives, and a 9-inch monitor. The RS201 case will hold the TRS-80 keyboard, the expansion unit, and up to two disk drives. This case

also has a power strip. It sells for \$109. The RS202 case holds the monitor with additional space for a small printer, modem, or similar equipment.

Circle 492 on inquiry card.

Screen-Management **Transaction System**

The E-Code language provides screenmanagement capabilities to the VT-100 video terminal. Designed to support four VT-100s and an LA-120 under the RT-11 operating system, E-Code allows DEC (Digital Equipment Corporation) LSI-11 and PDP-11/03 applications to operate simultaneously in key-to-disk, data entry, data edit, and record-management functions. The features include a structured programming language, multiterminal support, virtual memory, and provisions for validating operator input in character or block mode. Multifile capabilities allow independent data-file manipulation from each attached terminal. The price is \$850 and the manual is \$15. Contact MCPC Systems, 2344 Nicollet Ave S, Suite 220, Minneapolis MN 55404, (612) 870-3841. Circle 493 on inquiry card.

Asynchronous-Synchronous Translator

The AST (asynchronous-synchronous translator) enables users to access large data bases and mainframes. The data base is accessed by communicating under the Bisync protocol. The single circuit board utilizes the 6809 microprocessor, controlling advanced data-link protocol, with the controlling firmware contained on EPROM (erasable programmable readonly memory). This card also enables the company and the user to apply the AST boards under other operating systems. Peripherals and microcomputers will be able to access large data-processing centers, usually as a remote-job-entry station. For more information, contact SDS Technical Devices Ltd, POB 1998, Winnipeg, Manitoba, Canada, R3C 3R3, (204) 589-7507.

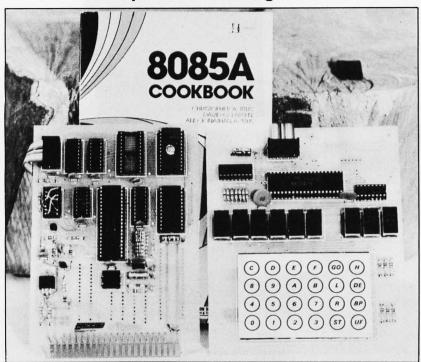
Circle 494 on inquiry card.

TI-990 Software

Synergistic Systems, Cobble Hill Rd, East Thetford VT 05043, (802) 785-4121, has several software packages for the Texas Instruments (TI) TI-990 computer written in TI BASIC. Mail-990 is a mailing-list program that maintains up to 10,000 addresses per disk. Text-990 is a text editor with screen-oriented text-preparation functions for documents of up to 400 lines. Index-990 is a set of multikey indexed-sequential-access routines that provide access to any record in a file by up to five keys, and sequential access in key or reverse-key order from any starting key. Forms-990 has functions and subprograms to simplify the development of forms-oriented input routines. The Seek-990 interactive data-base system for office personnel helps create and maintain data bases by means of menu specifications. Circle 495 on inquiry card.

MISCELLANEOUS

Microprocessor Training Course



The 8085AAT Microprocessor Training Unit includes a tested and assembled 8085A microcomputer with 1 K bytes of programmable memory, a 1 K-byte PROM (programmable read-only memory), a 1 K-byte EPROM (erasable programmable read-only memory), programmable I/O, keyboard, microprocessor card, display and operating system, a 44-pin edge connector that allows configuration to any bus structure, an area on the processor card for wire-wrap design or user-defined interface circuitry, and a 20 mA asynchronous port. The software comes with

an instruction manual, a user's manual with programs, a 352-page 8085A cookbook that includes basic microprocessor concepts and actual designs of an 8085A software-design book with over 190 executable program examples, an examination of all 244 instructions, plus an overview of assembly language for the 8080/8085A microprocessors. The Training Unit is \$299.95; a kit version is \$249.95. Contact Paccom, 14905 NE 40th St, Redmond WA 98052, [206] 883-9200.

Circle 486 on inquiry card.

Backplane I/O Connectors with Up to 72 Contacts

Mupac Corporation, 646 Summer St, Brockton MA 02402, (617) 588-6110, has announced a family of plug-style connectors with 26, 36, 40, 52, and 72 contacts. They can be mounted onto backplanes, printed-circuit boards, or wire-wrappable panels. They are available with straight or

right-angle pins and have either printed-circuit tails or wire-wrappable pins. Mating connectors that mass-terminate to flat cable are also available. The contact material is phosphor bronze with gold-over-nickel plating. Prices in quantities of one to nine range from \$3.43 each to \$8.37 each. Prices for mating connectors range from \$4.33 to \$8.54 each.

Circle 487 on inquiry card.

Asynchronous EPROM from RCA

A 256-word by 8-bit static CMOS (complementary metal-oxide semiconductor) EPROM, the CDP18U42CD, has been developed by RCA Solid State Division, Rt 202, Somerville NJ 08876, (201)

685-6423. The device is useful in generalpurpose asynchronous ROM (read-only memory) applications and will interface directly with the CDP1802 microprocessor. It has common data inputs and outputs. The 100-unit price is \$38.70.

Circle 488 on inquiry card.

Dual Integrated-Circuit Schottky Rectifiers

Intended for center-tap rectification, these 30 A Schottky rectifiers are available as full-wave bridges in medium-power switching supplies. The MBR 3020CT, 3035CT, 3045CT, and SD241 are single packages made up of two integrated circuits. These 20, 35, and 45 V units have an operating junction temperature of 150° C, with reverse voltages to 45 V. A built-in guard ring reduces junction stress and operates like a zener diode for transient protection. An extra layer of barrier metal acts as an interface between a working barrier metal of chrome or platinum and the nickel-gold ohmic contact metal, thus it virtually eliminates contamination and failure. Prices in 100- to 999-unit quantities range from \$5.70 to \$7. Contact Motorola Semiconductor Products Inc., POB 20912, Phoenix AZ 85036, (602) 244-4624.

Circle 489 on inquiry card.

Sixteen-Port Serial I/O Board

Konan's sixteen-port asynchronous serial I/O (input/output) board can communicate with peripherals on all S-100 bus systems, and also interconnects computers within networking systems. Omniport can talk to sixteen peripherals with RS-232 interfaces and has sixteen selectable data rates. It also features sixteen asynchronous channels with full handshaking capabilities. Omniport has a 4-character buffer on each channel, including the receive register. All operations, except the interrupt, are enabled with push-on jumpers. Omniport is compatible with all S-100 bus specifications proposed by the IEEE (Institute of Electrical and Electronics Engineers). The price for Omniport is \$800 in OEM (original equipment manufacturers) quantities of two. Konan Corporation is located at 1448 N 27th Ave, Phoenix AZ 85009, [602] 269-2649.

Circle 490 on inquiry card.

Adapt for DG

Data Financial Systems Inc has introduced the Adapt Software Package for use on all DG (Data General) minicomputers. The package includes modules for General Ledger, Accounts Receivable, Accounts Payable, and Payroll Applications. These may be custom tailored by nontechnical personnel with little knowledge of programming, utilizing the Adapt tool. Data Financial Systems Inc is located at 4350 E Camelback Rd, Phoenix AZ 85018, (602) 959-9240.

Circle 491 on inquiry card.

PERIPHERALS

It's Smooth Scrolling with Micro-Term

Micro-Term Inc. 1314 Hanley Industrial Ct, St Louis MO 63144, (314) 968-8151, is offering the ACT-5A and Mime-2A video terminals with a smooth-scroll feature. This feature allows the operator to read data as it passes over the screen in one continuous motion. This eliminates the jump scroll found in other terminals. Other features in the 5A-2A line include a bidirectional printer port and editing capabilities. In addition, the Mime-2A will emulate the DEC (Digital Equipment Corporation) VT-52, Hazeltine 1500, and Soroc IQ120. The ACT-5A and the Mime-2A cost \$995 and \$1045 respectively. Circle 496 on inquiry card.

Power Supply with 200 W Peak Capacity

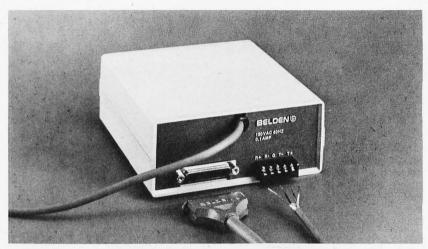
The Model AC-130 is a 130 W multioutput, switched-mode power supply with a 200 W peak output capability. The supply is compatible with the Boschert OL-130 unit, and has an input-voltage tolerance of 80 to 140 VAC and 160 to 264 VAC. The unit also has an adjustable power-fail signal. The outputs are +5 V \pm 3% at 15 A, \pm 12 V \pm 5% at 4 A, \pm 12 V \pm 5% at 2 A, and \pm 5 V \pm 5% at 1 A. A +24 V at 2 A output can be substituted for the $-5 \, \text{V}$ output. The single-unit price is \$340 from Conver Corporation, 10629 Bandley Dr, Cupertino CA 95014, (408) 255-0151. Circle 497 on inquiry card.

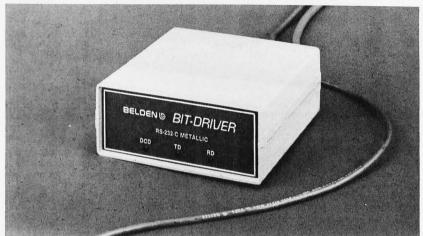
Dithertizer II

The Dithertizer II is a binary videodigitizer board for the Apple II. The board utilizes a video camera with external sync to load the video display of the Apple II. The device is designed as a frame grabber, DMA-type (direct memory address) digitizer that requires one frame, or onesixtieth of a second, to capture a binary image. Software is included to build dithered (pseudo gray scale via half tones) images from multiple binary images and to capture image-intensity contours using image subtraction. The software allows the user to select and change the matrix size and view the effects on the monitor. Users may also adjust the contrast and density of the image with joysticks and adjust matrix size. The Dithertizer II requires a video câmera with an external sync. The price for the unit is \$300. A package consisting of the Dithertizer II and a Sanyo video camera is \$650. Contact Computer Station, 12 Crossroads Plz, Granite City IL 62040, (618) 452-1860.

Circle 498 on inquiry card.

Belden Introduces a Short-Haul Modem





The Belden Model 9338 metallicconductor Bit-Driver short-haul modem has been developed as part of an RS-232compatible data-transmission system for in-house and in-plant applications. The 9338 provides asynchronous simplex and duplex data transmission, at speeds up to 56 K bps (bits per second). The metallicconductor unit is recommended for use in clean electrical environments. The operating range extends from 1500 to 4500 meters (5000 to 15,000 feet). An LED (light-emitting diode) array on the front panel indicates system status and aids in diagnosis. The price of the Model 9338 is \$195. Contact the Marketing Manager, Belden Corporation, 2000 S Batavia Ave, Geneva IL 60134, (312) 232-8900.

Circle 499 on inquiry card.

92 K-Bit Magnetic **Bubble-Memory Kits**

The TIBK090 and TIBK091 92 K-bit magnetic bubble-memory kits provide engineers with the bubble memory and integrated circuits required to lay out and assemble a 92 K-bit bubble-memory system. The 091 kit contains the parts required to construct one minimum memory system. The 090 kit contains all the parts required to construct one modular-memory unit (MMU). The MMU consists of all the parts in the 091 kit except the function-timing generator and controllers. The memory capability of the 091 kit can be expanded by assembling additional 090 kits and utilizing the timing generator and controller capabilities of the 091 kit. The TIBK090 kit costs \$151, and the TIBK091 kit is priced at \$191, both in quantities of one to twenty-four. Contact Texas Instruments Inc, Inquiry Answering Service, POB 225012, M/S 308, Attn: TIBK090, Dallas TX 75265.

Circle 500 on inquiry card.

PERIPHERALS

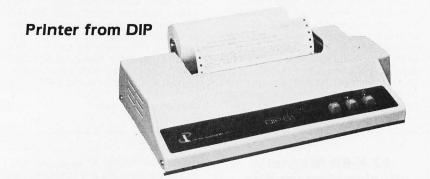
Word-Processing-Quality Video Terminal



The WP2000 word-processing-quality video terminal is available from Industrial Micro Systems Inc, 628 Eckhoff St, Orange CA 92668, (714) 978-6966. The unit features an EPROM (erasable programmable read-only memory) character generator, special function keys, an IBM Selectric keyboard layout, and a fifteen-key cursor-positioning and editing keypad on a removable keyboard. Also included is a tenkey numerical keypad. The high-resolu-

tion video monitor utilizes a 9 by 13 dot matrix. The 12-inch screen displays 25 lines. The WP2000 also features normal and reverse video; blinking, underlined and highlighted fields; uppercase and lowercase characters with descenders; 2-page memory; automatic self-test; pen interface; and printer port.

Circle 482 on inquiry card.



The DIP-81 dot-matrix impact printer features 7 by 7 or 14 by 7 matrix printing, plus uppercase and lowercase character sets. The bidirectional printing speed is 100 cps (characters per second), and the DIP-81 uses ordinary bond paper in sheets, roll, or fanfold form. The printer has the full 96-character ASCII (American Standard Code for Information Interchange) set, printing both 40 and 80 character and the printing both 40 and 80 character and

acters per line on standard-sized paper. Operator control includes power, select/deselect, line feed, top of form, and self-test. A Centronics-compatible parallel interface is standard, and a serial RS-232 or 20 mA current-loop interface is optional. The printer costs \$499. For more information, contact DIP Inc, 121 Beach St, Boston MA 02111, (617) 482-4214.

Circle 485 on inquiry card.

516-Megabyte Removable Disk Drive



Century Data Systems Inc, 1270 N Kraemer Blvd, Anaheim CA 92806, [714] 632-7500, has introduced the Trident T-600/602 disk drives, offering 516 megabytes storage capacity. The price per unit in lots of 100 is under \$12,000 and singleunit prices are around \$15,500. The T-600 is compatible with the Trident T-200 and T-300 drives. The capacity in the T-600 drive has been achieved by using narrower tracking heads that have increased output by 25% and resolution up to 5%. The servo surface has been rewritten to provide for 1349 cylinders. The unit's mean time between failures is specified at 4000 hours and calculated at 6000 hours, with a mean time to repair of less than one hour. Standard features include dualaccess operation and fixed or variable sec-

Circle 483 on inquiry card.

High-Quality Cassette Tapes

Marathon cassettes, made by Magnetic Information Systems, 415 Howe Ave, Shelton CT 06484, [203] 735-6477, have 50% more storage capacity than other digital cassettes on the market. Each Marathon cassette contains 450 feet of a 0.30-mil-thick polyester-film-base tape. Tape quality and case tolerances exceed ANSI/ECMA/ISO specifications. Each tape is certified in the cassette to be 100% error free.

Circle 484 on inquiry card.

SYSTEMS

Two Items for the Blind

Total Talk and Speak Easy are microprocessor-based products that convert computer-transmitted data into synthetic speech. Total Talk is a computer terminal that converts data into full-word synthetic speech. By translating data into phonetic characters and feeding that data into a synthesizer, the blind can have direct access to information stored on computers. Total Talk switches from full word to spelled speech output. The speech rate (45 to 720 words per minute), pitch, tone, and volume are adjustable. The unit is based on the Hewlett-Packard 2621A terminal. It is priced at \$5995.

Speak Easy is a subset of Total Talk. It does not have the editing and cursorcontrol capabilities of Total Talk. Applications include computer-aided instruction, instrument control, vocal feedback, and more. Speak Easy costs \$4000 with RS-232 interface and IEEE-488-bus interface capabilities. For details, contact Maryland Computer Services Inc, 502 Rock Spring Ave, Bel Air MD 21014, (301) 879-3366.

Circle 471 on inquiry card.



OSM System Allows 128 Terminals

OSM Computer Corporation, 2364 Walsh Ave, Santa Clara CA 95051, (408) 496-6910, has introduced a multi-user, multitasking microcomputer system called the OSM Model 6300. Each user has a microprocessor, memory, I/O (input/output) ports, and shares common disk storage of up to 128 megabytes, using

CP/M 2.2 and DPOS/2 operating systems. A service processor, consisting of a Z80A microprocessor, programmable memory and I/O, links the user processors to the disk drives and printer. User hardware consists of the Z80 processors, 64 K bytes of memory, I/O, and optional printers. The Model 6300 allows up to 128 user terminals with no console-response degradation, because each user has his own microprocessor. This can be helpful in

word-processing environments and other applications where console speed is critical. The Model 6300 comes with two 8-inch double-density floppy-disk drives. Several hard-disk options are available. The complete system is available with the IBM 3101 video terminal and Texas Instruments 820 RO or optional letter-quality printer. The single-user system is priced at \$5195.

Circle 472 on inquiry card.

Single-Board **Bubble-Memory System**

The RMS family of single-board bubblememory systems includes the controller, all electronics, and the bubble-memory devices. The four modules with 32 K-bytethru 256 K-byte-capacity systems interface with the Rockwell AIM-65 microcomputer. System 65 development system, and the Motorola EXORciser and Micromodule family. The average data rate for an accessed block is 22 K bytes per second. Depending upon block location, the access time ranges from 20 µs to 20 ms. The RMS includes checksum-error detection, redundancy control, and power-fail memory-protect circuitry. Prices range from \$1800 for a 32 K-byte system to \$5350 for a 256 K-byte system with a 1-megabyte bubble-memory device. For information, contact Bubble Memory Products, Electronic Devices Division, Rockwell International, POB 3669 RC55, Anaheim CA 92803, (714) 632-3729.

Circle 473 on inquiry card.

6802 Single-Board Computer with 2 K-Byte EPROM

The Model SBC-02 computer from Star-Kits, POB 209, Mt Kisco NY 10549, is a single-board computer that features a 6802 processor with 128 bytes of programmable memory, a 2 K-byte EPROM lerasable programmable read-only memory), and parallel or serial I/O. A wirewrap area is provided for custom interfac-

ing and expansion. The board costs \$25 with instructions, \$75 for a parallel I/O kit, or \$150 when wired and tested. An optional machine-level monitor can be installed to provide program entry and control, single-stepping, breakpoints, and other front-panel functions from a serial terminal. It is supplied separately in an EPROM for \$40 (included at no charge in the kit and wired verisons).

Circle 474 on inquiry card.

Single-Board 6809 Computer

The ADS 6809 S-100 single-board computer features provisions for 2 K bytes of programmable memory, 4 to 16 K bytes of EPROM, RS-232 serial communication with selectable data rates, parallel I/O ports, and simulated 8080-type I/O. ADSMON, a 2 K-byte monitor, allows

users to examine and change memory and registers, test memory, calculate relative offsets, load and punch tape files, and more. The ADS 6809 is sold as a printedcircuit board with a manual for \$69.95 from Ackerman Digital Systems, 110 N York Rd, Suite 208, Elmhurst IL 60126, (312) 530-8992.

Circle 475 on inquiry card.

SOFTW/ARE

Graftrax Graphics for the TX-80 Printer



Graftrax is a high-resolution bit-plot graphics capability for the Epson TX-80 dot-matrix printer. The bit-plot mode allows individual bit control of the print wires. Graftrax enables the printer to perform programmable universal form-handling functions. The length of a line feed is software definable in 255 steps of 0.007 inches each. The skip-over-perforation function allows the size of the print field to be adjusted from one line to a full page.

Graftrax counts the dots being printed in the high-density graphics mode so that Graftrax slows the printer down if a safe duty cycle is exceeded. Graftrax is built into a PROM (programmable read-only memory). For more information, contact Epson America Inc., 23844 Hawthorne Blvd, Torrance CA 90505, (213) 378-2220.

Circle 476 on inquiry card.

Apple II Cassette Pascal

Dynasoft Pascal is a p-code implementation of a Pascal subset intended for use with cassette-based microcomputer systems that cannot support full-scale systems such as UCSD Pascal. It includes the control structures of standard Pascal and supports integer, char, boolean, scalar, subrange, pointer, and array data types. A linkage to machine-language subroutines is also provided. The one-pass compiler produces a position-independent program

that is run with a 2 K-byte interpreter. The package, including the compiler, interpreter, and a line-oriented editor, requires 8 K bytes of memory space and will run on a 16 K-byte Apple II or Apple II Plus. Support is provided for low- and high-resolution graphics. This cassette system costs \$50. For more details, contact Dr Allan Jost, c/o Dynasoft Systems Ltd, POB 51, Windsor Junction, Nova Scotia, Canada, BON 2VO, (902) 861-2202. Circle 477 on inquiry card.

TRS-80 Disk **BASIC Compiler**

ACCEL2, a TRS-80 Disk BASIC compiler. is being marketed by Allen Gelder Software, POB 11721, Main Post Office, San Francisco CA 94101. The compiler produces compact machine-code translations of selected Disk BASIC statements and functions in integer, single- and doubleprecision, and string variable types. Subset compilation minimizes output code expansion with little loss of execution speed. Six

diagnostic messages and a set of local/ global compilation options increase compatibility with subject programs and control output code growth. The compile-time routines are self-relocating and occupy 5120 bytes; the run-time component takes 1 K bytes, making the compilation process available to 16 K-byte non-diskdrive machines. ACCEL2 comes on cassette tape with a manual for \$88.95.

Circle 478 on inquiry card.

TRS-80 Payroll System Uses TRSDOS 1.2

PR is a payroll system for the TRS-80 Model II. It requires TRSDOS 1.2, a 132column printer, a dual-disk drive, and 64 K bytes of memory. PR calculates the payroll for all employees as it maintains monthly, quarterly, and yearly totals for reporting purposes. It can produce paychecks, 941 forms, W-2 forms, paycheck registers, monthly summaries, general-ledger transaction registers, employee file lists, and more. Priced at \$129, PR comes with a manual, an installation guide, twelve programs, and sample data files on an 8-inch floppy disk. Contact Micro Architect Inc, 96 Dothan St, Arlington MA 02174, (617) 643-4713.

Circle 479 on inquiry card.

TRS-80 Text Editor

Textan is a text editor for the TRS-80 using Level II BASIC. It is a machine-language editor requiring at least 16 K bytes of memory. It is a video editor designed to read tapes written in Level II BASIC. Upon completion of the edit function, it returns to BASIC with the program loaded. Textan includes 32 command functions and 26 reserved-word keys. The command functions allow for top, bottom, and center of screen; end of and first of line; character, word, to end of line, and line delete; previous screen; automatic line numbering; line and character insert; and more. The reserved-word keys will automatically enter AND, GOSUB, CHR\$, DIM, ELSE, FOR, GOTO, and most of the other command words. Contact Southeastern Software, 512 Conway Ln, Birmingham AL 35210, (205) 956-2389.

Circle 480 on inquiry card.

Alpha Micro Computer FORTH

FORTH is available on the Alpha Micro Microsystems AM-100 computer. Based on the model by FIG (FORTH Interest Group), AM-FORTH runs under the AMOS operating system and includes FORTH, an interface to the AMOS file structure, and a FORTH text editor. AM-FORTH has facilities to permit processing data using AMOS sequential files. Memory is controlled so that the program uses only enough for the dictionary with the application routines and file I/O (input/output) buffers. An AMS or STD format disk is available with documentation and the FORTH program for \$40. Contact George Young, c/o Sierra Computer Company, 617 Mark NE, Albuquerque NM 87123, (505) 296-8085.

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SH	IUGART -	SIEMANS -	CDC 8"				
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+5V @ 1.7A	-5V @ 1.5A	+24V @ 2A	US-272	69.00			
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SOFTWARE

Timeclok

Timeclok/Billing is a time-management and billing program for businesses and professional offices chiefly concerned with projects or cases. It maintains client and overhead expenses by time and disbursement charges. Up to thirty employees can charge time and billing-rate units, such as per hour, per day, and miles traveled. One hundred work and overhead codes can be user-defined. The program can handle 100 clients on a single floppy disk. Timeclok generates reports on employee contribution, cash receipts, charges per client, charges per case or project, and billing statements. Reports can be assembled for a month or all months to date. Reports on individual clients, projects, and staff members can also be obtained. Client accounting balances are maintained for fourteen months. Timeclok requires the North Star disk operating system, 48 K bytes of memory, twin floppy-disk drives, a 24-line by 80-character video terminal, and a 132column printer. Contact Ladco Development Company Inc, POB 464, Olean NY 14760, (716) 372-0168.

Circle 465 on inquiry card.

Enhanced NEW/DOS/80 for the TRS-80 Model I

NEWDOS/80 is an enhancement of Apparat's NEWDOS 2.1 disk operating system for the TRS-80 Model I. NEWDOS/80 can mix or match disk drives and support track counts from 18 thru 80. It contains new editing commands and an improved RENUMBER command, plus it can route data to displays and printers simultaneously. Also included are Superzap/80, print spooling, and specifiable system options (SYSGEN). The price of NEWDOS/80 on a floppy disk with documentation is \$149 from Apparat Inc, 4401 S Tamarac Pky, Denver CO 80237, (303) 741-1778.

Circle 466 on inquiry card.

FORTH for the 6502

This version of FORTH is available for the 6502-based KIM-1, SYM-1, AIM-65, and Apple II microcomputers. This version of FORTH contains a built-in 6502 assembler, a text editor, and a cassette file-management system. Information on interfacing FORTH to a floppy disk is provided, as well as several extensions to the language. 6502 FORTH sells for \$90, which includes a manual, source listing, and the cassette containing the object code. Contact Eric C Rehnke, Tech Services, 1067 Jadestone Ln, Corona CA 91720, (714) 371-4548.

Circle 467 on inquiry card.

CP/M-86 Operating System from Digital Research



Digital Research, the originator of the CP/M operating system, has introduced CP/M-86 for Intel 8086/8088-based microcomputers. This is a single-user system. The file format of CP/M, release 2, has been retained. CP/M-86 can also function

as a slave node in a CP/NET network. For details, contact Digital Research, POB 579, 801 Lighthouse Ave, Pacific Grove CA 93950, [408] 649-3896.

Circle 468 on inquiry card.

Monty Plays Monopoly

The Ritam Corporation, Fairfield, lowa, has developed a "computer-opponent" program for the Apple II and the TRS-80 Model I Level II computers that plays Parker Brothers' popular board game, Monopoly. This program, called Monty Plays Monopoly, uses the standard Monopoly playing board and pieces, and plays the game according to the official rules. Monty is an entertaining opponent because he performs musical and graphics

diversions for you while waiting for his turn to play. When it is Monty's turn, he appears on the video screen and proceeds to wheel and deal as any other Monopoly player. The program is priced at \$29.95 for 16 K-byte cassette systems and \$34.95 for 32 K-byte floppy-disk systems. Monty Plays Monopoly is distributed by Personal Software, 1330 Bordeaux Dr. Sunnyvale CA 94086, [408] 745-7841.

Circle 469 on inquiry card.

FORTH for OSI Systems

This FORTH language, based on the FIG (FORTH Interest Group) model language, runs under OSI's (Ohio Scientific's) OS65D-3.2 operating system. High-level FORTH disk-operating-system words are implemented in FORTH for full compatibility with FIG-standard extensions. A line editor and a 6502 assembler are included. Also featured are a programmable-memory dump, video graphics, data-disk initializer, a sample machine-code routine, and a system disk optimizer. Minimal require-

ments are 24 K bytes of programmable memory and one disk drive. The 5-inch floppy-disk version works on C2-4P and C4 models. The 8-inch version works on C2-8P, C8P, C2-OEM, and C3 models. Superboard, C1P, and C2 versions will also be available. The program and manual are available from Consumer Computers, 8907 La Mesa Blvd, La Mesa CA 92041, (714) 698-8088, for an introductory price of \$69.95.

Circle 470 on inquiry card.



74LS122 74LS122 '74LS123 74LS126 74LS138 74LS151 74LS153

74LS155

74LS158 741 5160

74LS161 74LS162 74LS163

74LS164

74LS165 74LS165 74LS170 74LS174 74LS175 74LS190 74LS193

74LS 195 74LS195 74LS196 74LS221 74LS240 74LS241 74LS243

741 S244 74LS244 74LS245 74LS253 74LS257

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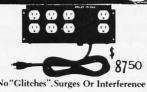
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.22 .24 .22 .28 .35 .49 .29 .29

.25 .23 .29 .24 .22 .57 .78 .78 .20 .20

SN74165N SN74174N SN74175N

SN74180N SN74181 N

SN7400N SN7402N SN7404N SN7408N SN7410N SN7412N

SN7412N SN7413N SN7414N SN7416N SN7417N

SN7423N SN7425N

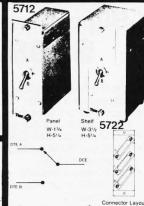
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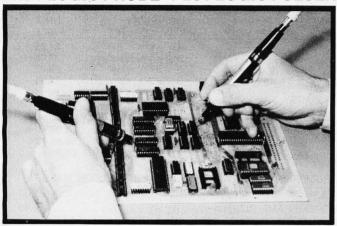
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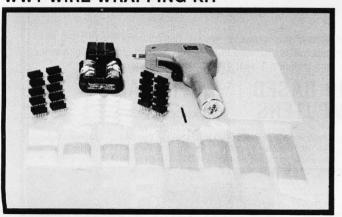
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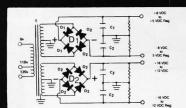
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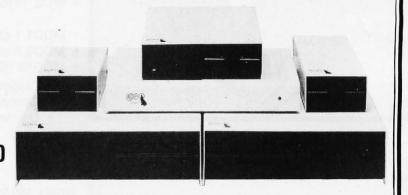
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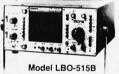
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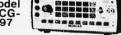


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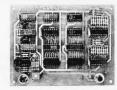
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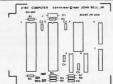
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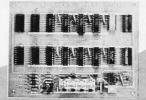
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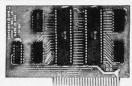
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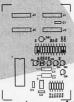
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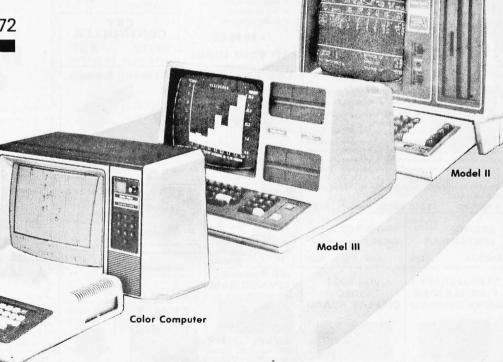
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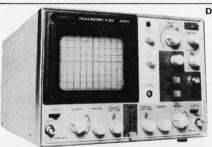
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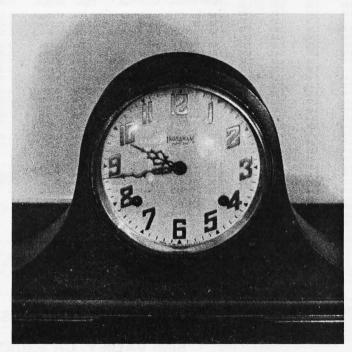
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4090	50/100 Imsai W/W	.250	4.30ea.	3.85ea.	3.45ea.	15110	6/12 S/T PET/NSC	.140	1.85	1.65	1.50	PAR	T NUMBER	DESCRIPTION.	1-9pcs.	10-24pcs.	25-99pcs.
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129865	50/100 Solder Eye	.140	6.80ea.	6.10ea.	5.45ea.	15175	6/- S/E Sgle Row		1.70	1.50	1.30	DE	98	Female	2.25ea.	2.00ea.	1.90ea.
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129990	50/100 S/T Cromem.	.250	4.75	4.25	3.80	15440	12/24 S/T PET	.140	2.65	2.40	2.15	DA	15S	Female	3.25ea.	3.10ea.	2.90ea.
						15445	12/24 S/T PET	.200	2.75	2.50	2.20	DA	51211-1	1. pc. Grey Hood	1.40ea.	1.20ea.	1.15ea.
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12305	22/44 S/E No Ears	.140	4.15	3.75	3.35	15510	15/30 S/T GRI Key	.140	2.40	2.15	2.95	DA	110963-2	2 pc. Grey Hood	1.60ea.	1.35ea.	1.30ea.
12759	36/72 S/T	.140	5.40	4.85	4.35	15515	15/30 W/W GRI Key	.200	2.60	2.35	2.10						
12790	40/80 W/W	.250	6.30	5.65	5.00	15600	18/36 S/E	.140	3.35	3.05	2.70	DB	25P	Male	2.80ea.	2.60ea.	2.40ea.
						15610	18/36 S/T	.140	3.00	2.70	2.40	DB	258	Female	3.60ea.	3.40ea.	3.20ea.
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10180	20/40 W/W TRS 80	.200	3.30	3.00	2.15	15875	25/50 S/E	.140	4.65	4.20	3.75	DC	37S	Female	6.00ea.	5.75ea.	5.50ea.
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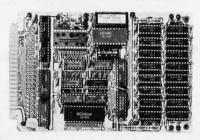
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RCA Cosmac 1802 Super Elf Computer \$106.95 plus load, reset, run, wait, input, memory pro-

Compare features before you decide to buy any other computer. There is no other computer on the market today that has all the desirable beneitts of the **Super Elf** for so little money. The Super Elf is a small single board computer that does many **big** things. It is an excellent computer for training and for learning programming with its machine language and yet it is easily expanded with additional memory, Full Basic, ASCII Keyboards, video character generation, etc.

Before you buy another small computer, see if it includes the following features: ROM monitor; State and Mode displays; Single step; Optional address displays; Power Supply; Audio Amplifier and Speaker; Fully socketed for all IC's; Real cost of in warranty repairs; Full documentation.

The Super Elf includes a ROM monitor for pro gram loading, editing and execution with SINGLE STEP for program debugging which is not in-cluded in others at the same price. With SINGLE STEP you gas not the STEP you can see the microprocessor chip opera-ting with the unique Quest address and data bus displays before, during and after executing in-structions. Also, CPU mode and instruction cycle are decoded and displayed on 8 LED indicators.

An RCA 1861 video graphics chip allows you to connect to your own TV with an inexpensive video modulator to do graphics and games. There is a speaker system included for writing your own music or using many music programs already written. The speaker amplifier may also be used to drive relays for control purposes.

A 24 key HEX keyboard includes 16 HEX keys

Super Expansion Board with Cassette Interface \$89.95 points can be used with the register save feature to isolate program bugs quickly, then follow with single step. If you have the **Super Expansion Board** and **Super Monitor** the monitor is up and running at the push of a button.

This is truly an astounding value! This board has been designed to allow you to decide how you want it optioned. The Super Expansion Board comes with 4K of low power RAM fully addressable anywhere in 64K with built-in memory protect and a cassette interface. Provisions have been made for all other options on the same board and it fits neatly into the hardwood cabinet alongside the Super Eff. The board includes slots for up to 6K of EPROM (2708, 2758, 2716 or TI 2716) and is fully socketed. EPROM can be used for the monitor and Tiny Basic or other purposes.

A IK Super ROM Monitor \$19.95 is available as an on board option in 2708 EPROM which has been preprogrammed with a program loader/ editor and error checking multi file cassette read/write software, (relocatable cassette file) another exclusive from Quest. It includes register save and readout, block move capability and video graphics driver with blinking cursor. Break

Other on board options include Parallel Input and Output Ports with full handshake. They allow easy connection of an ASCII keyboard to the input port. RS 232 and 20 ma Current Loop for teletype or other device are on board and if you need more memory there are two \$-100 slots for

tect, monitor select and single step. Large, on board displays provide output and optional high

and low address. There is a 44 pin standard connector slot for PC cards and a 50 pin connector slot for the Quest Super Expansion Board. Power supply and sockets for all IC's are in-

cluded in the price plus a detailed 127 pg. instruc-tion manual which now includes over 40 pgs. of software info. including a series of lessons to help get you started and a music program and

graphics target game. Many schools and universities are using the Super Elf as a course of study.

Hemember, other computers only offer Super Elf features at additional cost or not at all. Compare before you buy. Super Elf Kit \$106.95, High address option \$8.95, Low address option \$9.95, Custom Cabinet with drilled and labelled plexiglass front panel \$24.95. All metal Expansion Cabinet, painted and silk screened, with room for 5 S-100 boards and power supply \$57.00. NiCad Battery Memory Saver Kit \$6.95.

All kits and options also completely assembled

Questdata, a software publication for 1802 computer users is available by subscription for \$12.00 per 12 issues. Single issues \$1.50. Is-

Tiny Basic Cassette \$10.00, on ROM \$38.00.

original Elf kit board \$14.95. 1802 software; Moews Video Graphics \$3.50. Games and Music

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\$3.00, Chip 8 Interpreter \$5.50.

need more memory there are two 3-10u slots for static RAM or video boards. Also a 1K Super Monitor version 2 with video driver for full capa-bility display with Tiny Basic and a video interface board. Parallel 1/0 Ports \$9.85, RS 232 \$4.50, TTY 20 ma 1/F \$1.95, \$-100 \$4.50. A 50 pin connector set with ribbn cable is available at connector set with ribbon cable is available at \$15.25 for easy connection between the Super Elf and the Super Expansion Board.

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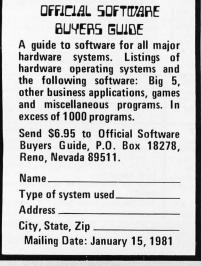
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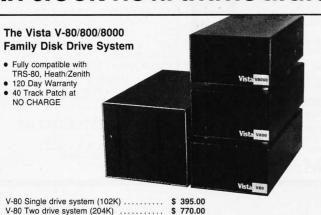
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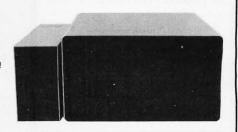
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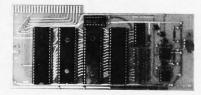
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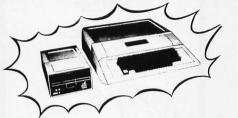
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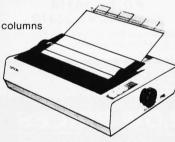
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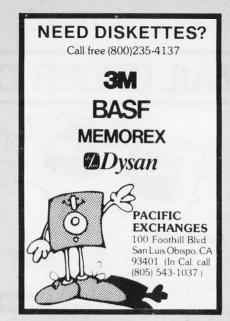
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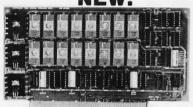
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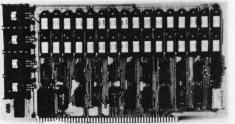
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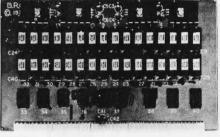
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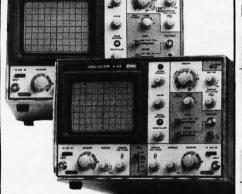
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Memory: none. GSCLP2

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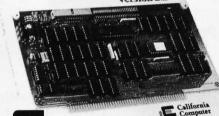
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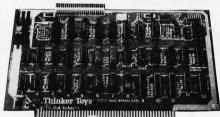
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 Plug compatible with Shugart, Remex and Siemens single- or double-sided drives

single- or double-sided drives
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3740/1 | | | | box
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Packaged in the same physical size as the industry standard 5 1/4" minifloppy disk drive. The micro-Winches-ter stores thirty times as much data (6.38 megabytes unformatted), accesses data twice as fast (170 milli-seconds) and transfers data twenty times faster (5.0 megabits per second.)

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\$300

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feet 50 conductor with edge card connector

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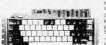
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ese used data terminats were originally designed for retail store order entry stems. The operator enters the inventory control number, merchandise of areas is telephoned, the handset is placed in the acoustic coupler an all the re-ded information is transmitted back the master computer. all the re-ded information of the same transmitted of the same transmitted to the same transmitted to

Regulated **Power Supply 5 VOLT 5 AMP**





DATA INPUT

The system is comprised of a premium cast aluminium and fiberglass enclosure, along with a Honeywell/ Microswitch hall effect keyboard. Thirty display lamps advise the operator of the systems status. Four inch load speaker akonshedges acceptance of data and alerts the operator of pending problems. But most of all hist "USED" terminal, with a little imagination, can be engineer to make the perfect home for an 8-100 computer and video display; or with slight modification will accept the Rockwell All-8-6 intervol computer, where the perfect home for an 8-100 computer and video display; or with slight modification will accept the Rockwell All-8-6 intervol computer.

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Full 136 Characters for the price of 80

DURABILITY...is the key component of the new Anacom 150. No bells no whistles, no problems, just consistent high quality output.

This nine wire dot matrix printer features a ballistic type print mechanism guaranteed for three million characters. Low count (16) integrated circuits add to the reliability of the printer.

Microprocessor controlled logic seeking bi-directional head allows the Anacom to print up to speeds of 150 characters per second. 136 columns wide.

Adjustable tractors and variable head gap permit the Anacom to accept lifteen inch wide multi-part forms.

Switch selectable: skip paper perforation, carriage return/line feed and six or eight lines per inch. Lexan paper shield and enclosure sound proofing add to the overall quality of the printer.

If you are in the market for a "Quality Engineered dot matrix printer, please consider the Anacom 150 before purchasing a less reliable machine.

Available either RS-232 serial 9600 baud. PRA-15\$S or Centronic parallel PRA-15\$P. Field exchange. UPS shipping weight 40 pounds.

3101

The new 3101 display terminal is the IBM entry into the plug compatable micro computer industry.

This modularly constructed CRT terminal has been engineered with the user in mind. The video display modules swivels and tilts to provide the operator with a comfortable viewing posture.

Standard 80 by 24 line screen format with a 25th line to display machine status and aid in the diagnostics in the event of a system malfunction.

87 key Selectric style keyboard arrangement along with numeric entry pad. Eight user definable function keys.

The 3101 video terminal is RS232 compatable and dis-plays all 128 ASCII characters including control codes.

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But most of all, built into every 3101 terminal is the quality that you have learned to expect from the IBM Corporation. VDT-3101



IBM direct price \$1295 CALIFORNIA DIGITAL discount price \$1195 immediate deliver

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from INTEGRAL DATA 460 Paper Tiger GRAPHICS \$1150

pondence quality printing.

the printer's nine-wire print head uses staggered needle rows to create the vercally overlapping dots. The head is driven bi-directionally under microproeasor control by a stepper motor driven mechanism.

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The word processing quality Spinwriter prints at speeds upto 55 char-acters per second. The Model 5519PS is supplied with both parallel and IRS-232 serial interfacials. Also included is the tractor feed mech-anism, along with print thirdle and ribbon. PIN-5319PS 70 lbs. Keyboard (KSH) Model 5520 PS available 29295. PIN-55229PS 75 lbs.



Finally a reasonably priced letter quality printer... Bi-directional printing at 23 characters per second. Full 136 print positions wide. Proportional spacing Uses standard Diablo brand interchangiable daisy print wheels. Intel 8085 CPU microprocessor controlled. Interfaces via Centronics parallel connector. Shipping 50 lbs. PRV-300.

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matrix print head. Parallel interfacing. Add \$65 for RS232. PRC-730P (S). PRC-737P (S) 17 lbs.

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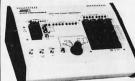
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| 1111111 | 1-24 | 25-49 | 50-100 | 11,1,1,1,1 | 1-24 | 25-49 | 50-100 |
| 8 pin LP | .17 | .16 | .15 | 14 pin ST | .27 | .25 | .24 |
| 14 pin LP | .20 | .19 | .18 | 16 pin ST | .30 | | |
| 16 pin LP | .22 | .21 | .20 | 18 pin ST | | .27 | .25 |
| 18 pin LP | .29 | .28 | .27 | | .35 | .32 | .30 |
| 20 pin LP | .34 | .32 | .30 | 24 pin ST | .49 | .45 | .42 |
| 22 pin LP | .37 | .36 | .35 | 28 pin ST | .99 | .90 | .81 |
| 24 pin LP | .38 | .37 | .36 | 36 pin ST | 1.39 | 1.26 | 1.15 |
| 28 pin LP | .45 | .44 | .43 | 40 pin ST | 1.59 | 1.45 | 1.30 |
| 36 pin LP | .60 | .59 | .58 | and the same of th | MIDE | UD AD CO | OVETC |
| 40 pin LP | .63 | .62 | .61 | | | VRAP SO
.D) LEVE | |
| connana de | SOLDE | RTAIL (| GOLD) | 11111 | 1-24 | 25-49 | 50-100 |
| N. Carrier | | TANDAR | | 8 pin WW | .59 | .54 | .49 |

| 40 pin LP | .63 | .62 | .61 | 1111111 | (GOL | D) LEVE | L #3 |
|-----------|------|--------|--------|-----------|------|---------|--------|
| 0000000 | | ERTAIL | | illilli | 1-24 | 25-49 | 50-100 |
| P | S | TANDAR | RD | 8 pin WW | .59 | .54 | .49 |
| TTTTT" | | 22.32 | 22322 | 10 pin WW | .69 | .63 | .58 |
| 1111111 | 1-24 | 25-49 | 50-100 | 14 pin WW | .79 | .73 | .67 |
| 8 pin SG | .39 | .35 | .31 | 16 pin WW | .85 | .77 | .70 |
| 4 pin SG | .49 | .45 | .41 | 18 pin WW | .99 | .90 | .81 |
| 6 pin SG | .54 | .49 | .44 | 20 pin WW | 1.19 | 1.08 | .99 |
| 8 pin SG | .59 | .53 | .48 | 22 pin WW | 1.49 | 1.35 | 1.23 |
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| 8 pin SG | 1.10 | 1.00 | .90 | 28 pin WW | 1.69 | 1.53 | 1.38 |
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| | | | | | | | |

| 74C48
74C73
74C74
74C85
74C86
74C89
74C90
74C93
74C95 | 1.95
.79
.79
1.95
.99
6.95
1.29
1.29
1.59 | 74C161
74C162
74C163
74C164
74C173
74C174
74C175
74C192
74C193 | 1.60
1.49
1.69
1.59
1.39
1.39
1.39
1.69 | 74C912
74C915
74C917
74C922
74C922
74C923
74C925
74C926
80C95
80C97 | 10.95
1.69
10.95
5.49
5.75
7.50
7.50
7.9 |
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4.50 | LINEA | AR | NE570N
LM702H | 4.95 |
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| TL074CN | 2.49 | LM341P-5 | .75 | LM723N | .69 |
| LH0082CD | | LM341P-12 | | LM733N | 1.00 |
| TL082CP | 1.19 | LM341P-15 | | LM739N | 1.19 |
| TL084CN | 2.19 | LM342P-5 | .69 | LM741CN | .35 |
| LH0094CD
LM300H | 36.80 | LM342P-12
LM342P-15 | .69 | MC17415C0 | 3.00
.79 |
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| LM302H | 1.95 | LM350K | 5.75 | LM1014N | 2.75 |
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| LM305H | .99 | LF353N | 1.00 | LM1458CN | |
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1.8K | 820 Ohm
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.34 .29
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.34 .29
.34 .29
.34 .29
.34 .29 | 1.5/35V
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3.3/25V
4.7/25V
6.8/25V
15/25V
22/6V | .41 .37
.51 .45
.53 .47
.63 .56
.79 .69
1.39 1.25
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.55 |
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Microprocessor With 64-Digit RAM
and Direct LED Drive
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& Direct LED Drive w/N Buss Int.
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AV-5200
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ROM'S

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Character Generator (Lower Case)
Character Generator
2048-Bit Read Only Memory

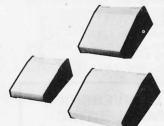
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Width | PRICE |
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|----------|------------------------------|
| DB25S | D-Subminiature Socket \$3.50 |
| DB51226 | Cover for DB25P/S \$1.75 |
| 22/44SE | P.C. Edge (22/44 Pin) \$2.95 |
| UG88/U | BNC Plug \$1.79 |
| UG89/U | BNC Jack \$3.79 |
| UG175/U | UHF Adapter \$.49 |
| SO239 | UHF Panel Recp \$1.29 |
| PL258 | UHF Adapter \$1.60 |
| PL259 | UHF Plug \$1.60 |
| UG260/U | BNC Plug \$1.79 |
| UG1094/U | BNC Bulkhead Recp \$1.29 |
| | |

TRS-80 16K Conversion Kit

Expand your 4K TRS-80 System to 16K.

★ 8 ea. MM5290 (UPD416/4116) 16K Dyn. Rams (*NS)

★ Documentation for Conversion

TRS-16K2 *150NS \$49.95 TRS-16K4 *250NS \$39.95

JE610 ASCII **Encoded Keyboard Kit**



The JE610 ASCII Keyboard Kit can be interfaced into most any computer system. The kit comes complete with the second seco

JE610/DTE-AK (as pictured above) ... \$124.95 JE610 Kit & Components (no case)....\$ 79.95

K62 62-Key Keyboard (Keyboard only) ...\$ 34.95

DTE-AK (case only - 34"Hx11"Wx834"D)\$ 49.95

JE600 Hexadecimal Encoder Kit

FULL 8-BIT LATCHED OUTPUT 19-KEY KEYBOARD



The JE600 Encoder Keyboard Kit provides two separate hexadecimal digits produced from sequential key entries to allow direct programming for 8-bit microprocessor or 8-bit memory circuits. Three additional keys are provided for user operations with one having a bistable output available. The outputs are latched and monitored with 9 LED readouts. Also included is a key entry strobe. Features: Full 8-bit latched output for microprocessor use. Three user-define keys with one being bistable use. Three user-define keys with one being bistable strong the second of the second o

JE600/DTE-HK (as pictured above) \$99.95 JE600 Kit 19-Key Hexadec. Keyboard, PC Board & Cmpnts. (no case) . . \$59.95 K19 19-Key Keyboard (Keyboard only) \$14.95 DTE-HK (case only -342"Hx84"Wx84"D) \$44.95

Circle 335 on inquiry card.

BYTE January 1981

Unclassified Ads

UNCLASSIFIED POLICY: Readers who are soliciting or giving advice, or who have equipment to buy, sell or swap should send in a clearly typed notice to that effect. To be considered for publication, an advertisement must be clearly noncommercial, typed double spaced on plain white paper, contain 75 words or less, and include complete name and address information.

These notices are free of charge and will be printed one time only on a space available basis. Notices can be accepted from individuals or bona fide computer users clubs only. We can engage in no correspondence on these and your confirmation of placement is appearance in an issue of BYTE.

Please note that it may take three or four months for an ad to appear in the magazine.

FOR SALE: Southwest Technical Products Corp PR-40—40 character/line on 4-inch paper tape, 64 character ASCII; \$200. Heathkit oscilloscope IO-4560—5 MHz triggered sweep; \$75. Holden Caine, 1 Windsor PI, Melville NY 11746, (516) 692-9512.

FOR SALE: Programmer's toolkit for old read-only memory PET. Perfect condition, with manual. Plugs onto right of PET. Commands: auto-automatic line numbering. Renumber-renumbers program. Find-lists occurrences of command or text in program. Help-after error, list line; error is in reverse field. Append-merge saved programs. Other commands: trace, step, delete, dump. Costs about \$100. Make me an offer. David Magill, 2001 Carling Ave, Apt 1709, Ottawa Ontario, K2A 3W5 Canada, (613) 722-3566.

FOR SALE: TI-59 programmable calculator with PC-100C printer cradle; both in excellent condition. Includes extra paper for printer, all manuals, and my own library of programs. \$380 or best offer; I pay shipping. Mike Smith, 908 Murray Hill Rd, Binghamton NY 13903.

FOR SALE IN CANADA: 8 K and 16 K static-memory board for H-8 computer, both assembled and tested. Give me your offer. P Liem, 15 Ardell Grv, Nepean Ontario, K2G 4G5 Canada.

FOR SALE: Partially completed Heath Company digital computer with related tools. Additional information provided on request. Sale necessary to settle estate. Robert L Kennedy, Attorney at Law, POB 222, Colfax LA 71417.

FOR SALE: Xitan/TDL microcomputer disk system, software, and extras. 64 K memory, Xitan disk BASIC, INFO 2000 dual 8-inch floppy-disk drive (made by PerSci), CP/M operating system, twelve floppy disks, sorting and telecommunications software packages, spare 32 K memory board, equipment covers, and all documentation; \$1500. V Roningen, 4707 9th St S, Arlington VA 22204, (703) 521-1451.

FOR SALE: Like-new 16 K Heathkit H-89 computer with H-77 floppy-disk drive, H-885 cassette interface, and HDOS. Used three months. \$1800. Charles Leet, Jr, Box 517, Jetmore KS 67854, (316) 357-6531. FOR SALE: 4116-type dynamic programmable memories from several manufacturers. Each 16 K circuit has been heat-tested and is guaranteed good. Speed is unknown. Price is \$4 each postpaid. Steve Marley, 425 N Hickory #305, Escondido CA 92025, (714) 489-8293.

FOR TRADE: Two LSI-11 processor boards, two 4 K memory boards, and a serial-line unit. Will trade for H780H power supply, front panel for PDP-11/03/LSI-11, and H909C enclosure box. Robert McCown, 180 Farm St, Millis MA 02054.

FOR SALE: Heath ETA-3400 trainer accessory with full 4 K programmable memory. \$175 or best offer. David Haas, 9 Marget Ann Ln, Suffern NY 10901, (914) 357-3447.

FOR SALE: I/O: OSI 8-slot chassis with fully populated 500 processor (read-only memory BASIC) 540 video with graphics; 430 with parallel, serial, dual D/A; fully socketed 24 K programmable-memory board with 2 K installed; video monitor; GRI keyboard; cassette recorder; graphics and music demonstration software; \$800. Stephen P Smith, 106 E Clearview Ave, State College PA 16801.

FOR SALE: Apple serial I/O interface by Electronic Systems. Have three brand-new boards. Will sell with software and RS-232 connector; \$52. I pay shipping. Dan Pote, 3105 Falkland, Carrollton TX 75007, (214) 492-2027 after 6:30 PM or on weekends.

FOR SALE: TRS-80, Model 1, Level 2, 16 K with expansion interface (RS-232C installed) plus Editor, Assembler, Micro Music, and blackjack programs. Excellent condition; \$800 firm. RS-232C original carton; \$55. Jerry Coyle, 11 Town Way, Hull MA 02045, (617) 925-1282.

MICROSTAT NOW AVAILABLE FOR CP/M*

MICROSTAT, the most powerful statistics package available for microcomputers, is completely file-oriented with a powerful Data Management Subsystem (DMS) that allows you to edit, delete, augment, sort, rank-order, lag and transform (11 transformations, including linear, exponential and log) existing data into new data. After a file is created with DMS, Microstat provides statistical analysis in the following general areas: Descriptive Statistics (mean, sample, and population S.D., variance, etc.), Frequency Distributions (grouped or individual), Hypothesis Testing (mean or proportion), Correlation and Regression Analysis (with support statistics), Non-parametric Tests (Kolmogorov-Smirnov, Wilcoxon, etc.), Probability Distributions (8 of them), Crosstabs and Chi-square, ANOVA (one and two way), Factorials, Combinations and Permutations, plus other unique and useful features.

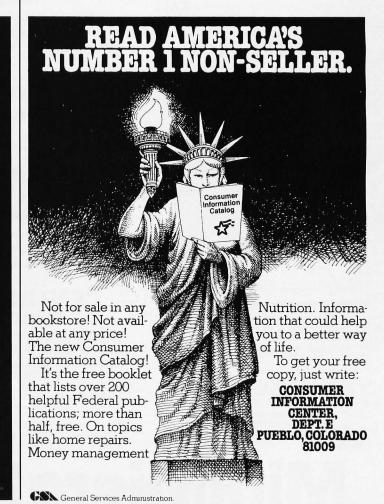
MICROSTAT requires 48K, Microsoft Basic-80 with CP/M and is sent on a single-density 8" Disk. It is also available on 5" diskettes for North Star DOS and Basic (32K and two drives recommended), specify which when ordering. The price for Microstat is \$250.00. The user's manual is \$15.00 and includes sample data and printouts. We have other business and educational software, call or write:



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FOR SALE: Changing systems, have LA 36 DECwriter. Two years old; in excellent condition. Will take best offer. Robert A Leverone, 279 Cambridge St, Burlington

FOR SALE: IMSAI cabinet and power supply for two minidiskette (5.25-inch) drives. Sturdy, with ample power. Have purchased a North Star Horizon. \$800 or best offer. Ronald Subler, 25 First Parish Rd, Scituate MA 02066, (617) 545-6578.

FOR SALE: Heathkit ET-3400 assembled microprocessor trainer in excellent working condition. All additional components and manuals in good order. Asking \$205 or \$155 for trainer only. Also, new Base 2 Model MST impact printer with graphics for \$650. Apple parallel inter-face available. Will ship via UPS. Jeff Sumey, 5 Nell St, Hopwood PA 15445, (412) 437-3021 evenings.

FOR SALE: Comprint 912 printer with TRS-80 interface. Works great. Price \$425. Steven Wexler, 1634 Buck Hill Dr, Huntingdon Valley PA 19006, (205) 947-8236.

WANTED: SwTPC 6800 processor with MPA2 board. Depending on condition, will pay 70% of new cost. Bob 7601 Wordham Dr, Austin TX 78749, (512) 441-9700 days.

WANTED: TRS-80 Level II programs to swap. Utilities, languages, games, and business. Send list, name, and address. Dennis Leong, 5910 N Washtenaw, Chicago IL

FOR SALE: I have upgraded my TRS-80 to 16 K and want to sell my old 4 K chip set. Used only twenty hours and is in prime condition. Will take best offer. (MCM 6604AC) Also, would like to swap Level II programs to increase library. Paper listing only. Will take cassette or listing. Mark Cruse, 3609 Stanolind, Midland TX 79703, (915) 694-4868.

FOR SALE: Ohio Scientific CIP with the 610 expander board installed. Features Microsoft 8 K BASIC in readonly memory, 16 K user memory easily expandable to 32 K by plugging in more memory, and minifloppy controller read-only memory with double-sided option. Original carton, documentation, and many programs in-cluded. Sell all for \$600 plus \$10 shipping. Charles F Allen, 9 Annabelle St, Carnegie PA 15106, (412) 276-8265.

FOR SALE: 12 V Reed relays; \$0.50 each. All brand new, same as Electrol R4248-2. Also, a Power-One D24-4.8, 24 V power supply. M6800 software to trade. Send SASE with any offers. T Preston, 9274 Marinus Dr. Fenton MI

FOR SALE: IMSAI 8080, Tarbell floppy interface for two PerSci Model 70, IMSAI MIO, Processor Tech VDM-1, 8 K and 16 K programmable memory boards, Cromem-co TUART, and more. All operational. Make offer or send SASE for detailed list. Tom Tai, POB 142, Eagleville PA 19408.

FOR SALE: Axiom 801 printer with six rolls of paper. Cable for hookup to PET or CBM computer, \$280. Also, CAT modem and SOURCE program on disk and cassette for CBM. \$150. Both for \$400. Kurt Hesselden, 2201 E 11th, Farmington NM 87401, (505) 327-7682.

FOR SALE: Two 8085 microprocessor chips, fifteen 8155 static programmable memory chips, three 2716 eprom, and one 8212 I/O port. New/never used. Will not split up set. Cost \$500. Will sell for \$450 or best offer. Ted Poe, 28C Coolbrook Ct. East Amherst NY 14051.

FOR SALE: Shugart SA800; \$375. SA900; \$225. Pertec 8-inch floppy drive and 8-inch standard media; \$225. Memorex 630 plug-compatible to IBM 2311 but has selfcontained power supplies and uses modern voice coil-type positioning, 7.5 megabytes single density, and removable media; \$500. Frank Bennett, 1242 Cottonwood St, Broomfield CO 80020, (303) 466-2621.

FOR SALE: 32 K ARTIC static programmable memory board for the S-100 bus, fully populated with 250 ns 4044 chips. Used, but in excellent working condition; \$400. Cromemco BYTESAVER programmable read-only memory board. Programs 2704/2708s and has space for up to 8 K of programmable read-only memory. Used less than two hours and in excellent condition; \$100. A E Caudel, 8003 Benaroya Ln, Apt C3, Huntsville AL 35802, (205) 883-7425 evenings.

FOR SALE: Integral Data Systems IDS-125 printer with printer control option. Needs mechanical adjustment Over \$800 new. Asking \$400 or reasonable offer. Bill Krantz, 108 Hawthorne Dr, North Wales PA 19454, (215) 368-3697 evenings.

FOR SALE: Magnetic-tape Selectric typewriter (dualtape model); \$1200. Tapes; \$4. Also, 80-track minifloppy with hardware switch to 35/40 track for TRS-80 Model I; \$650. Arnold Vagts, 3713 S Parton St, Santa Ana CA 92707, (714) 549-7021.

WANTED: Texas Instruments SR-52 programmable calculator. Must be in excellent condition. Paul H White, 1539 Malcolm Ave, Los Angeles CA 90024, (213) 650-4001

FOR SALE: Memorex 651 floppy-disk drive (new), thirty blank disks, controller board (old); \$300, EVK 200 6800 development kit: 1 K programmable memory, 1/2 K erasable-programmable read-only memory, and read-only memory monitor; \$170. Gordon Wilson, 819 San Lucas Ave, Mtn View CA 94043.

FOR SALE: Hazeltine 1520 intelligent terminal. Features 110 thru 19.2 K bps, cursor movement keys and numeric keypad, antiglare screen, full uppercase and lowercase ASCII character set, 80 by 24 dual-intensity readout, switch-selectable reverse video, and local screen editing. Almost new; with owner's manual. Asking \$1150, will ship immediately UPS (insured) on receipt of certified check. Jeffrey J Nonken, 8 E Washington St, West Chester PA 19380, (215) 431-3513.

FOR SALE: Lear-Siegler ADM3A+ terminal, latest model, one-month old. \$800. P Gleeson, 3470 19th St, San Francisco CA 94110, (415) 864-1967.

FOR SALE: Rockwell AIM-65, 1 K programmable memory with assembler read-only memory. Used only two hours. In factory box with all manuals. \$350. Switching power supply, +5 V at 10 A, +24 V at 3 A, ±12 V at 1 A. \$150. Bruce Warren, Box 784, Freeport TX 77541, (713) 233-3700 home, (713) 238-2547 office

FOR SALE: 18-month-old Radio Shack TRS-80 Model I Level 2 computer in excellent condition; 32 K memory; expansion interface; 150 LPM Quick Printer; five rolls of paper; Data Dubber; light pen; two 6-plug isolator boxes; all manuals and a library of over fifty cassette programs. Original value over \$2200; asking \$1200. Michael Clark, 5967 Sullivan Trl, Nazareth PA 18064, (215) 759-6873.

FOR SALE: TI-25 and TI-30 calculators. Both in excellent condition. Will include documentation, batteries (TI-25 only), and the book Great International Math on Keys (TI-30 only) on request. Will sell any of the above separately. \$40 for all of the above; best offer for separate units. May swap for TI-59 or TRS-80 (Level II) software. Joe Sewell, 6776 Sheridan Rd, Melbourne Village, Melbourne FL 32901.

FOR SALE: Ticker-tape Teletype. Full alphanumeric, five-level code. Excellent condition. Both transmit and receive work fine. 60 mA loop. \$145. Chuck Gee, 1890 SW 3rd, Corvallis OR 97330, (503) 754-9422.

WANTED: Correspondents or exchange of Biotech Electronics (defunct) CGS-808B graphics software. Owners of firmware pack 2. Have firmware pack 1 source on CPM. Share with present group of four. Larry Snyder, S78 W17675 Canfield Dr, Muskego WI 53150, (414) 679-9706.

FOR SALE: ELF II with 4K programmable memory, Giant board, ASCII keyboard, and documentation. Asking \$400 or best offer. Kim Dixon, Box 33, Kenville Manitoba, ROL 0Z0 Canada, (204) 734-2411.

FOR SALE: Cromemco Z-2, 4FDC disk controller, plus a 32 K Dynabyte memory card. All are in perfect working condition. Runs at 4 MHz. Documentation is included, but no software. Asking \$1600. Bill Dyche, 2812 Windemere Dr, Donelson TN 37214.

October Winners: Sorting and Ciarcia

"Sorting with Binary Trees," by Bill Walker won first place in the BOMB for the October 1980 issue of BYTE, and Steve Ciarcia's "Make Liquid-Crystal Displays Work for You" came in second. Dr Walker's article, which is 2.1 standard scores above the mean, will net him an award of \$100, while Steve Ciarcia's article, 0.85 standard scores above the mean, wins a \$50 prize.

Other popular articles in this issue include "The 6502 Gets Microprogrammable Instructions,' Dennette Harrod, "Symbolic Math using BASIC," by David Stoutemyer, and "Machine Problem Solving, Part 2," by Peter Frey.

BOMB

BYTE's Ongoing Monitor Box

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The Brains of Men and Machines

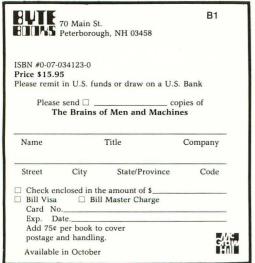
by Ernest W. Kent

When the "Brains of Men and Machines" series of articles originally appeared in BYTE magazine, the response was immediate and enthusiastic. Now Ernest W. Kent has expanded his ideas about the brain into a full-length book.

As researchers begin to unravel the mysteries of the brain's chemical, electrical, and synaptic circuitry, their findings are becoming immediately applicable to advances in robotic behavior and computer design. The Brains of Men and Machines ''dissects'' the brain to provide new insights into computer design and artificial intelligence.

It is one of the rare books that transcends disciplinary boundaries. In it the ever increasing relationship between man and machine is freshly examined—a relationship, Professor Kent concludes, that is today being reexamined in the light of man's own neurological self-image.

Hardcover 304 pages





Ohio Scientific: The leader in Winchester based microcomputers.

Ohio Scientific produced the first large capacity fixed media hard disk (Winchester) based microcomputers in 1977. Since then we have shipped more of these systems than the rest of the microcomputer industry combined.

Family Features

All standard C3 features including:

- 3-processor CPU with 6502, Z80 and 6800
- .7 MIPS 6502A
- 48K static RAM
- Dual 8" floppies
- Free standing rack for direct expansion capabilities
- 17-slot OSI 48 line BUS architecture for large system
- Directly accepts up to 8 users with currently available memory boards, more with higher density boards in the
- Directly expandable for use as Network data bases
- Slide-mounted subassemblies, removable side panels and locking rear door for easy expansions and service.

The floppy only rack based C3 for users who anticipate expansion to hard disk, multi-user and/or networking in the future Under \$7000.

The world's most powerful microcomputer (when GT equipped). Features the highly advanced and extensively field proven OKIDATA 3306 Winchester disk.

- System boots from floppies or hard disk on power up
- 74 megabytes end user workspace under OS-65U, 80 megabytes unformatted
- Ultra-high performance disk 74 millisec worst case access

38 millisec average

- 10 millisec access on cylinder (215K user workspace) 8 megabits per second transfer rate
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Multiple operating systems on disk Ultra-high speed 'FIND' command for high speed string searches (Associative Access)

Upward compatible with multi-user and network systems with full file, peripheral and communications arbitration between users



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A medium performance Winchester disk based system which provides the ideal cost/performance ratio in typical small business applications. The C3-C uses the Shugart SA4008 29 megabyte Winchester disk.

Performance specifications, hardware configuration and software is identical to the C3-B with the following exceptions:

- 23 megabytes of end user workspace under OS-65U
- 29 megabytes unformatted capacity
- Medium performance Winchester 240 millisec worst case access

87 millisec average access

- 10 millisec access on cylinder (110K user workspace)
- Simple on/off disk operation

Under \$11,000

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